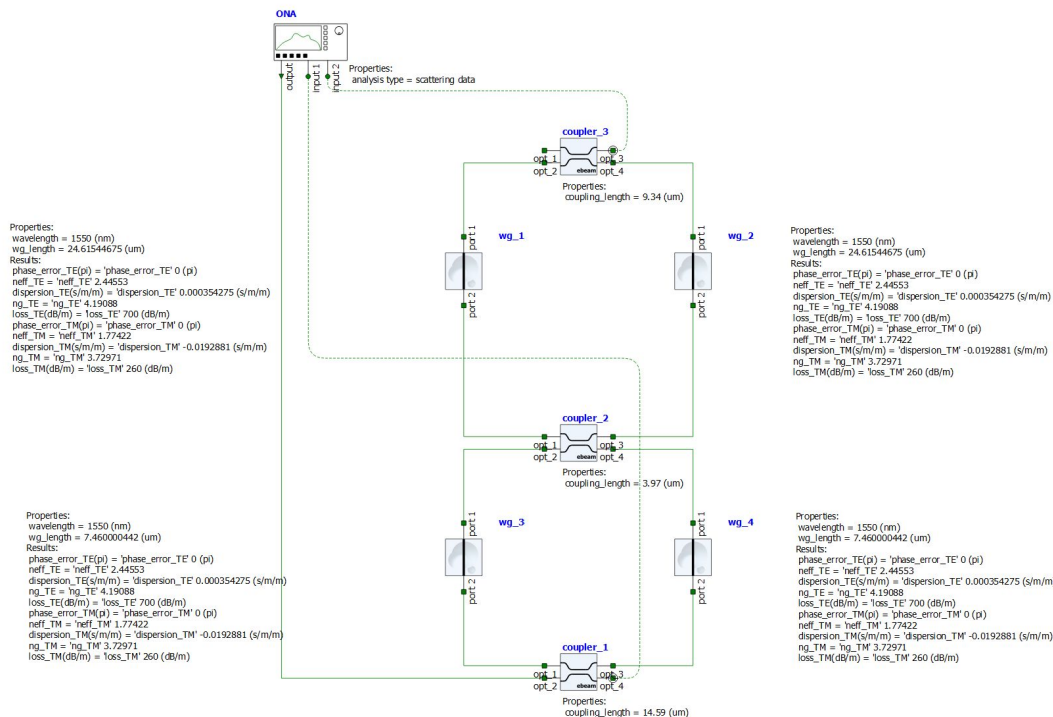




Ressonador em Anel

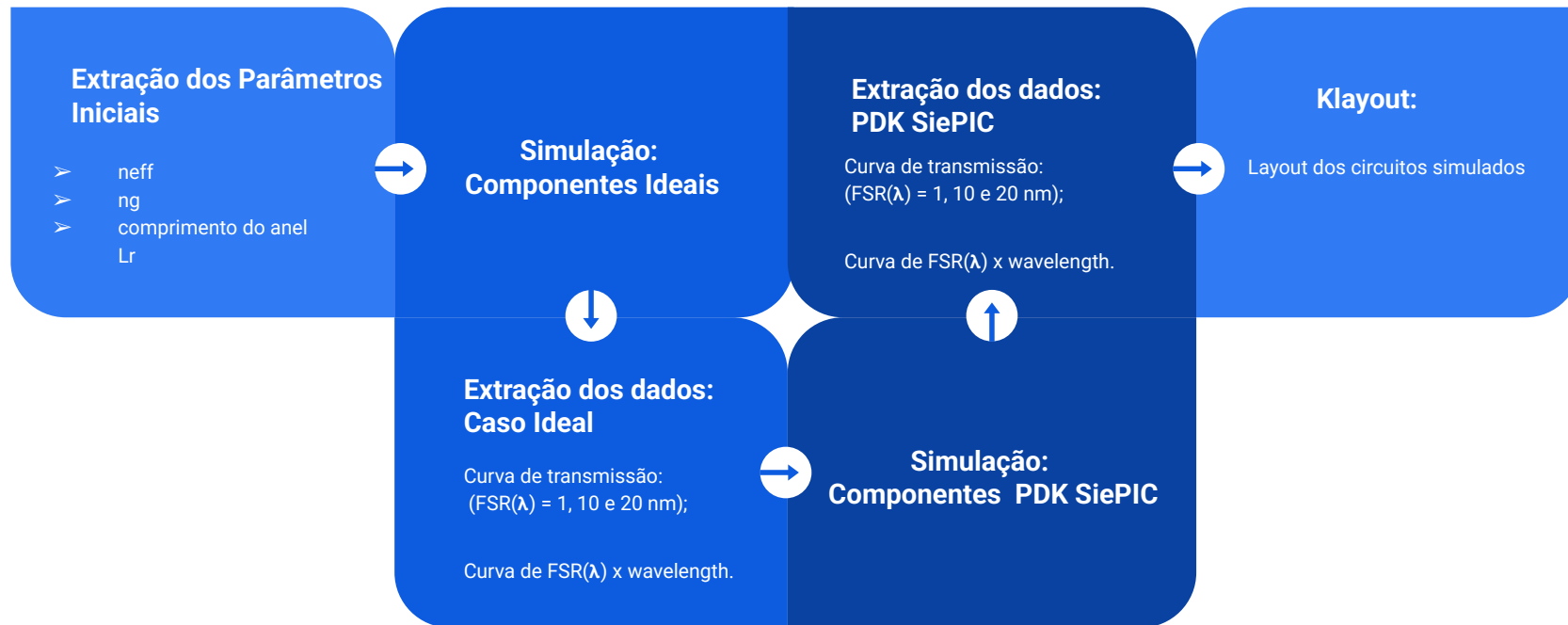
Aluno: Luiz Felipe Barros Alves

Esquemático Utilizado para Simulação no Interconnect



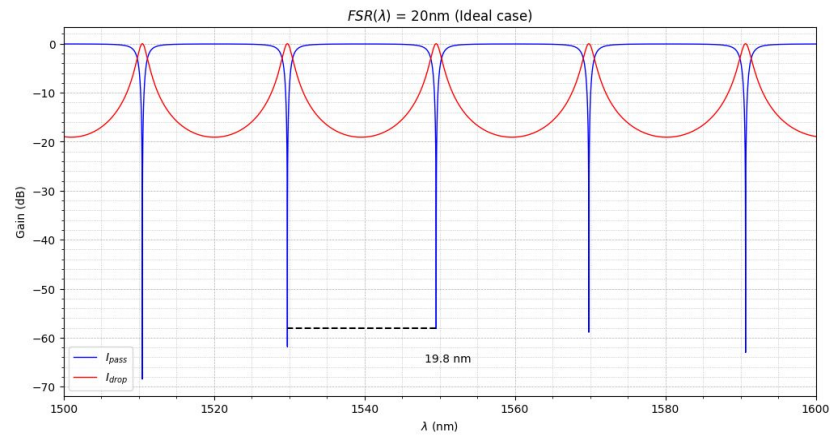
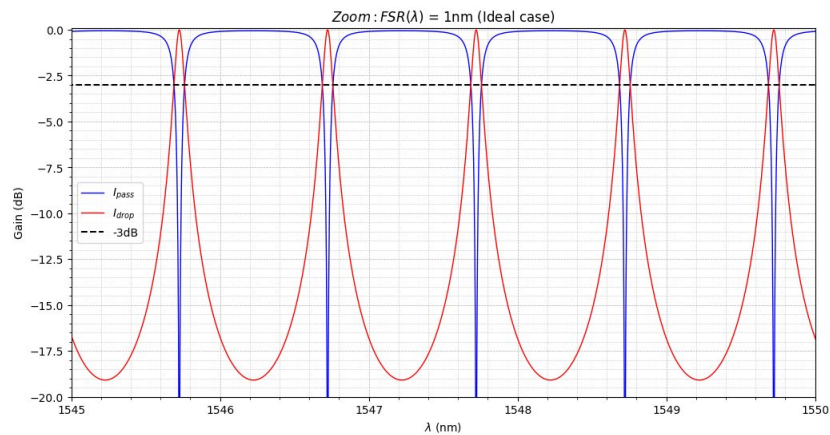
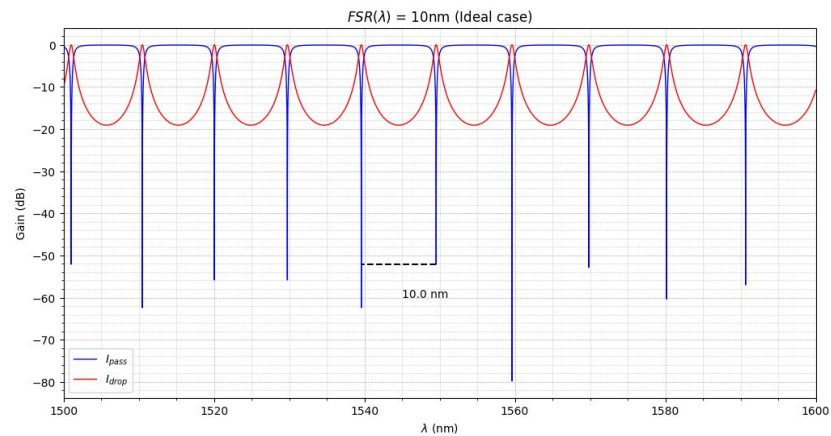
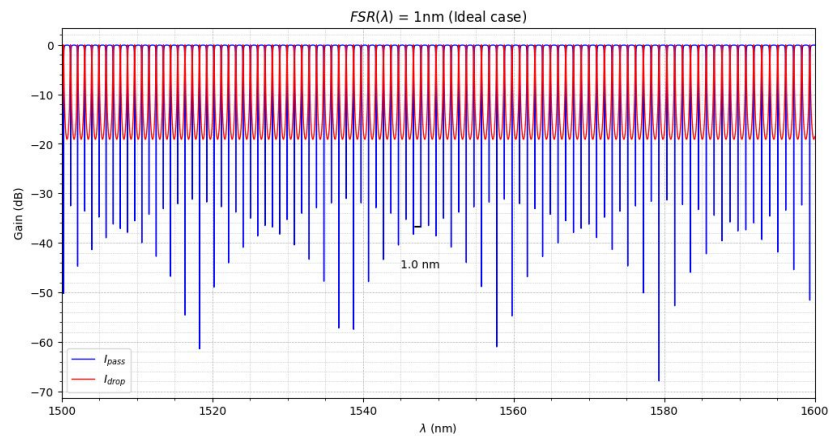


Fluxo de Atividades Desenvolvidas



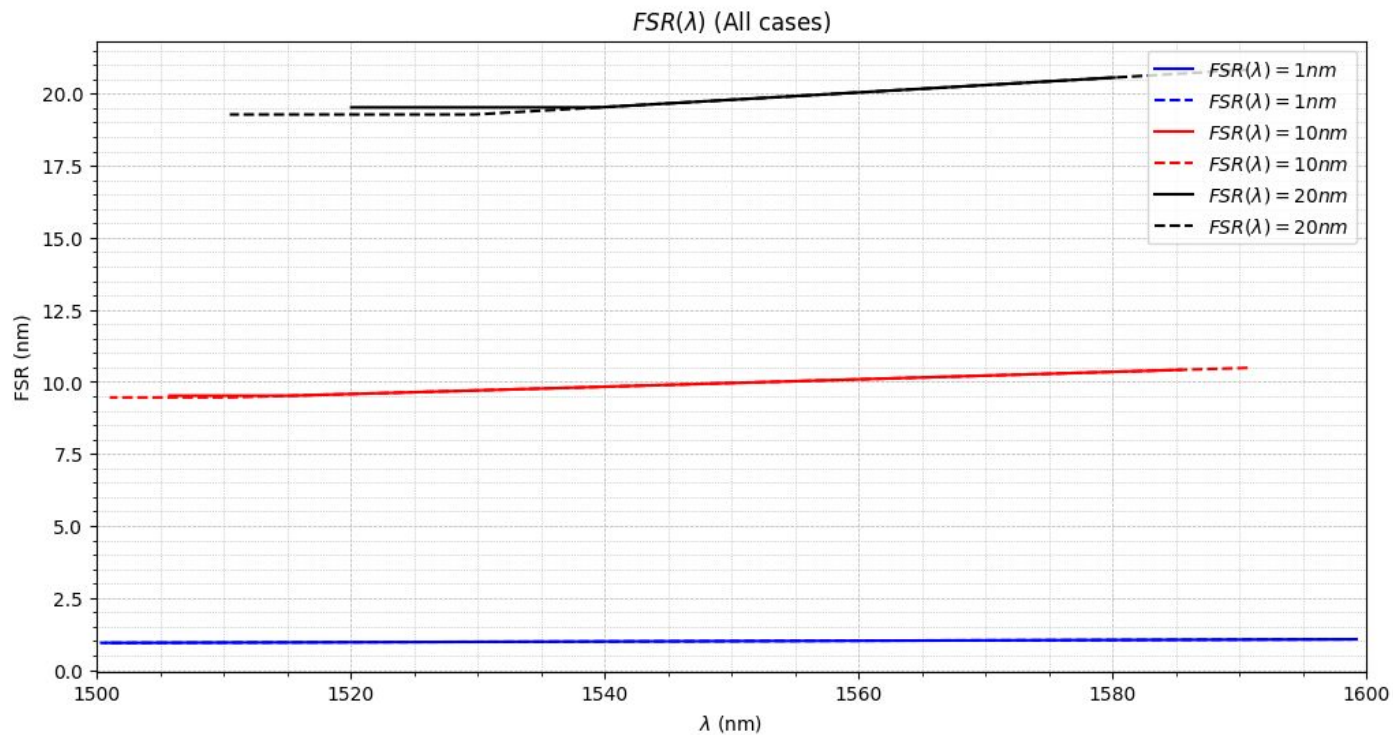
Resultados

- Caso ideal:



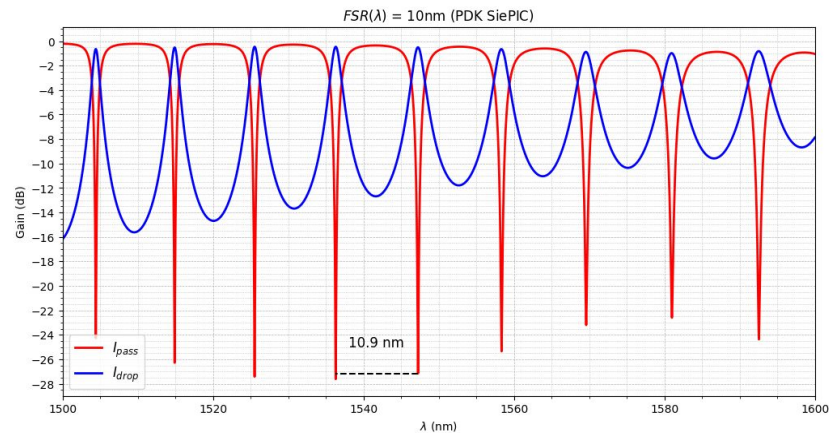
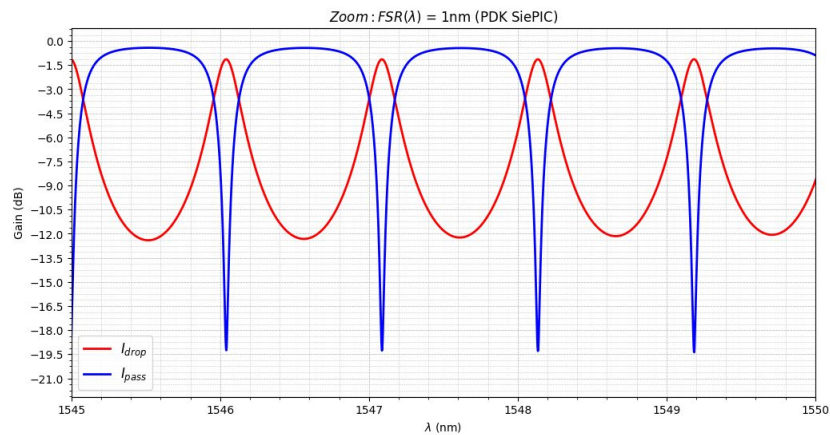
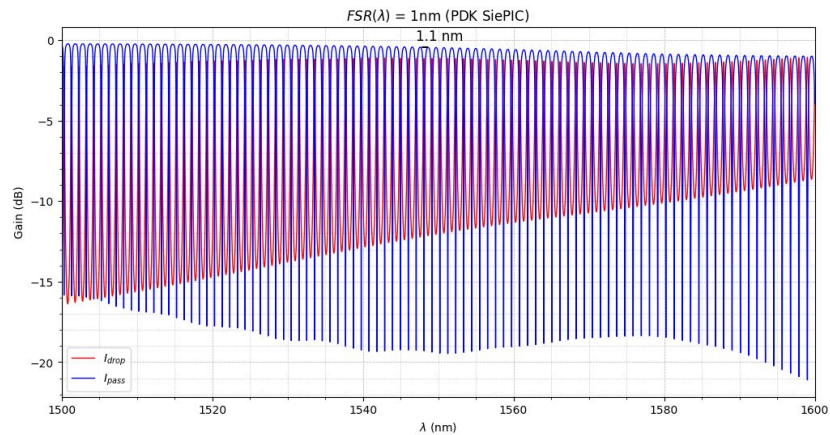
Resultados

- Caso ideal:



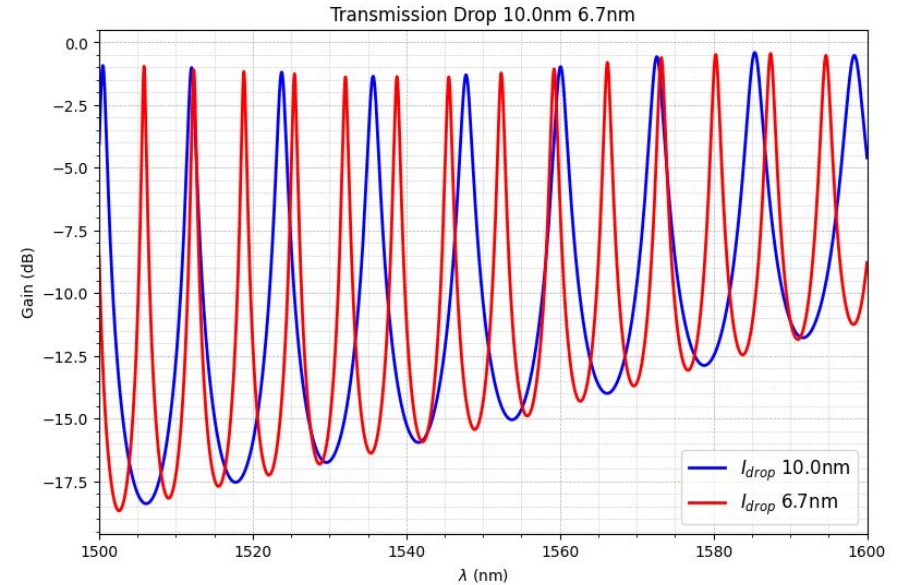
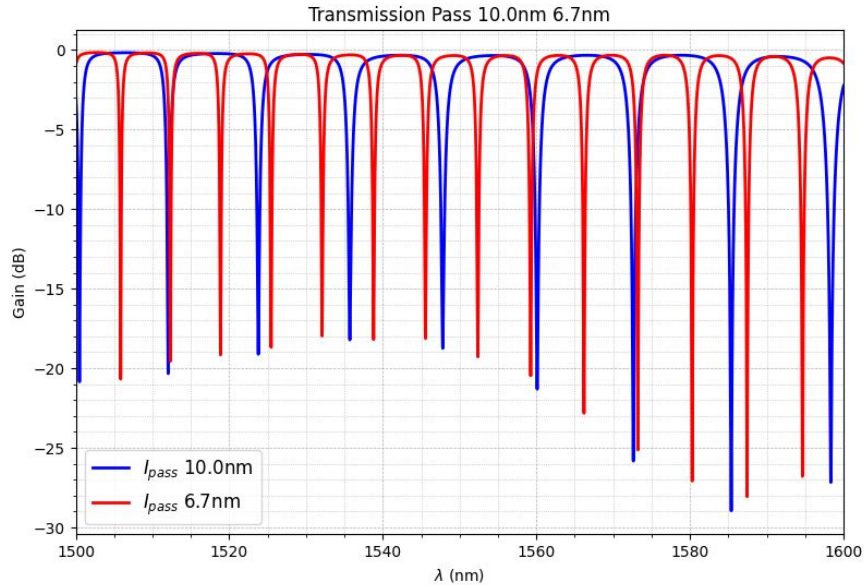
Resultados

- PDK SiePIC:



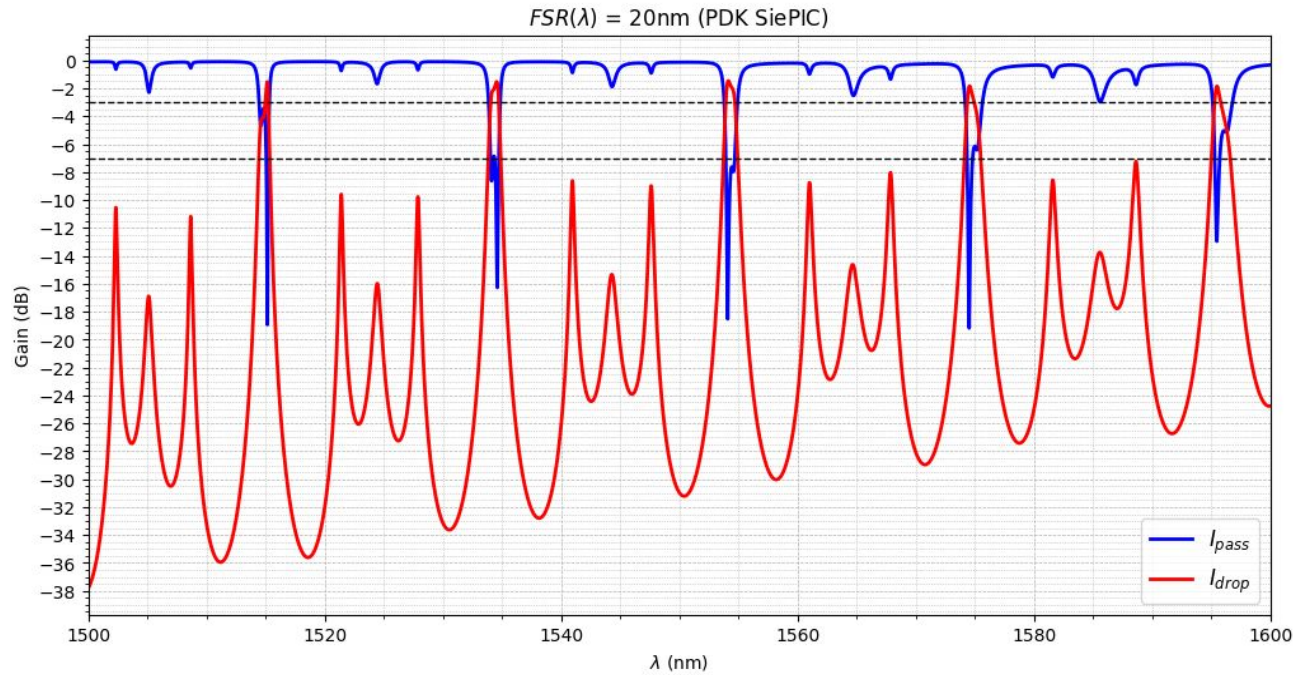
Resultados

- PDK SiePIC ($\text{FSR}(\lambda) = 20\text{nm}$):



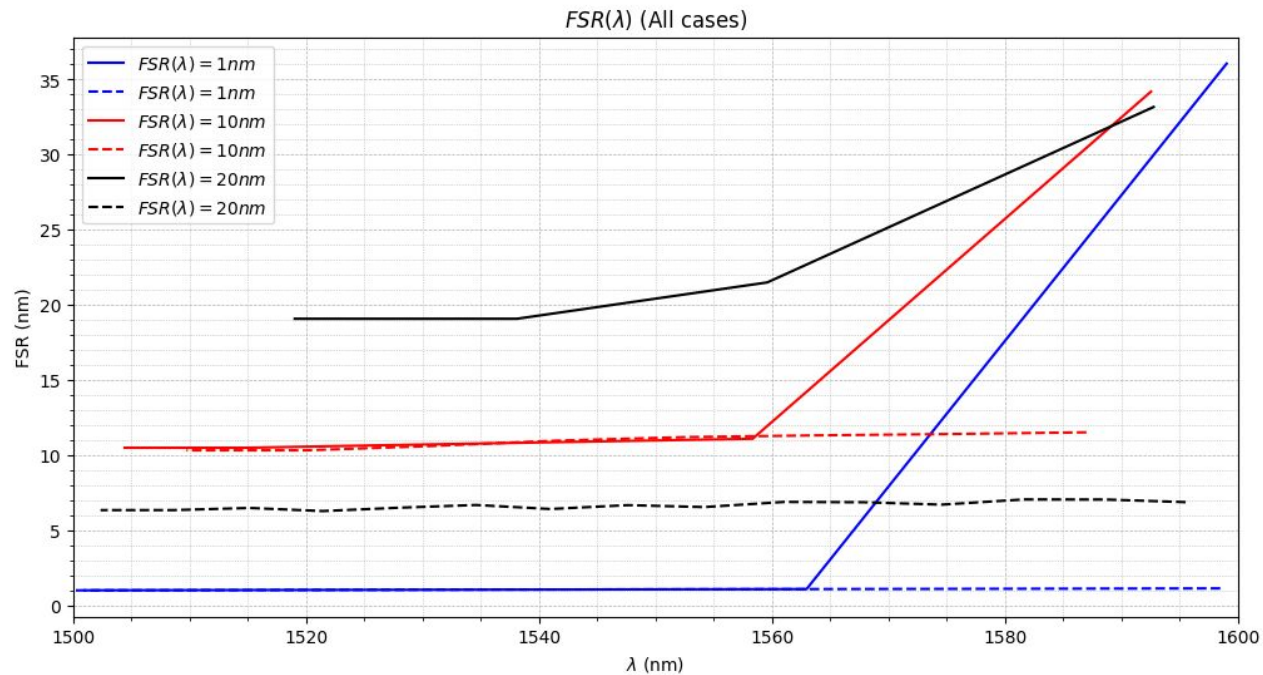
Resultados

- PDK SiePIC ($\text{FSR}(\lambda) = 20\text{nm}$):



Resultados

- PDK SiePIC:



- Klayout:

