

## CSE 232 SPRING 2020

## HOMEWORK 2.

1-) Assuming Moore's law (doubling each 18 months)

$$\frac{12 \cdot 10}{18} = 6,667 \text{ doublings.}$$

$$2^{6,667} = 101,617,143 \text{ billion transistors.}$$

2-)  $F = (a \text{ And } b) \text{ OR } c \text{ OR } d$

a)  $F = (1 \text{ And } 1) \text{ OR } 1 \text{ OR } 0 \Rightarrow F = 1$

b)  $F = (0 \text{ And } 1) \text{ OR } 1 \text{ OR } 0 \Rightarrow F = 1$

c)  $F = (1 \text{ AND } 1) \text{ OR } 0 \text{ OR } 0 \Rightarrow F = 1$

d)  $F = (1 \text{ AND } 0) \text{ OR } 1 \text{ OR } 1 \Rightarrow F = 1$

3-)

a) The variables are  $a, b, c, d$ .

b) The literals are  $a, a', b, c, c', d$ .

c) The product terms are  $a, a'b, acd$  and  $c'$ .

4-)  $F = a'b'c + a'bc' + a'bc + ab'c' + abc' + abc$

$$\begin{aligned} 5) F(a,b,c) &= a'b'c + a'bc' + a'bc + ab'c' + abc' + abc \\ &= a'b'c + ab'c' + a'bc' + a'bc + abc' + abc \\ &= (a'+a)b'c + a'b(c'+c) + ab(c'+c) \\ &= (1)b'c + a'b(1) + ab(1) \quad (\text{Since } x+x'=1) \\ &= b'c + a'b + ab \\ &= b'c + (a'+a)b \\ &= b'c + (1)b = b'c + b \\ &= (b'+b)(c+b) \\ &= (1)(c+b) \\ &= b+c \\ F(a,b,c) &= (b+c) // \end{aligned}$$

6)  $f = (a+b)' a$

a)  $F = (a+b)' a$   
 $= (a' b') a$   
 $= (a a') b'$   
 $= (0) b'$

$F = 0$  (1)  $G = a+b'$

Compare equation (1) and (2) different.

Hence, the two Boolean equations are not equivalent.

b) Table (1)

a	b	a+b	(a+b)'	$F = (a+b)' a$
0	0	0	1	0
0	1	1	0	0
1	0	1	0	0
1	1	1	0	0

Table (2)

a	b	b'	$G = a+b'$
0	0	1	1
0	1	0	0
1	0	1	1
1	1	0	1

⇒ Table (1) and Table (2) 's output values are different.

Hence, the two Boolean equations are not equivalent

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7) input

N3	N2	N1	N0
0	0	0	0
0	0	0	1
0	0	1	0
0	0	1	1
0	1	0	0
0	1	0	1
0	1	1	0
0	1	1	1
1	0	0	0
1	0	0	1
1	0	1	0
1	0	1	1
1	1	0	0
1	1	0	1
1	1	1	0
1	1	1	1

output

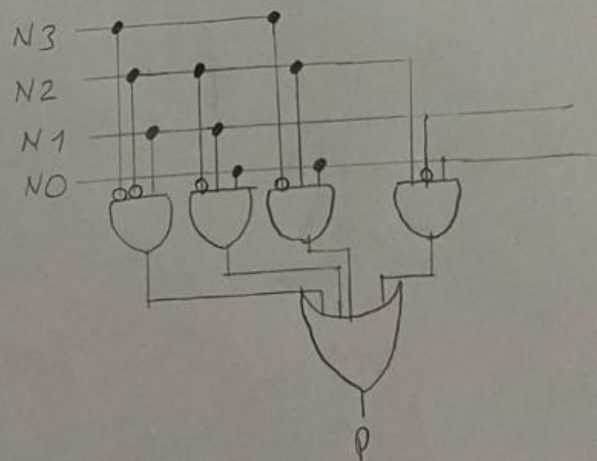
P
0
0
1
1
0
1
0
1
0
0
0
1
0
1
0
0

The output Boolean equation

$$P = \left\{ \begin{aligned} &N3'N2'N1N0' + N3'N2'N1N0 + N3'N2N1'N0 \\ &+ N3'N2N1N0 + N3N2'N1N0 + N3N2N1'N0 \end{aligned} \right\}$$

$$P = N3'N2'N1 + N2'N1N0 + N3'N2N0 + N2N1'N0$$

The gate level circuit for the simplified Boolean equation



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Input				Outputs
m3	m2	m1	m0	C
0	0	0	0	0
0	0	0	1	0
0	0	1	1	0
0	1	1	0	1
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

$$C' = m_3'm_2'm_1'm_0' + m_3'm_2'm_1'm_0 + m_3'm_2'm_1m_0' + m_3'm_2m_1'm_0' + m_3m_2'm_1'm_0'$$

The gate level circuit.

