st – state;
I – instruction;
ms – master clock;
out – slave clock;

program:

@123 (=999)

D=A

D=M

1 CYCLE					
T		T+1			
master 0+	slave 1	`1+`		`2+`	
I: @123	I: D=A	I: D=A	I: D=M	I: D=M	I: next
A ms = 123	A ms = 123	A st = 123	A st = 123	A st = 123	A st = 123
A out = 0	A out = 123	A out = 123	A out = 123	A out = 123	A out = 123
PC ms = 1	PC ms = 1	PC ms = 2	PC st = 2	PC st = 3	PC st = 3
PC out = 0	PC out = 1	PC out = 1	PC out = 2	PC out = 2	PC out = 3
D ms = 0	D ms = 0	D ms = 123	D st = 123	D st = 999	D st = 999
D out = 0	D out = 0	D out = 0	D out = 123	D out = 123	D out = 999
M addr = 0	M addr = 123	M addr = 123	M addr = 123	M addr = 123	M addr = 123
M in = ram[0]	M in = 999	M in = 999	M in = 999	M in = 999	M in = 999
slave 0+	master 1		2		3

made by Savva Sobolev

1