

Sequence  
input

Loop  
position  
input

Loop  
type  
input

TOKENISE EMBEDDING

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graph TD; SI[Sequence input] --> TE[TOKENISE EMBEDDING]; LPI[Loop position input] --> TE; LTI[Loop type input] --> TE; TE --> C[Concatenation]; C --> KE[KERAS EMBEDDING]; KE --> CON[CONVOLUTION]; CON --> Output[ ];
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The diagram illustrates a neural network architecture. It begins with three inputs: 'Sequence input', 'Loop position input', and 'Loop type input'. These inputs are fed into a 'TOKENISE EMBEDDING' block. The output of this block is then processed by a 'Concatenation' step. The concatenated output is then passed through a 'KERAS EMBEDDING' block, followed by a 'CONVOLUTION' block. The final output is indicated by a downward arrow at the bottom of the diagram.

Concatenation

KERAS EMBEDDING

CONVOLUTION