#### **COMP6210 Automated Software Verification**

Linear Temporal Logic

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#### **Intended Learning Outcomes**

By the end of these two lectures, you will be able to

 formalise simple temporal properties in Linear Temporal Logic (LTL)

LTL is the simplest and most widely used *temporal* specification language.

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# **System Specifications**

#### Some simple system properties:

- deadlock: a program is deadlocked when it cannot proceed
  - common in concurrent programs: threads waiting for each other
  - in a transition system model, this is seen as a state with no outgoing transitions
- basic propositions concerning values of the variables
  - e.g. *x* ≥ 1

#### System properties involving *time*:

- system invariant: all reachable program states must satisfy it
- reachability: eventually a state with a given property is reached
- . . .

We will use **Linear Temporal Logic** to formalise temporal properties that must hold **on all paths** which start in an initial state.

#### Example: temporal properties for mutual exclusion

- mutual exclusion: at most one process in critical section at any time (i.e. in every reachable state)
- starvation freedom (enhanced version): whenever a
  process tries to enter its critical section, it will eventually
  succeed (along every computation path)
- no strict sequencing: processes need not enter their critical section in strict sequence (i.e. there exists a computation path along which they don't)

### Recap on Propositional Logic

- assume a set *Prop* of atomic propositions
- syntax for formulas:

$$\phi, \psi ::= p \mid \text{tt} \mid \neg \phi \mid \phi \land \psi$$
  $(p \in Prop)$ 

Note: all other boolean operators are definable, e.g.

ff ::= 
$$\neg$$
tt  
 $\phi \lor \psi$  ::=  $\neg(\neg \phi \land \neg \psi)$   
 $\phi \to \psi$  ::=  $\neg(\phi \land \neg \psi)$ 

 models are valuations, stating which propositions are true and which are not:

$$V: Prop \rightarrow \{ \text{True}, \text{False} \}$$

- semantics (meaning) of formulas:
  - V defines the meaning of atomic propositions
  - · truth tables define the meaning of boolean operators

#### Recap on Propositional Logic: Example

*Prop* = { red, amber, green}

- some formulas: red, green, ¬amber, red ∨ green, red ∧ amber, red ∧ green, red → green
- · a model:

$$V(red) = True, V(amber) = True, V(green) = False$$

- · semantics:
  - red,  $red \lor green$ ,  $red \land amber$  all True in the above model
  - green,  $\neg$ amber,  $red \land green$ ,  $red \rightarrow green$  all False in the above model

#### **Outline**

Linear Temporal Logic (LTL)

#### **Paths in Transition Systems**

• a path in a transition system is a finite or infinite sequence of states

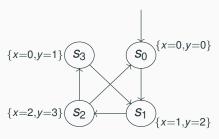
$$\pi = s_0 s_1 s_2 \dots$$

with  $s_0$  an initial state and  $s_i \rightarrow s_{i+1}$  for all i.

- we write  $\pi^i$  for the suffix of  $\pi$  starting at  $s_i$ 
  - $\pi^0 = \pi$
  - $\pi^1 = s_1 s_2 s_3 \dots$
  - ...

### Paths in Transition Systems – Example

transition system:



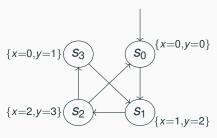
- as atomic propositions we take x=0, x=1, x=2, y=0, y=1, y=2, y=3
- some computation paths:

$$\begin{split} s_0 &\to s_1 \to s_2 \to s_3 \to s_1 \to s_2 \to s_3 \to \dots \\ s_0 &\to s_1 \to s_2 \to s_0 \to s_1 \to s_2 \to \dots \\ s_0 &\to s_1 \to s_2 \to s_3 \to s_1 \to s_2 \to s_0 \to \dots \end{split}$$

- · LTL formulas must hold on all computation paths
  - e.g. "on every path, a state satisfying x=1 is eventually reached"

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## Syntax of LTL

Fix a set *Prop* of atomic propositions.

LTL formulas are of two kinds:

path formulas:

```
    tt, p where p is an atomic proposition
```

if f and g are path formulas, then so are:

state formulas:

```
A f along All computation paths, f holds
```

• binding priorities: unary operators ; U ;  $\wedge$  and  $\vee$  ;  $\rightarrow$ 

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if f and g are path formulas, then so are:

```
\neg f not f
f \land g f and g

X f at the neXt point in time, f

F f at some point in the Future, f

G f Globally (at all future points) f

f \lor g
```

· state formulas:

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f \cup g f \cup f
```

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• binding priorities: unary operators ; U ;  $\wedge$  and  $\vee$  ;  $\rightarrow$ 

#### **Semantics of LTL**

Fix a transition system M = (S, R, V).

The semantics of LTL defines:

• when a computation path  $\pi$  through  $\emph{M}$  satisfies a path formula  $\emph{f}$ ,

Notation:  $\pi \models f$ 

• when a state s of M satisfies a state formula A f.

Notation:  $s \models Af$ 

# **Meaning of Temporal Operators (Pictorially)**

Let  $\pi = s_0 \longrightarrow s_1 \longrightarrow s_2 \longrightarrow \dots$  be a computation path.

$\pi^0$	$\pi^0=s_0 o\dots$	$\pi^1 = s_1 \rightarrow \dots$		$\pi^i = s_i \to \dots$	
<b>X</b> f		f			
F f				f	
<b>G</b> f	f	f	f	f	f
f <b>U</b> g	f	f	f	g	

#### Note:

• f here is a path formula, so it is itself interpreted over paths!!

# **Semantics of LTL (Cont'd)**

- Given  $\pi = s_0 \rightarrow s_1 \rightarrow s_2 \rightarrow \ldots$ , recall  $\pi^i = s_i \rightarrow s_{i+1} \rightarrow \ldots$
- Now define when a path formula f holds in a path  $\pi$ :

$$\pi \models \text{tt}$$
 $\pi \models p \quad \text{iff} \quad p \in V(s_0)$ 
 $\pi \models \neg f \quad \text{iff} \quad \pi \models f \quad \text{does not hold}$ 
 $\pi \models f \land g \quad \text{iff} \quad \pi \models f \quad \text{and} \quad \pi \models g$ 
 $\pi \models \mathbf{X} f \quad \text{iff} \quad \pi^1 \models f$ 
 $\pi \models \mathbf{F} f \quad \text{iff} \quad \text{there exists } i \text{ s.t. } \pi^i \models f$ 
 $\pi \models \mathbf{G} f \quad \text{iff} \quad \pi^i \models f \quad \text{for all } i \geq 0$ 

• Finally, define when a state formula  $\mathbf{A} f$  holds in a state  $s \in S$ :

 $s \models \mathbf{A} f$  iff  $\pi \models f$  for all paths  $\pi$  starting in s

## Semantics of LTL (Cont'd)

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 $\pi \models f \land g$  iff  $\pi \models f$  and  $\pi \models g$ 
 $\pi \models \mathbf{X}f$  iff  $\pi^1 \models f$ 
 $\pi \models \mathbf{F}f$  iff there exists  $i$  s.t.  $\pi^i \models f$ 
 $\pi \models \mathbf{G}f$  iff  $\pi^i \models f$  for all  $i \geq 0$ 
 $\pi \models f \cup g$  iff there exists  $i$  s.t.  $\pi^0 \models f, \ldots, \pi^{i-1} \models f, \pi^i \models g$ 

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 $\pi \models f \mathbf{U}g \quad \text{iff} \quad \text{there exists } i \text{ s.t. } \pi^0 \models f, \dots, \pi^{i-1} \models f, \pi^i \models g$ 

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### **Semantics of LTL - Example**

**Note:**  $\pi \models \mathbf{G} \mathbf{F} f$  iff f holds infinitely often along  $\pi$ .

## Semantics of LTL – Example

#### Note

•  $s \models A G f$  iff f holds in all states reachable from s (incl. s)

•  $s \models A G F f$  iff f holds infinitely often on every path from s

### **Semantics of LTL – Example**

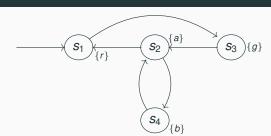
$$\begin{array}{c} s_0 \models \mathbf{A} ((x=0) \land (y=0)) \\ s_0 \models \mathbf{A} \mathbf{X} (x=1) \\ s_0 \models \mathbf{A} \mathbf{X} \mathbf{X} ((x=2) \land (y=3)) \\ s_1 \models \mathbf{A} \mathbf{G} \neg ((x=1) \land (y=3)) \end{array}$$

$$s_1 \models \mathbf{A} \mathbf{G} ((x = 1) \land (y = 3))$$
  
 $s_1 \not\models \mathbf{A} \mathbf{G} (x = 1)$   
 $s_1 \models \mathbf{A} \mathbf{G} \mathbf{F} (x = 0)$   
 $s_0 \not\models \mathbf{A} \mathbf{G} \mathbf{F} ((x = 0) \land (y = 1))$   
 $s_0 \models \mathbf{A} (\mathbf{G} \mathbf{F} (y = 1) \rightarrow \mathbf{G} \mathbf{F} ((x = 0) \land (y = 1)))$   
 $s_1 \not\models \mathbf{A} (((y = 2) \lor (y = 3)) \mathbf{U} (y = 0))$   
 $s_1 \models \mathbf{A} (((y = 2) \lor (y = 3)) \mathbf{U} ((y = 0) \lor (y = 1)))$   
 $s_0 \models \mathbf{A} \mathbf{X} (((y = 2) \lor (y = 3)) \mathbf{U} ((y = 0) \lor (y = 1)))$ 

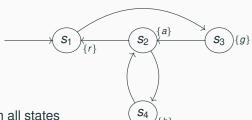
#### Note:

•  $s \models A G f$  iff f holds in all states reachable from s (incl. s)

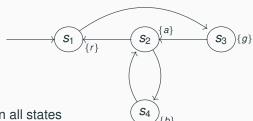
•  $s \models A G F f$  iff f holds infinitely often on every path from s



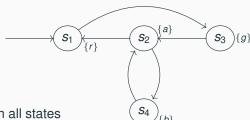
- **AF**a
- **AF**r
- **AF** *g*
- A G a
- **AGF**a
- A G F r
- **A** (*b* **U** ¬*b*)
- **A**(g**U**(a**U**r))



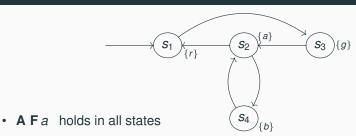
- A F a holds in all states
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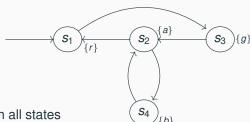
- A F a holds in all states
- **A** F r holds in state  $s_1$  only
- **AF** *g*
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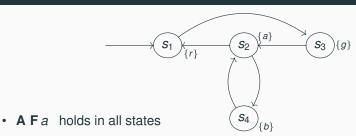
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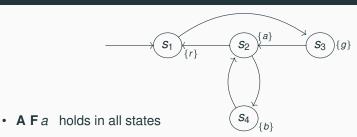
- A F r holds in state s<sub>1</sub> only
- **A F** g holds in states  $s_1$  and  $s_3$
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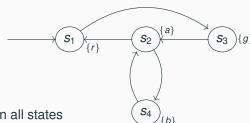
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• invariance (always):

"p remains invariantly true throughout every path"

guarantee (eventually):

"p will eventually become true in every path"

stability (non-progress):

"there is a point in every path where *p* will become invariantly true"

recurrence (progress):

"if p happens to be false at any given point in a path, it is always guaranteed to become true again later"

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#### Some LTL Patterns (Cont'd)

#### · response:

"any state satisfying p is eventually followed by a state satisfying q"

#### precedence:

"from any state satisfying p, the system will continuously satisfy property q until property r becomes true"

#### correlation:

'if p holds at some point in the future, so does q"

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• correlation:  $\mathbf{A}(\mathbf{F}p \rightarrow \mathbf{F}q)$ 

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## Some Interesting Equivalences

#### F(p且q是同时的意思)

$$\neg Gp \equiv F \neg p 
Gp \equiv GGp 
Fp \equiv FFp 
G(p \land q) \equiv Gp \land Gq 
F(p \lor q) \equiv Fp \lor Fq 
GF(p \lor q) \equiv GFp \lor GFq 
pUq \equiv pU(pUq) 
pUq \equiv (pUq)Uq$$



### Atomic propositions:

```
c_0, c_1 (critical state)

n_0, n_1 (non-critical state)

t_0, t_1 (trying to enter critical state)
```

 mutual exclusion: at most one process in critical section at any time

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**A** 
$$G \neg (c_0 \wedge c_1)$$

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$$((t_0 \to \mathbf{F} c_0) \land (t_1 \to \mathbf{F} c_1))$$

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Atomic propositions:

$$c_0$$
,  $c_1$  (critical state)  
 $n_0$ ,  $n_1$  (non-critical state)  
 $t_0$ ,  $t_1$  (trying to enter critical state)

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 absence of starvation: whenever a process tries to enter its critical section, it will eventually succeed

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$$((t_0 \to \mathbf{F} c_0) \land (t_1 \to \mathbf{F} c_1))$$

- no strict sequencing: processes need not enter their critical section in strict sequence
  - can only express the negation of this property . . .
  - ... but this is sufficient, since a counter-example to strict sequencing is proof for non-strict sequencing!

## Strict Sequencing in LTL

- define Weak Until operator:  $f \mathbf{W} g := \mathbf{G} f \vee f \mathbf{U} g$
- strict sequencing (assuming mutual exclusion holds!):

$$\mathbf{A}\left(\mathbf{G}\left(c_{0} 
ightarrow c_{0} \mathbf{W}\left(\neg c_{0} \wedge \neg c_{0} \mathbf{W} c_{1}
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ight) \wedge \mathbf{G}\left(\ldots
ight)
ight) imes \mathbf{X}$$

## Strict Sequencing in LTL

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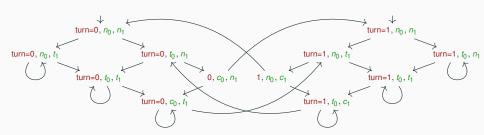
## Strict Sequencing in LTL

强Until,后续条件一定会发生 弱until,后续条件不一定发生

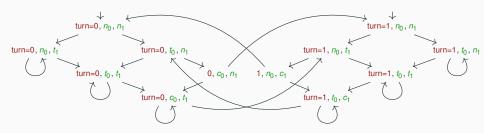
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$$\mathbf{A}\left(\mathbf{G}\left(c_{0}\rightarrow c_{0}\,\mathbf{W}\left(\neg c_{0}\wedge\neg c_{0}\,\mathbf{W}\,c_{1}\right)\right)\ \wedge\ \mathbf{G}\left(\ldots\right)\right)\qquad\times$$

- c0 在c1发生之后不能再次发生
- c0 不一定发生

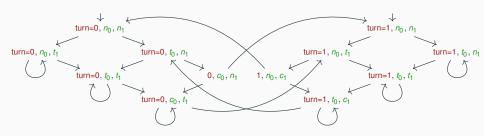


• **A G**  $\neg (c_0 \land c_1)$ 

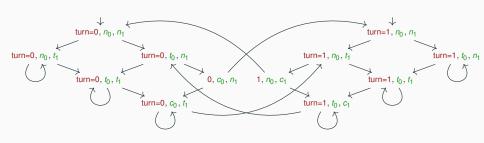


• **A G**  $\neg (c_0 \land c_1)$ 

Need to check that  $\neg(c_0 \land c_1)$  is true at all states reachable from the initial states.



- A G ¬(c<sub>0</sub> ∧ c<sub>1</sub>)
   Need to check that ¬(c<sub>0</sub> ∧ c<sub>1</sub>) is true at all states reachable from the initial states.
- AG  $((t_0 o \mathbf{F} c_0) \wedge (t_1 o \mathbf{F} c_1))$



• **A G** 
$$\neg (c_0 \land c_1)$$

Need to check that  $\neg(c_0 \land c_1)$  is true at all states reachable from the initial states.

• **A G**  $((t_0 \to \mathbf{F} c_0) \land (t_1 \to \mathbf{F} c_1))$  self loop  $\times$   $\checkmark$  Need fairness assumptions for the property to hold.

加前提(additional assumpution)可以变为真

#### **Exercise**

Assume the following atomic formulas: start, ready, requested, acknowledged, enabled, running, deadlock.

Specify the following correctness properties in LTL:

- It is impossible to reach a state where start holds but ready does not hold. AG(start->ready)
- Whenever a request occurs, it will eventually be acknowledged. AG(requested->F acknowledged)
- 3. If a process is enabled infinitely often along a path, then it runs infinitely often along that path. A(GF enabled->GF runs)
- 4. Whatever happens, deadlock will eventually occur. AF deadlock
- 5. From any state, it is possible to reach a *ready* state.

  AGF ready

AGF(enable->GF runs)

AG(G enable->GF runs)

AGF(enable U runs)