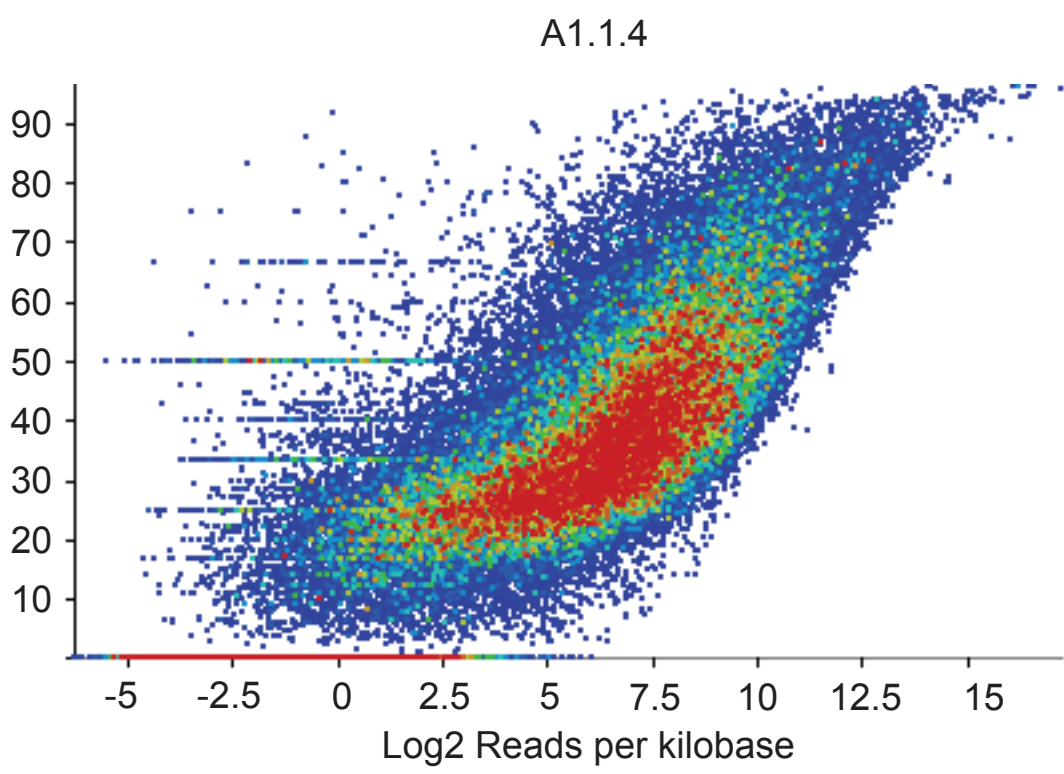
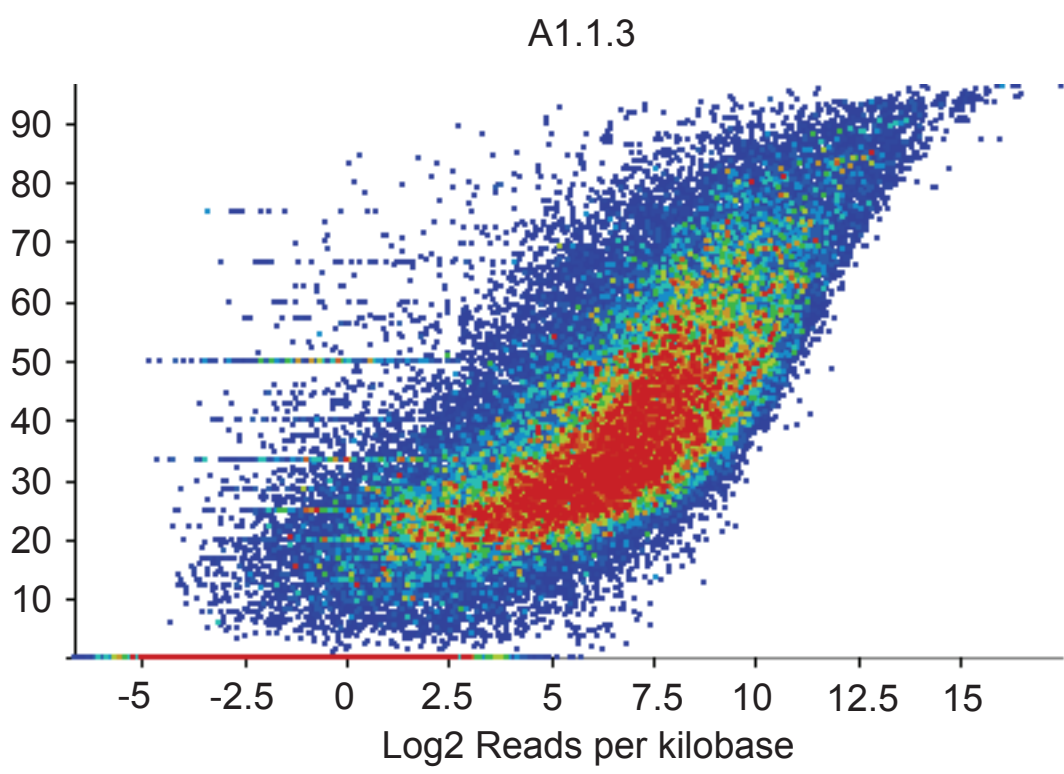
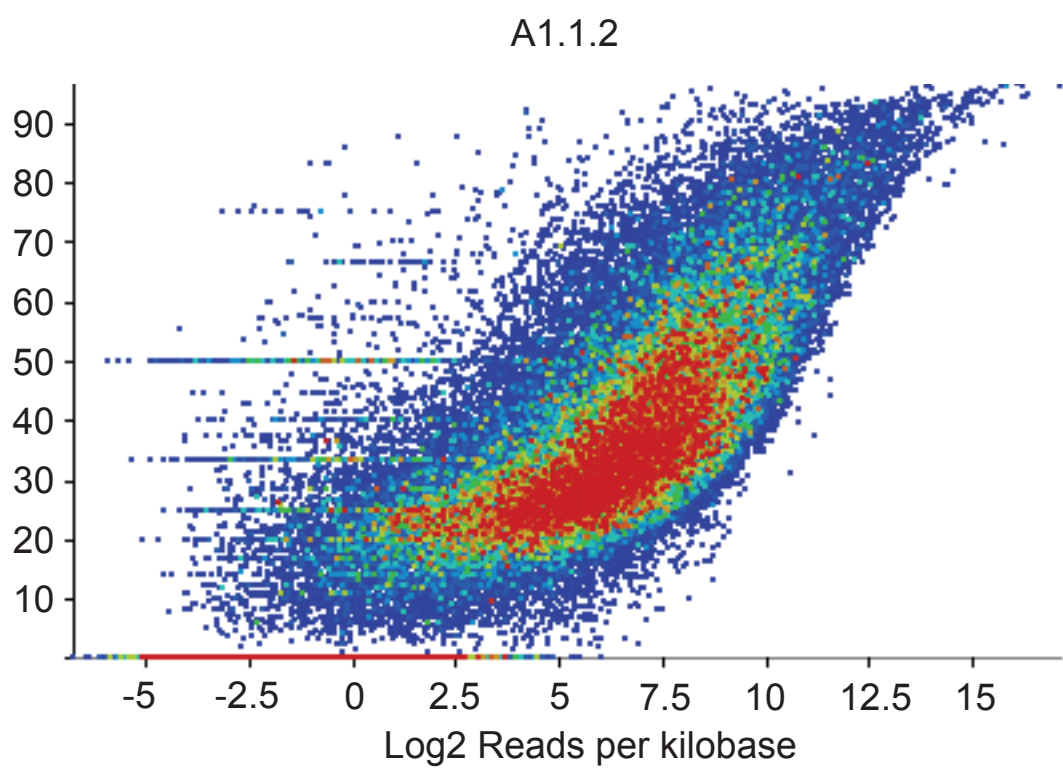
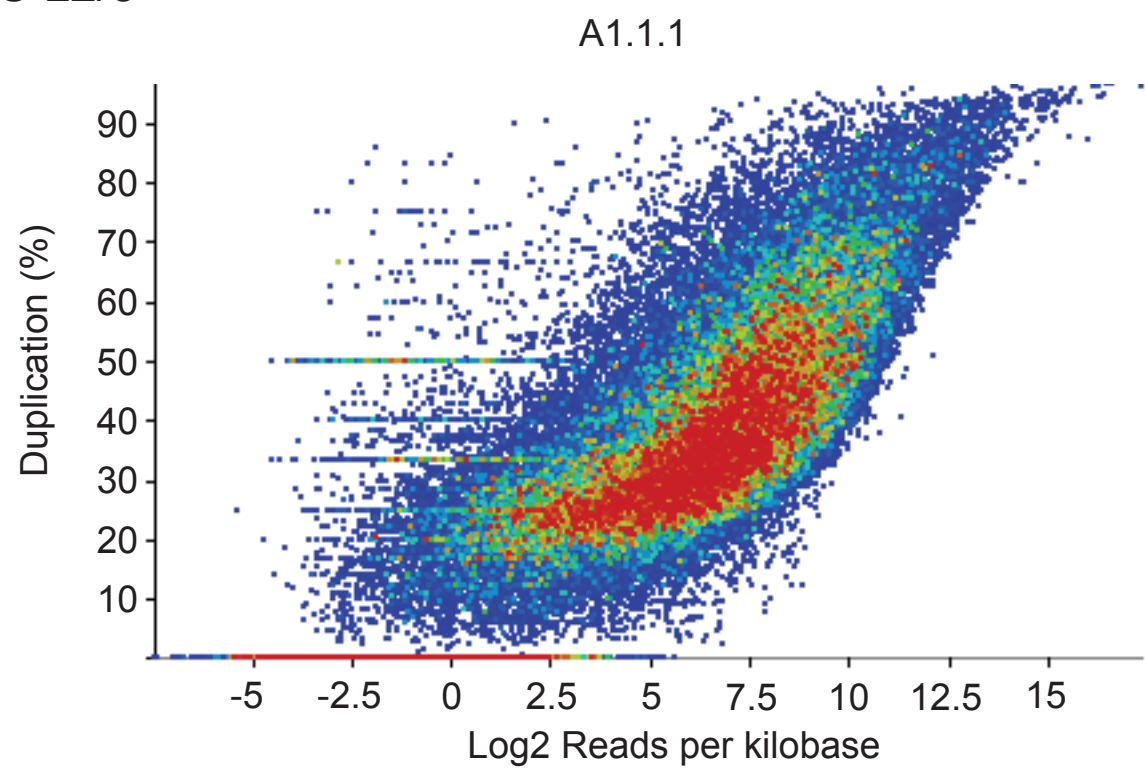
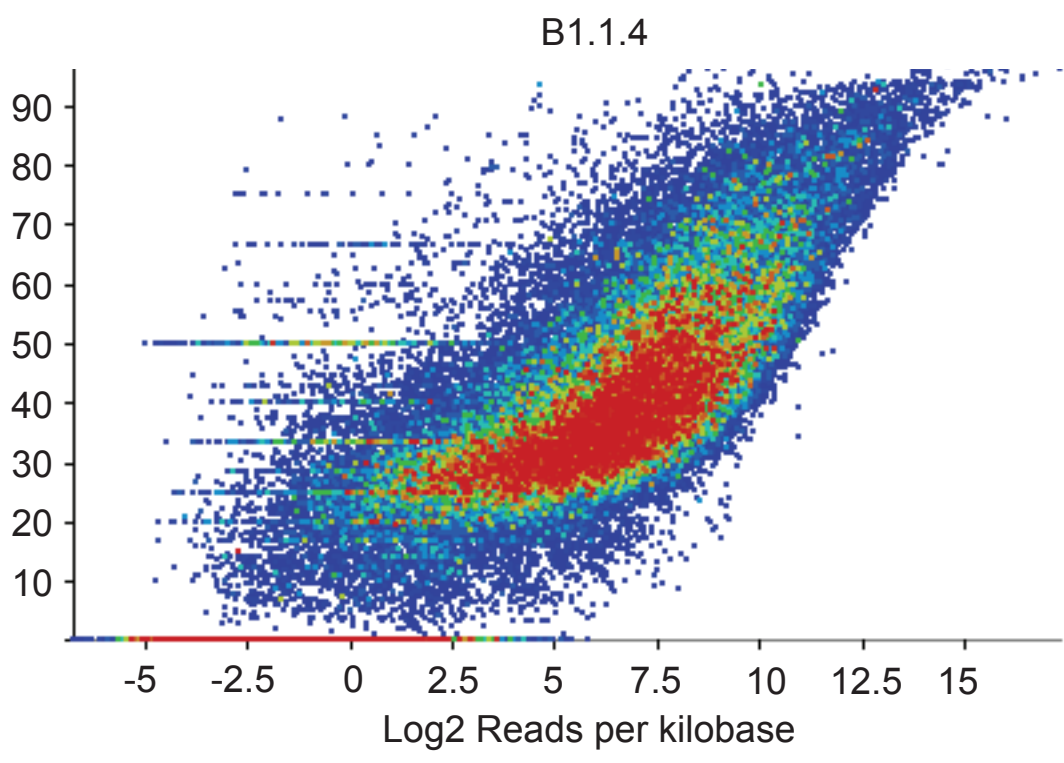
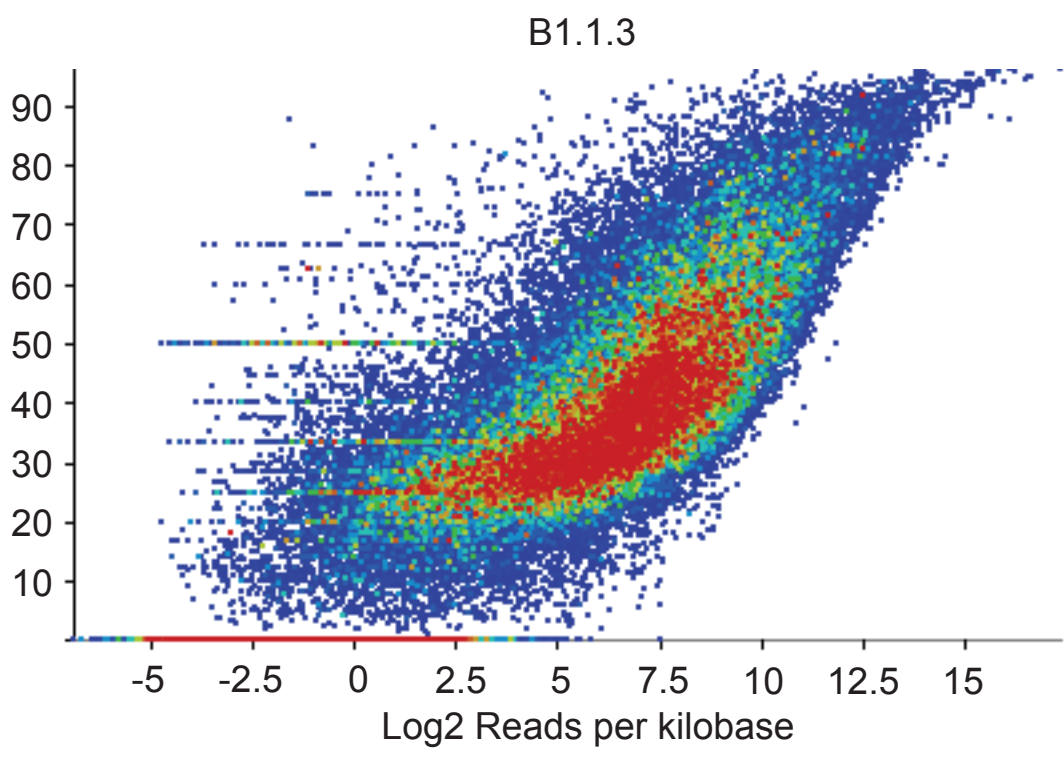
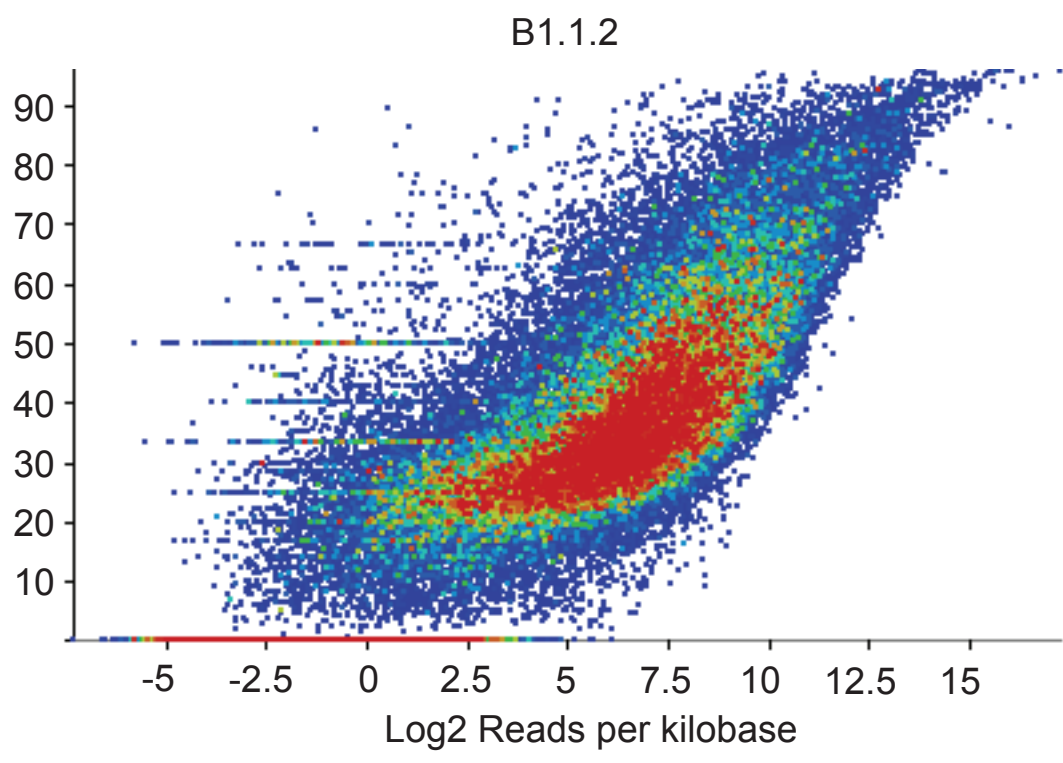
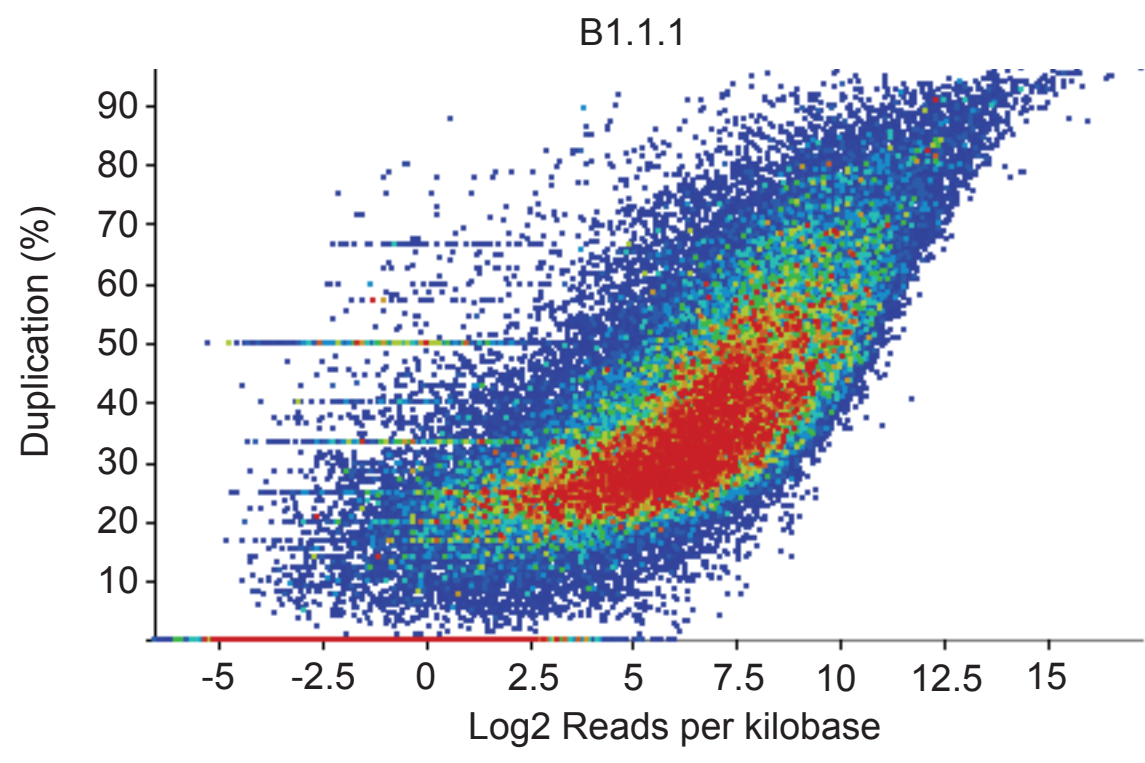


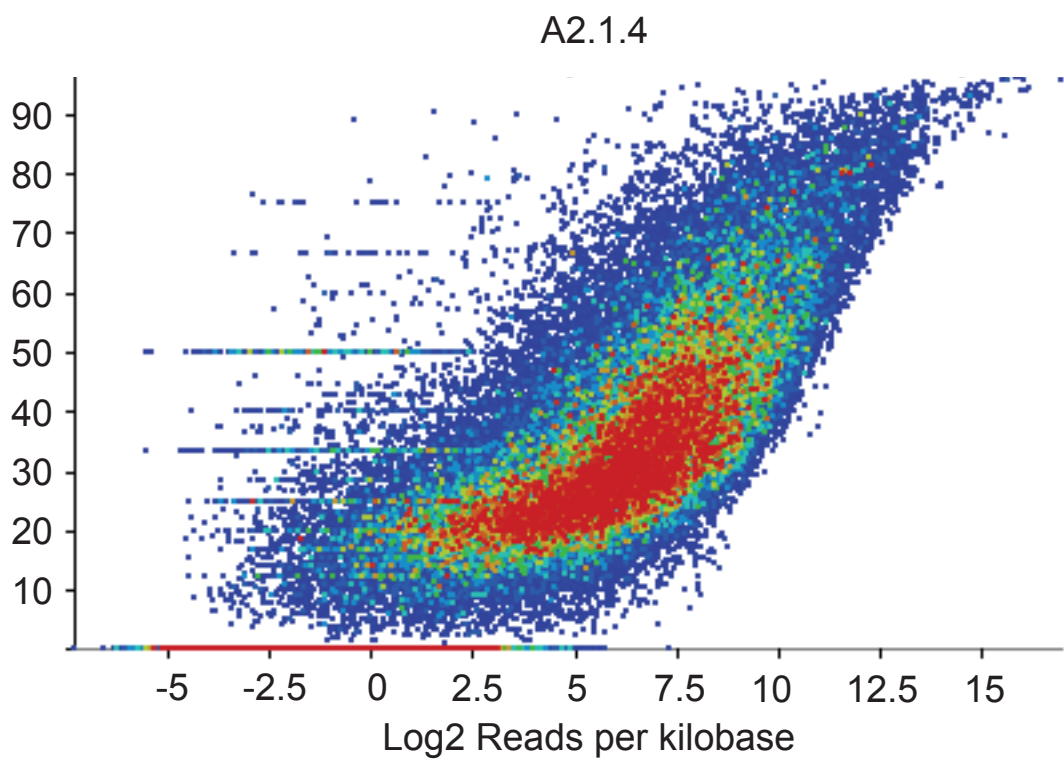
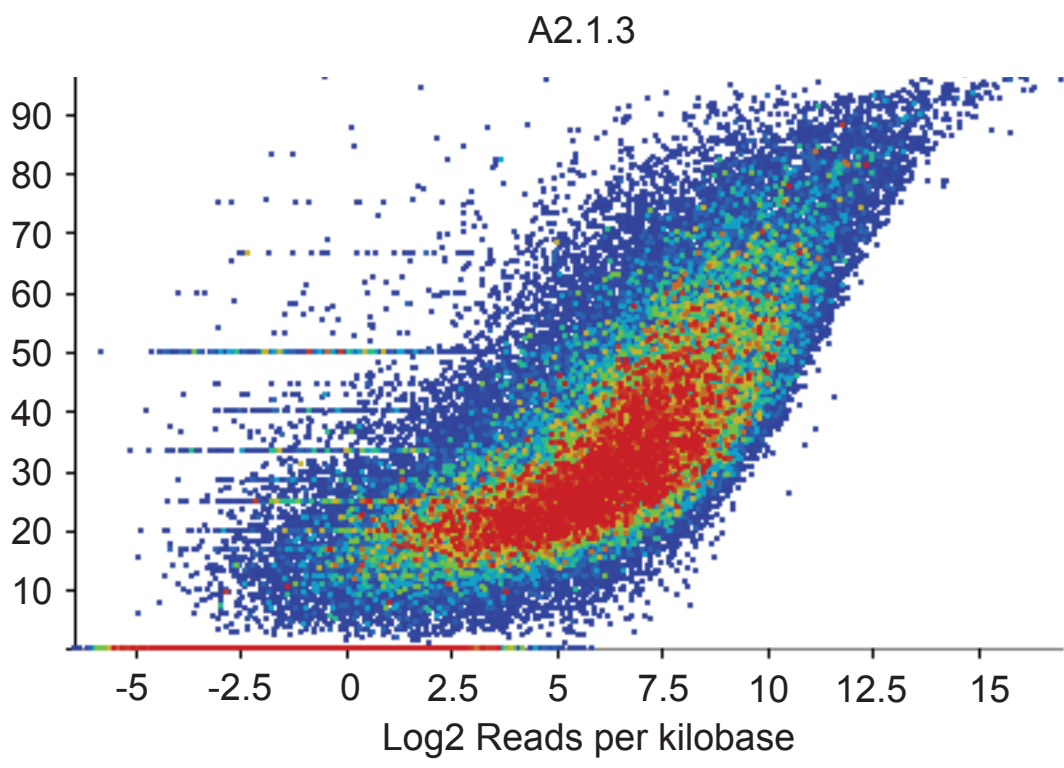
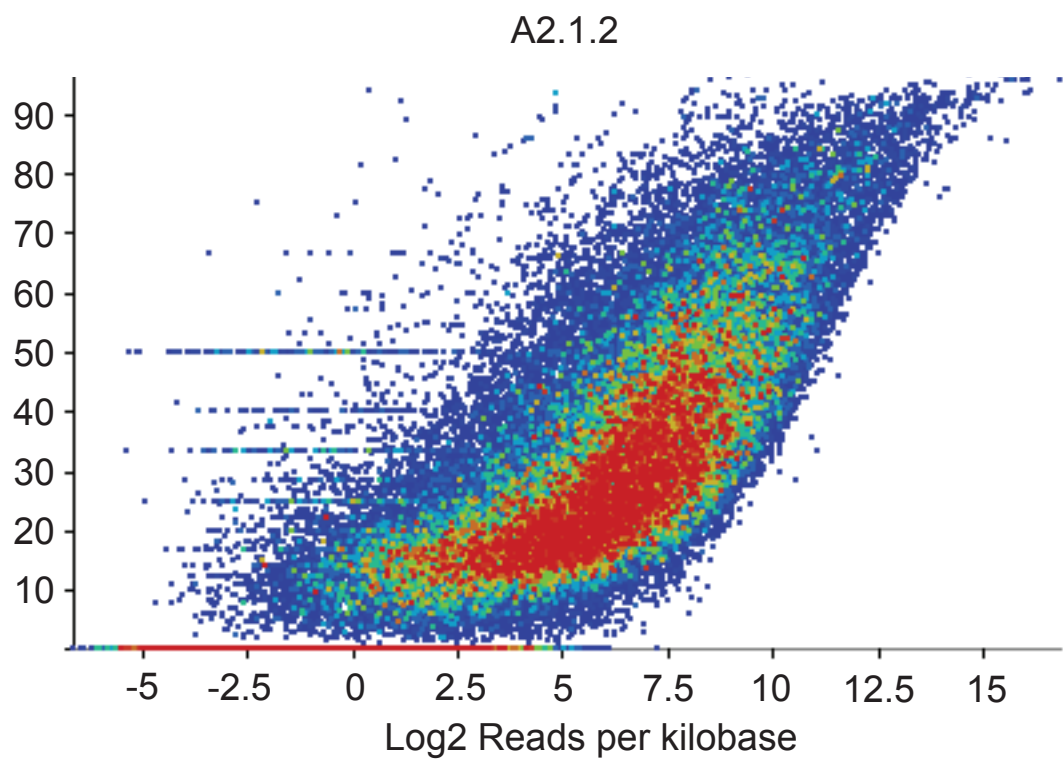
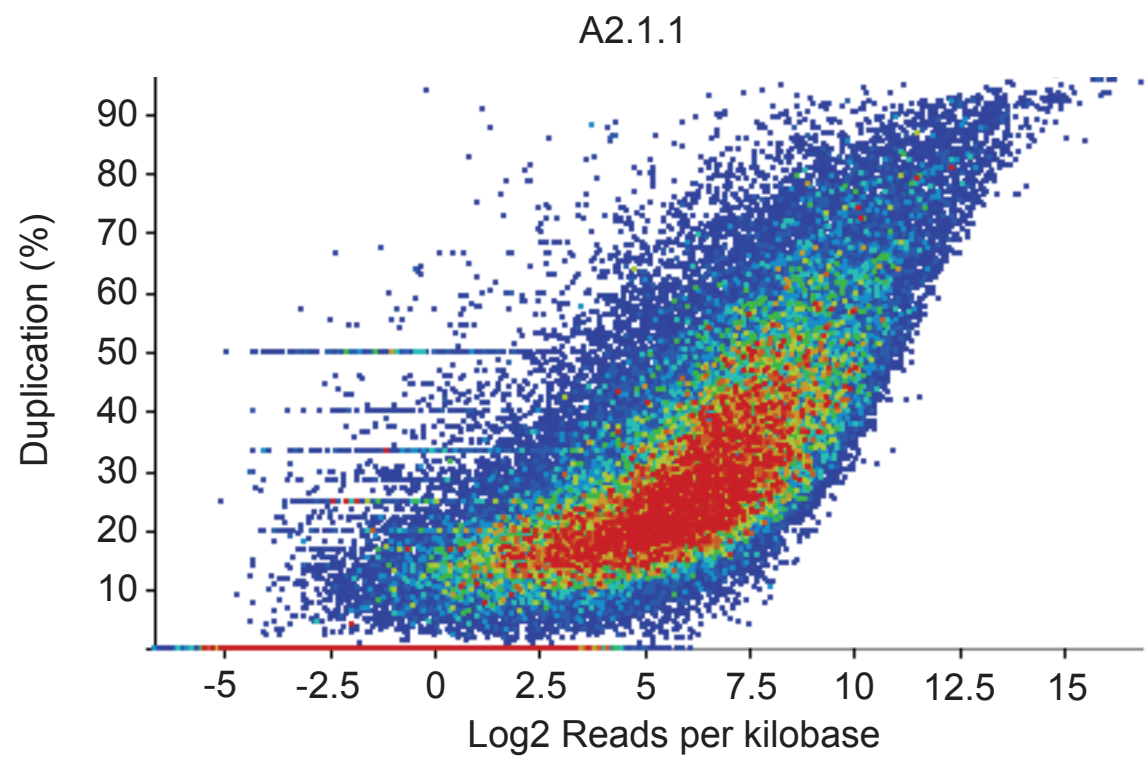
Control C L2/3



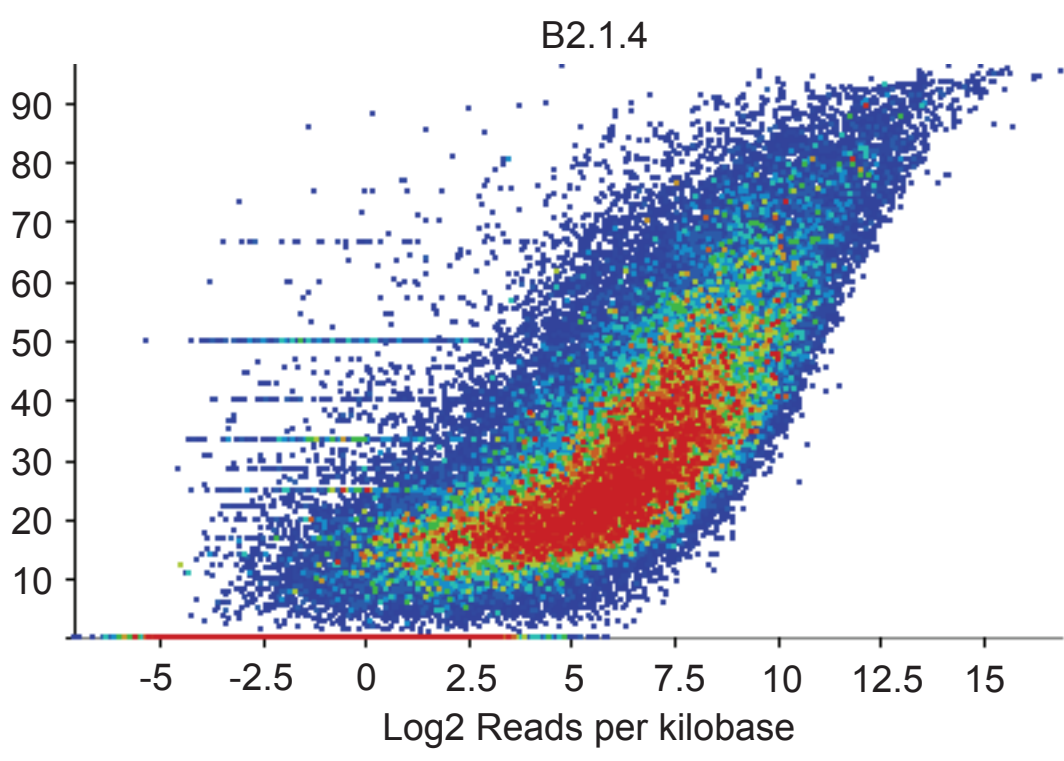
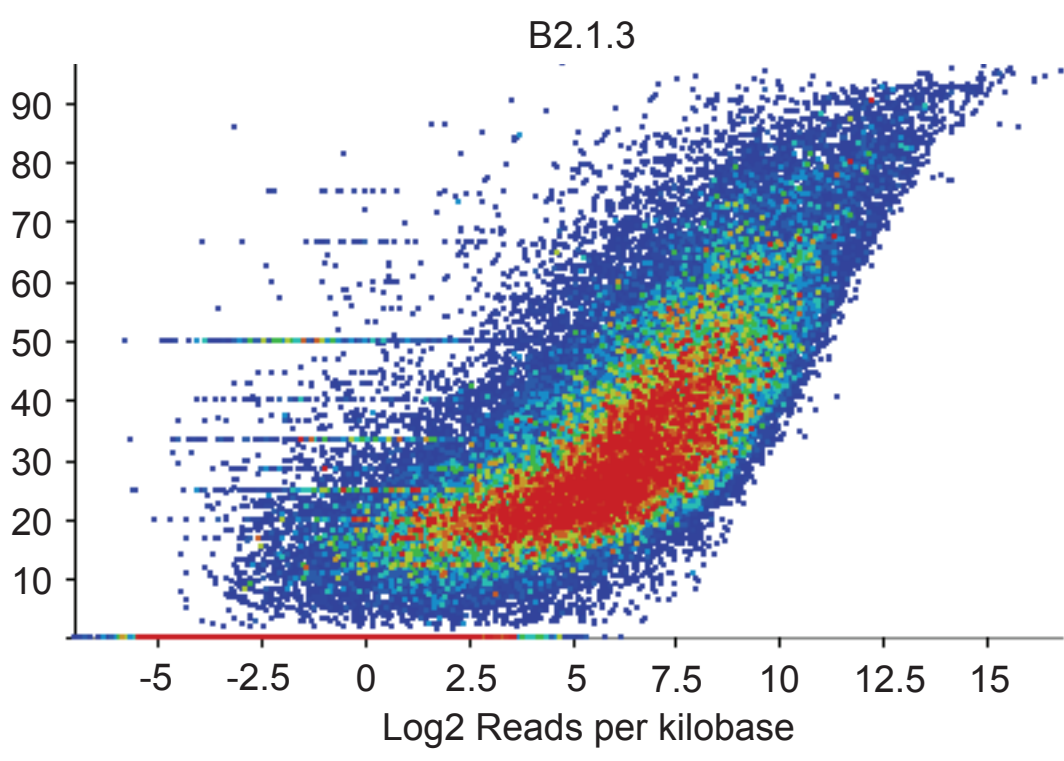
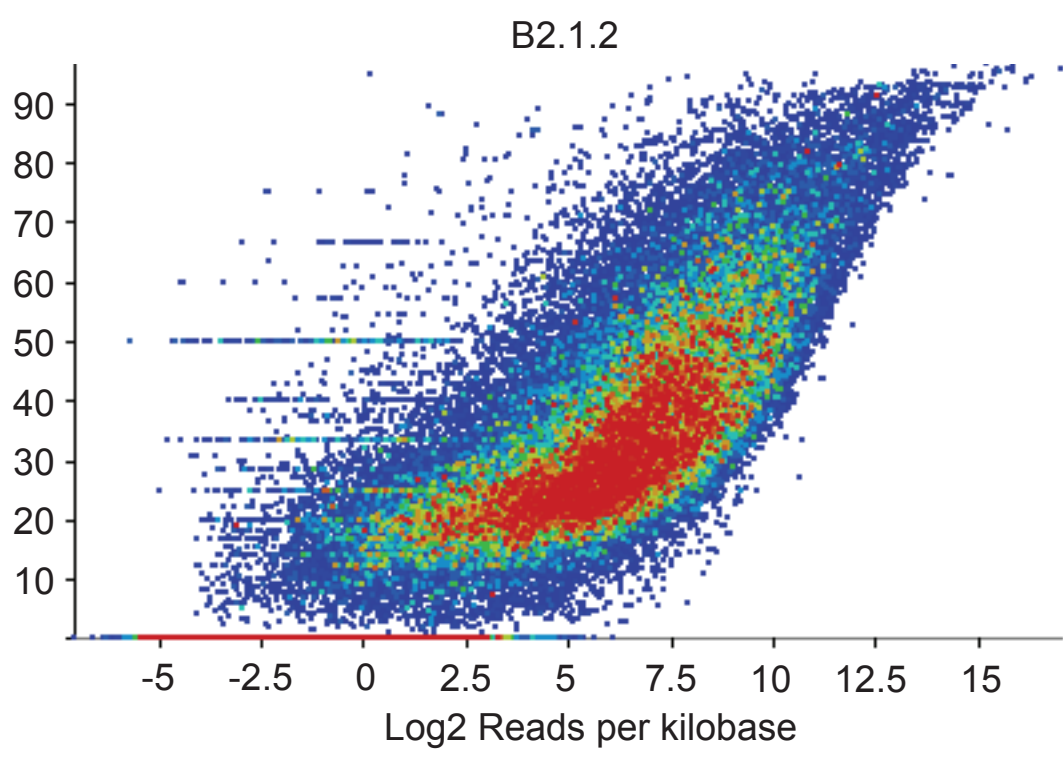
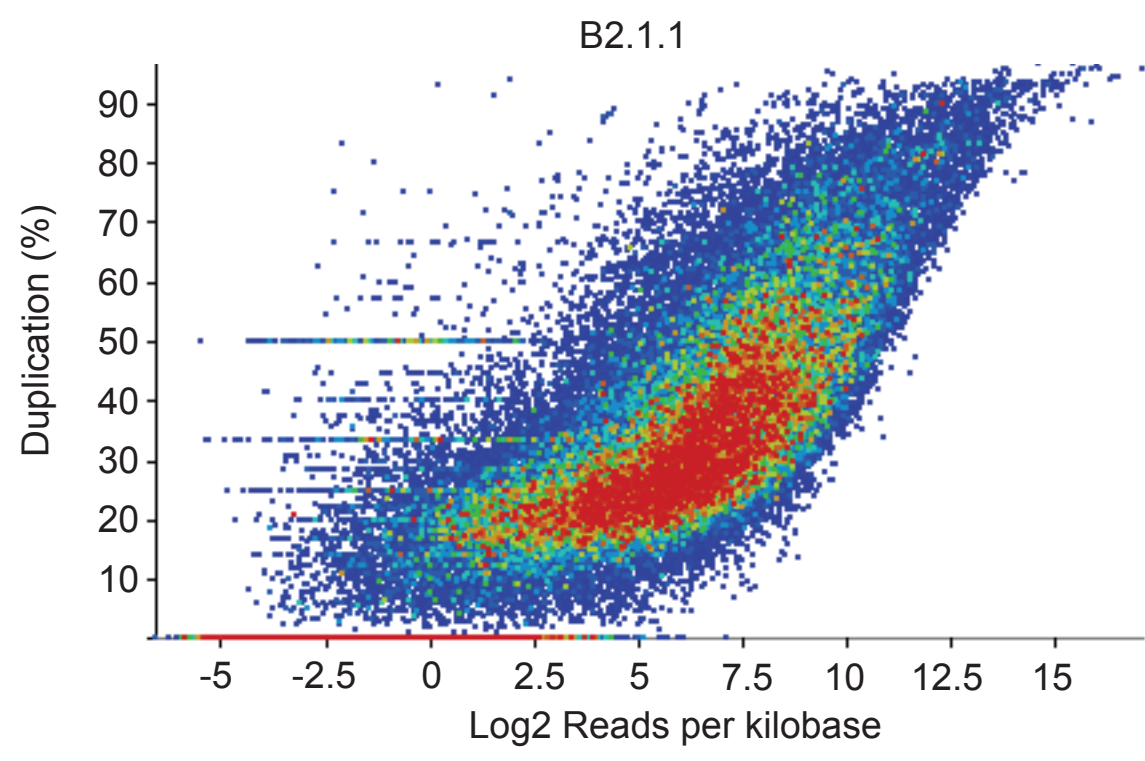
Control C L4



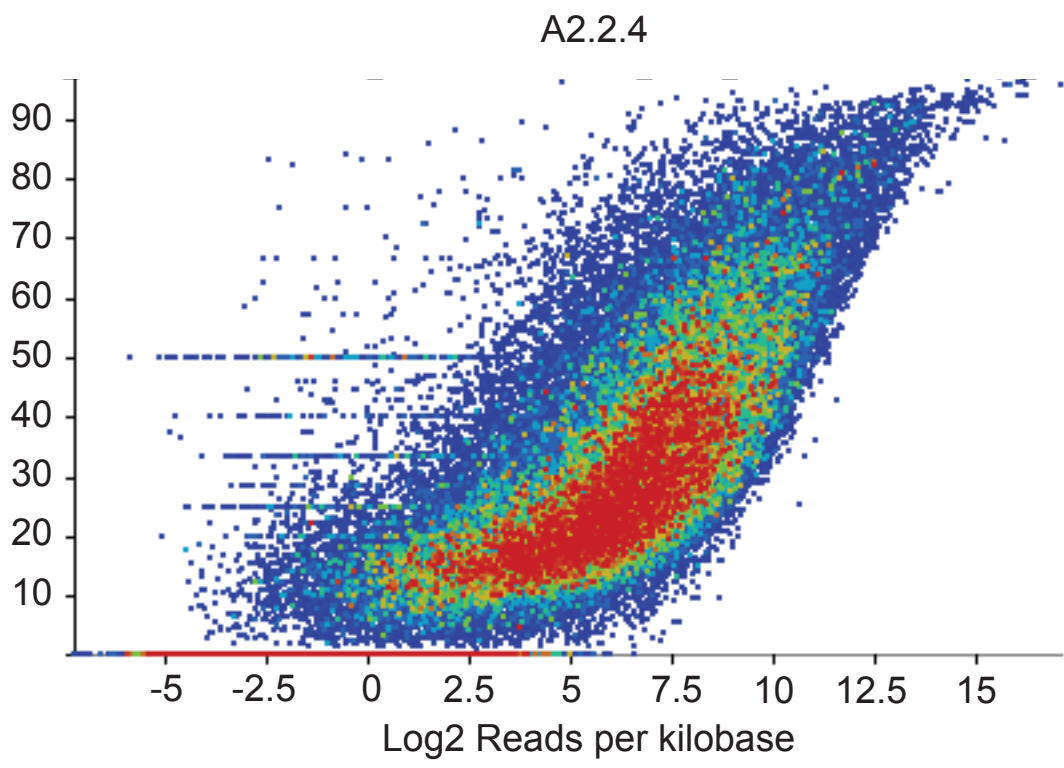
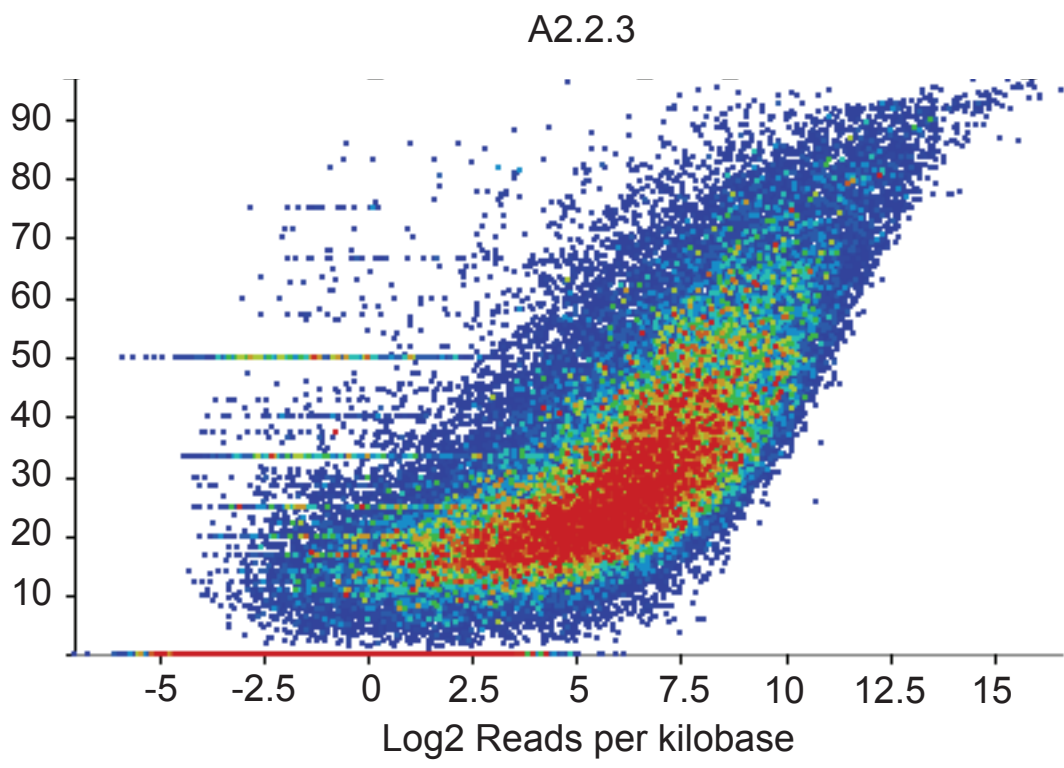
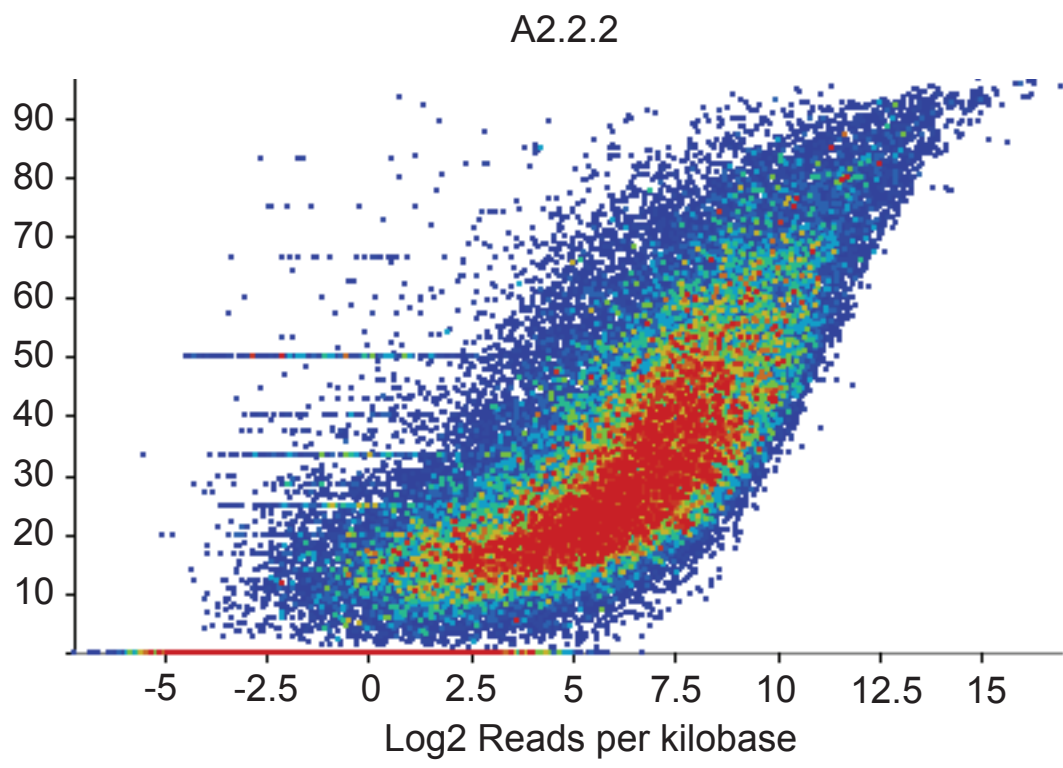
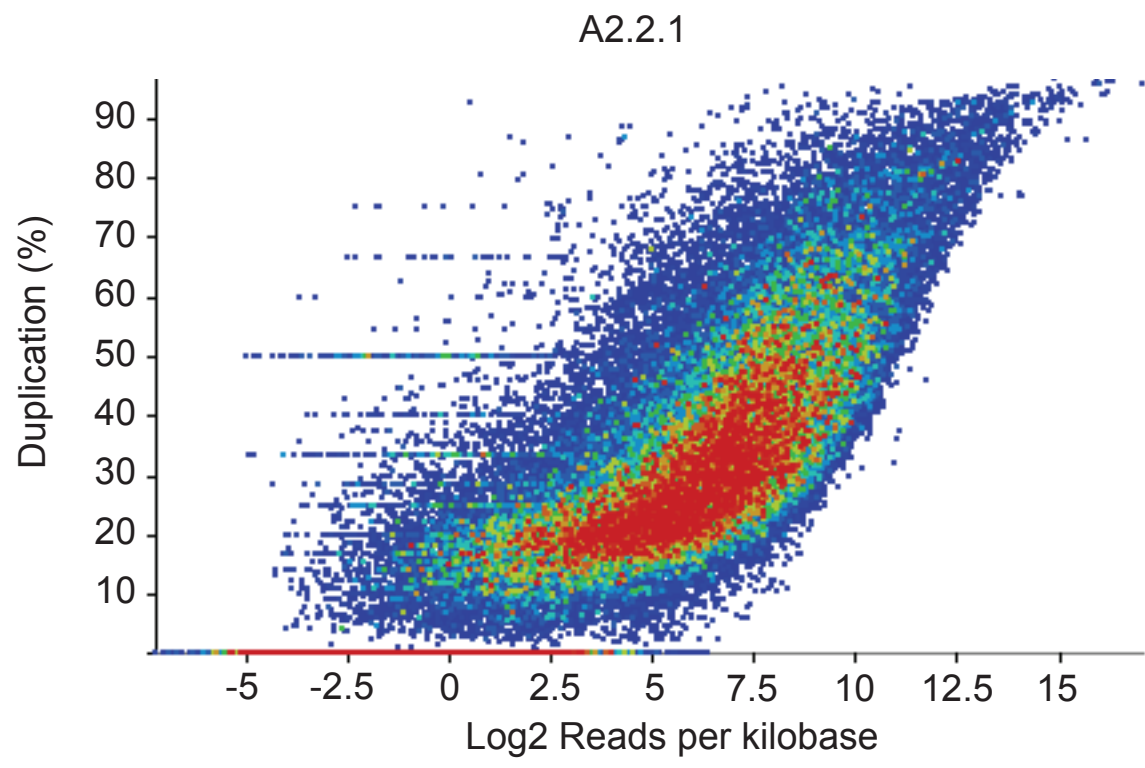
Deprived C L2/3



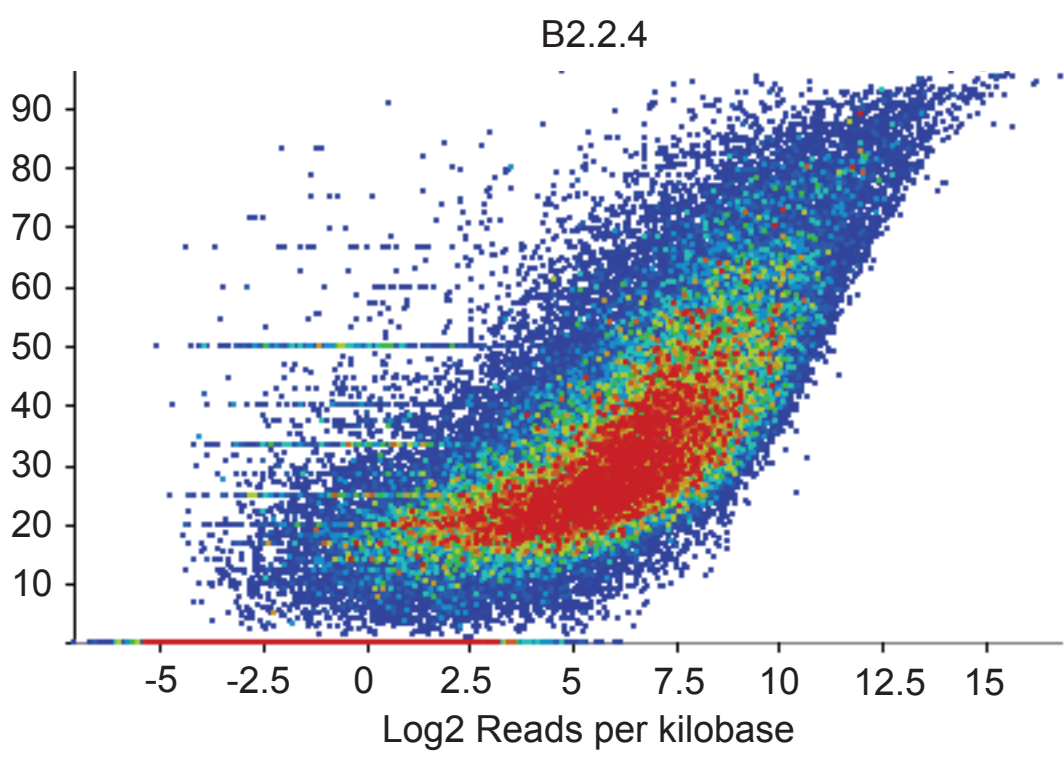
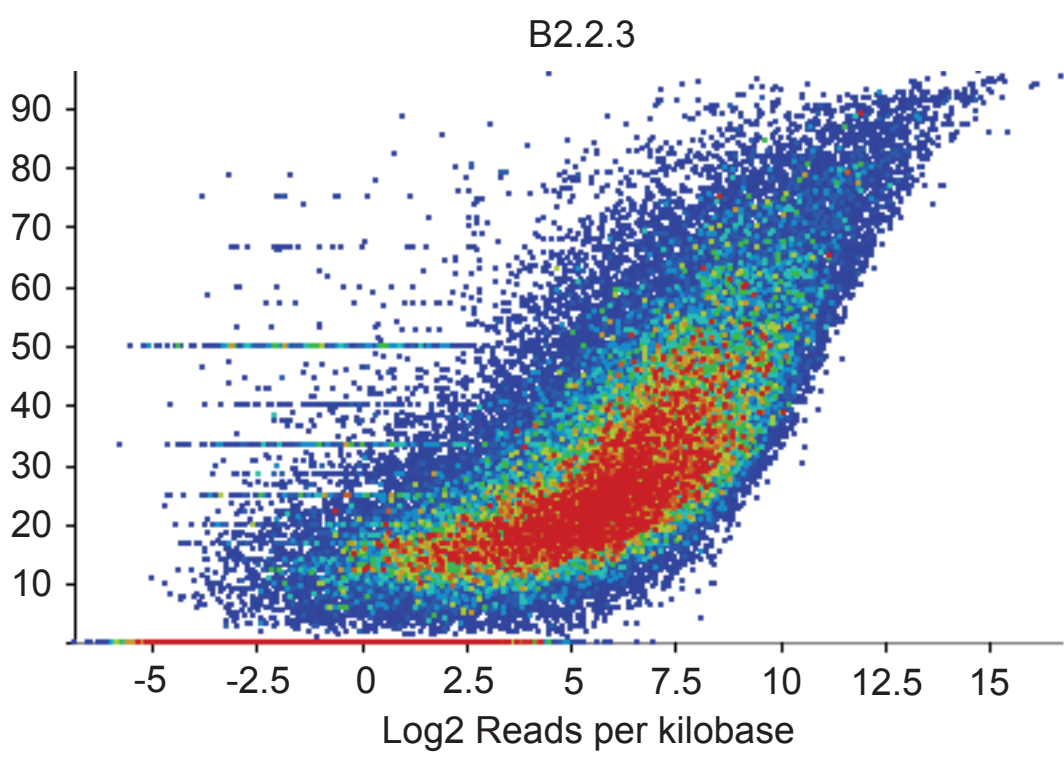
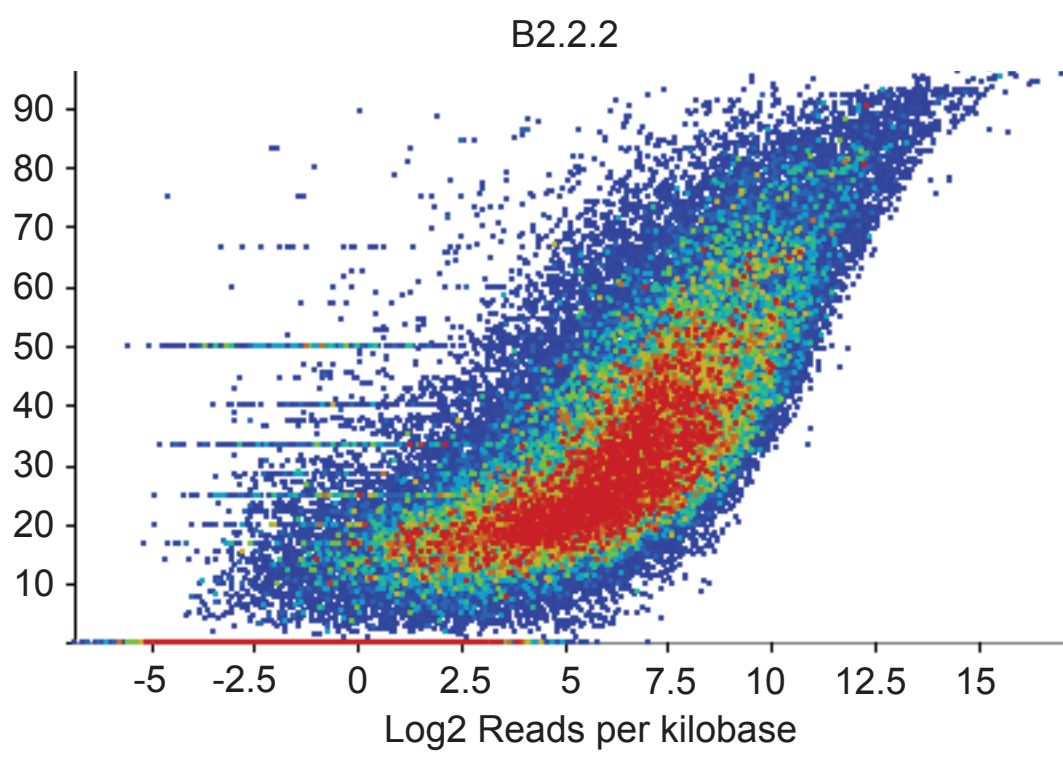
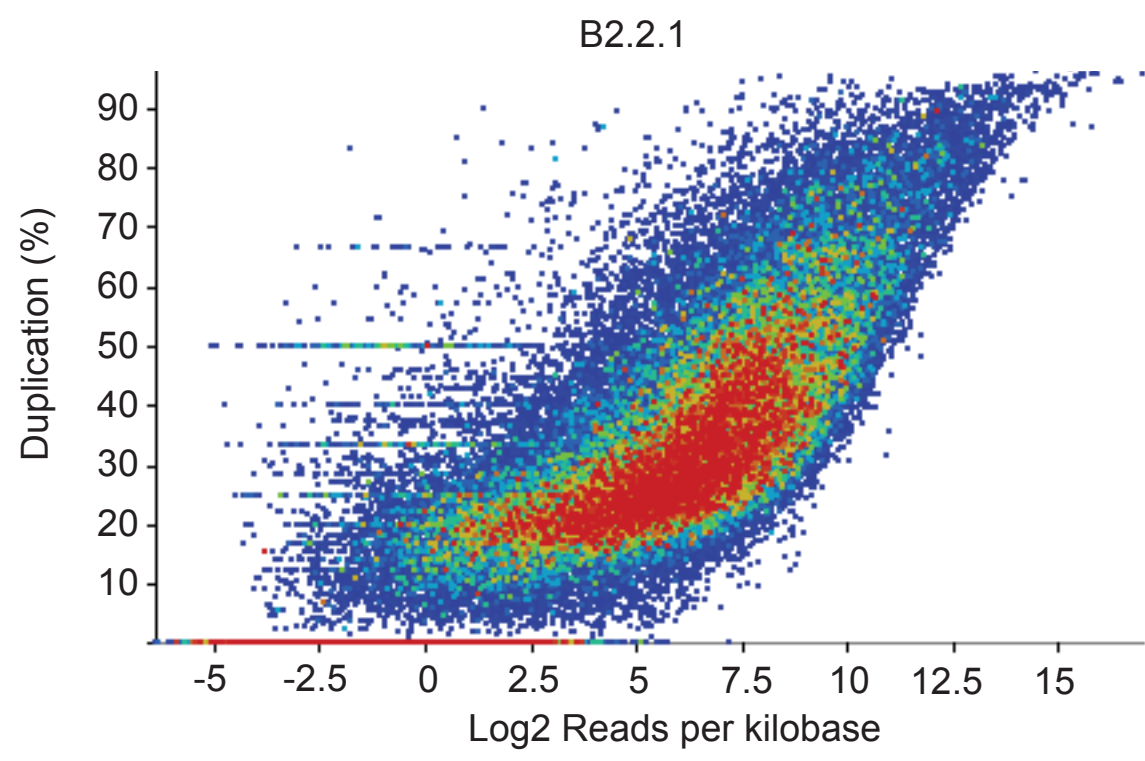
Deprived C L4



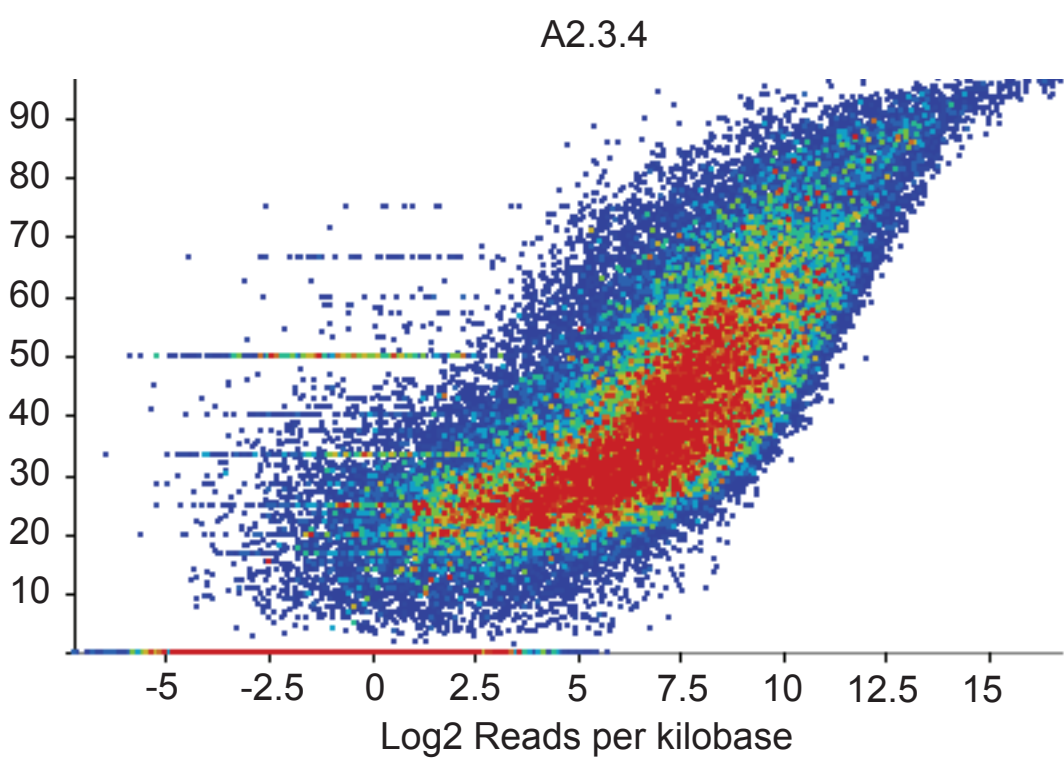
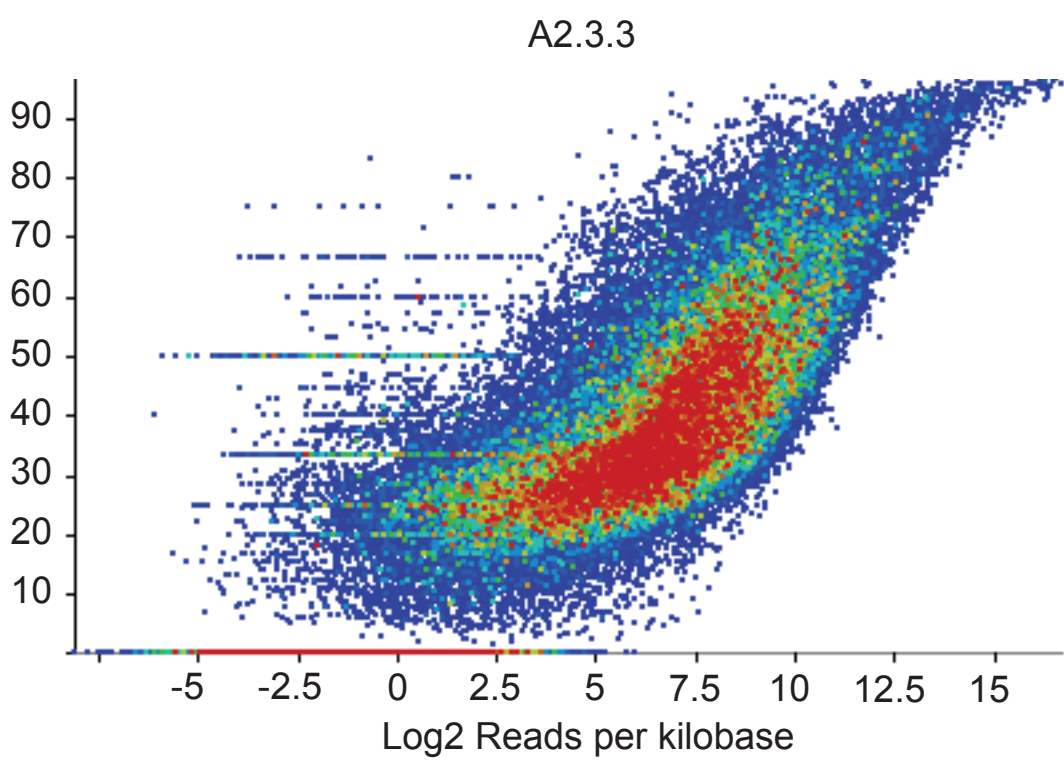
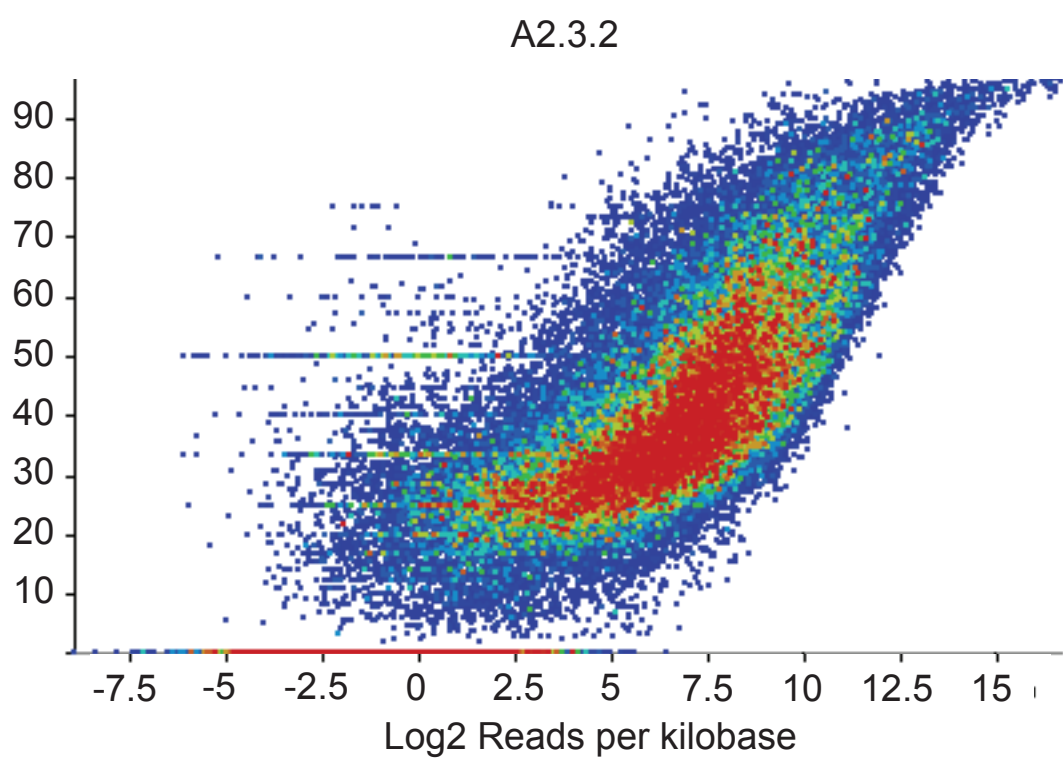
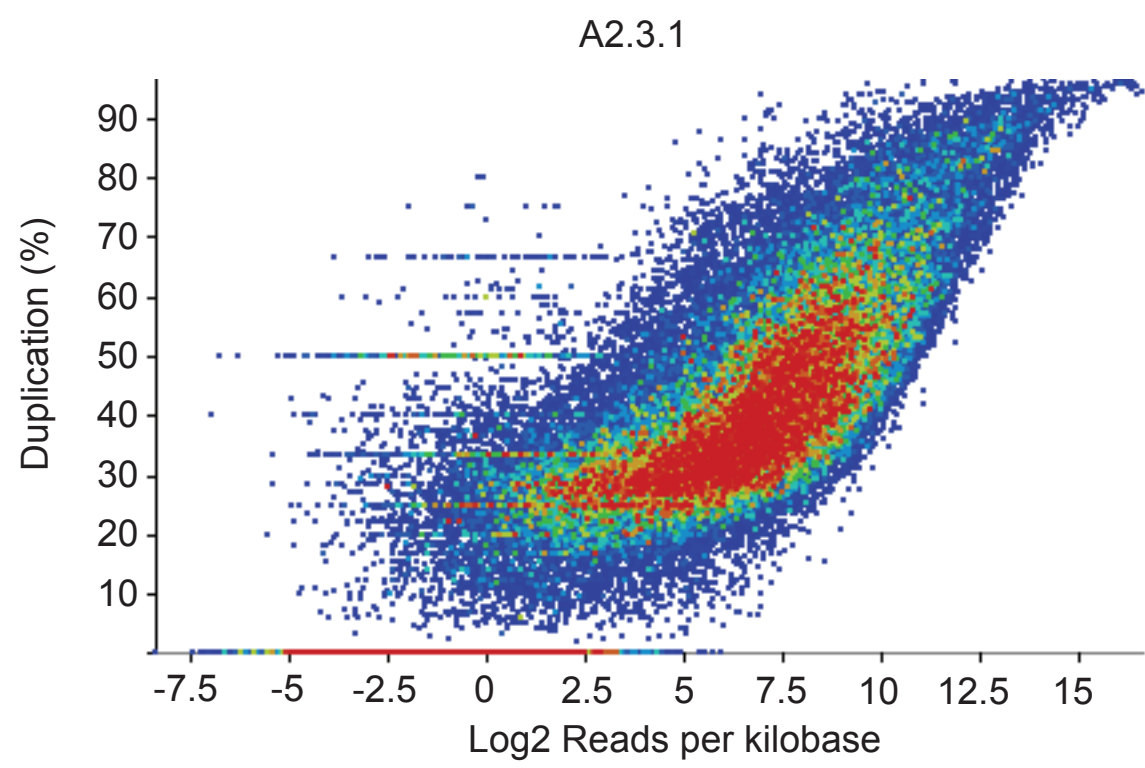
Spared B/D L2/3



Spared B/D L4



Spared A/E L2/3



Spared A/E L4

