

CPU_Mux2_17Bit_21363904



The timing diagram for the 17Bit Multiplexer is shown above. All numbers are in Hexadecimal notation.

The **red lines** mark when the value of the select changes, and the **blue lines** mark when the corresponding output appears after a 5ns propagation delay. When the select is 0, in0 is selected, and when the select is 1, in1 is selected. Here the values of in0 and in1 are 0x6 and 0x9 respectively.