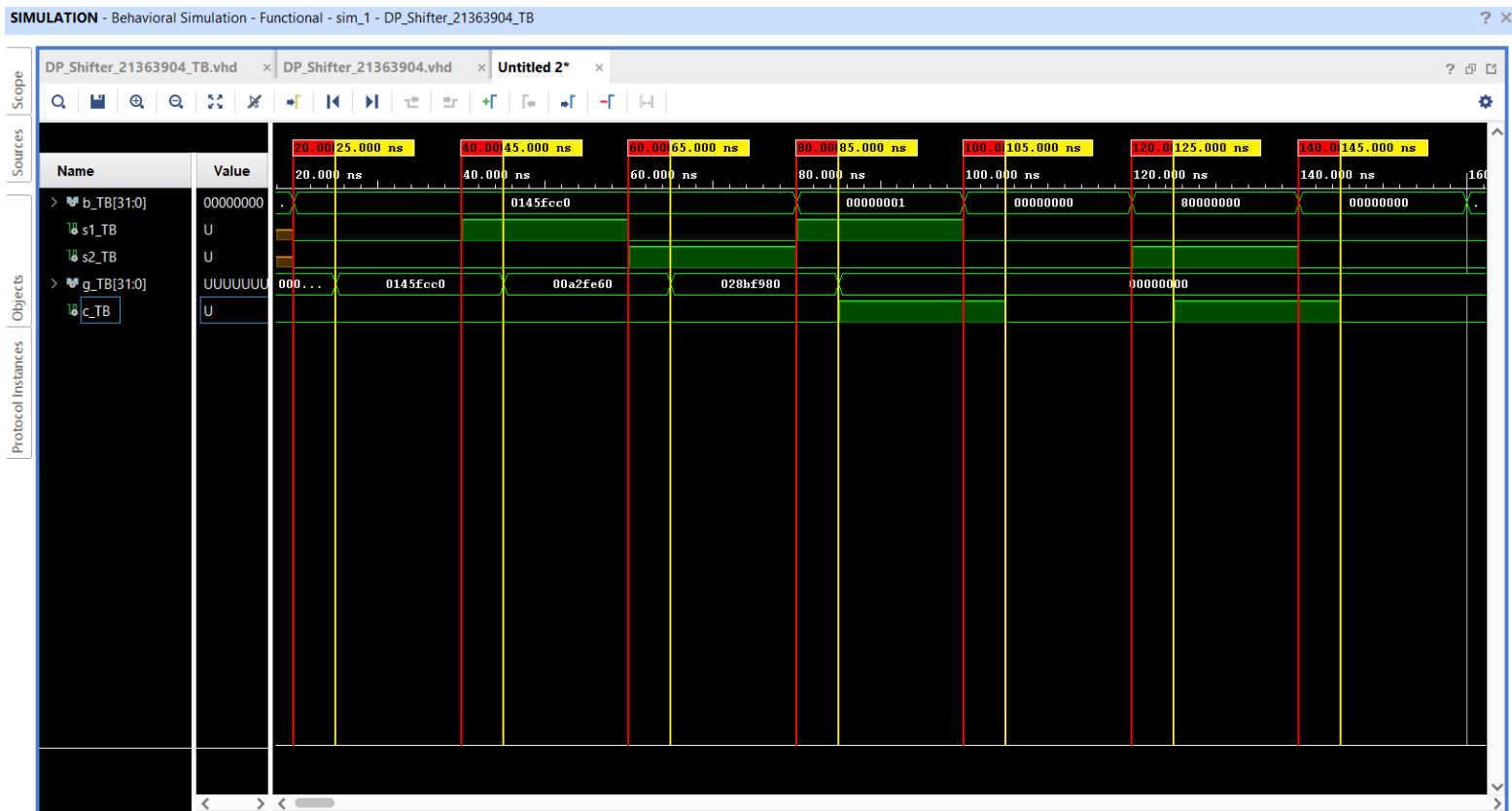


# DP\_Shifter\_21363904



The timing diagram for the Shifter is shown above. All numbers are in Hexadecimal notation. The **red lines** mark when the select inputs change, and the **yellow lines** indicate when the correct value of each operation appears at the output. My Student ID (0145FCC0) is provided to the input *B*, and is shifted right 1 bit when *s1* is 1, and shifted left 1 bit when *s2* is 1, otherwise no operation is performed. The last four tests demonstrate the shifting operations setting and unsetting the carry flag (shift right, then 0, followed by shift left, then 0).