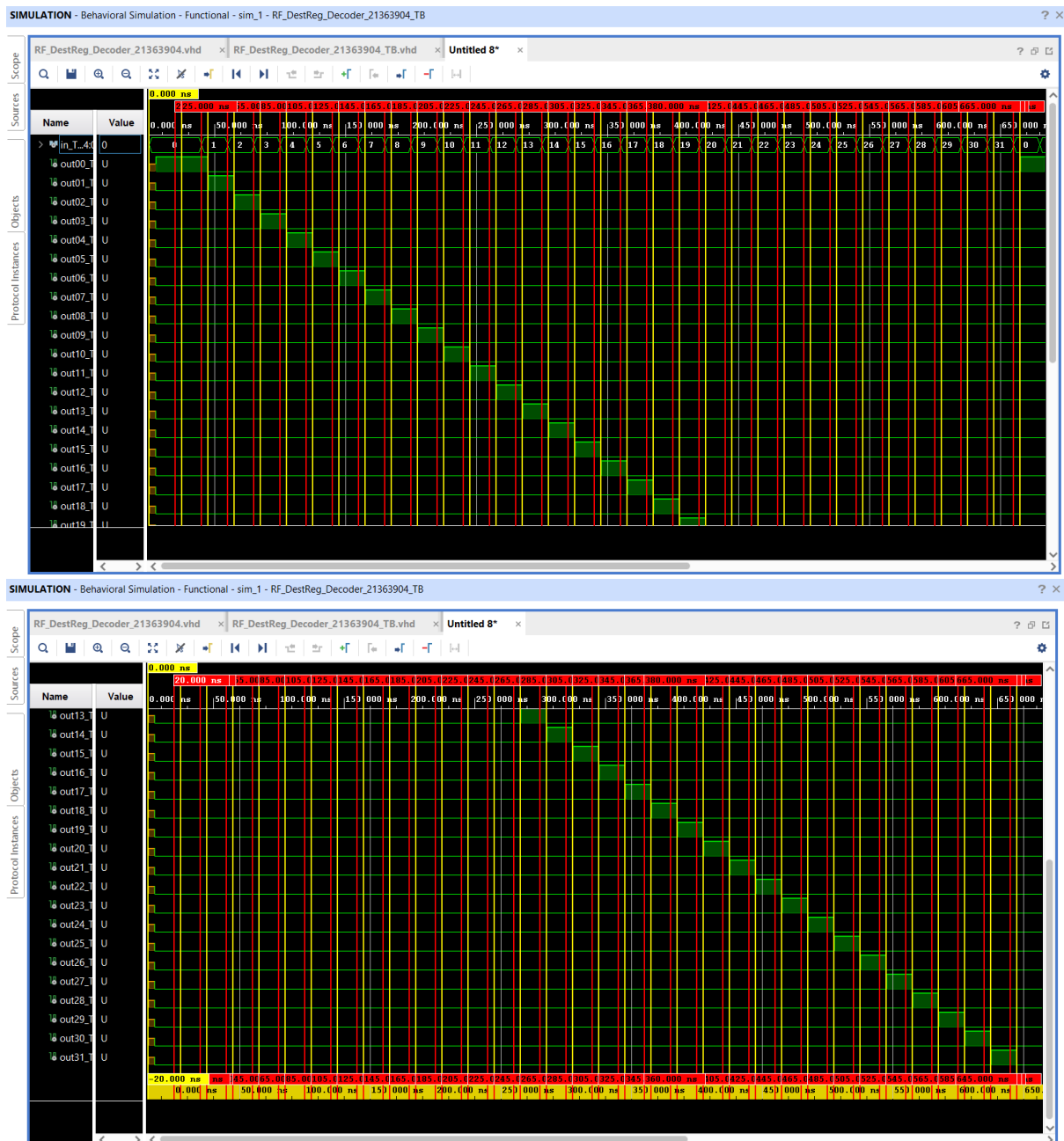# RF_DestReg_Decoder_21363904

The timing diagrams for the Destination Register Decoder are shown above. The first image shows the input and the first 20 output signals, while the second shows the remaining output signals. The red lines mark where the input value changes, and the yellow lines show when the correct signal appears at the output. The distance between the red and yellow lines is the propagation delay (5ns in this case).