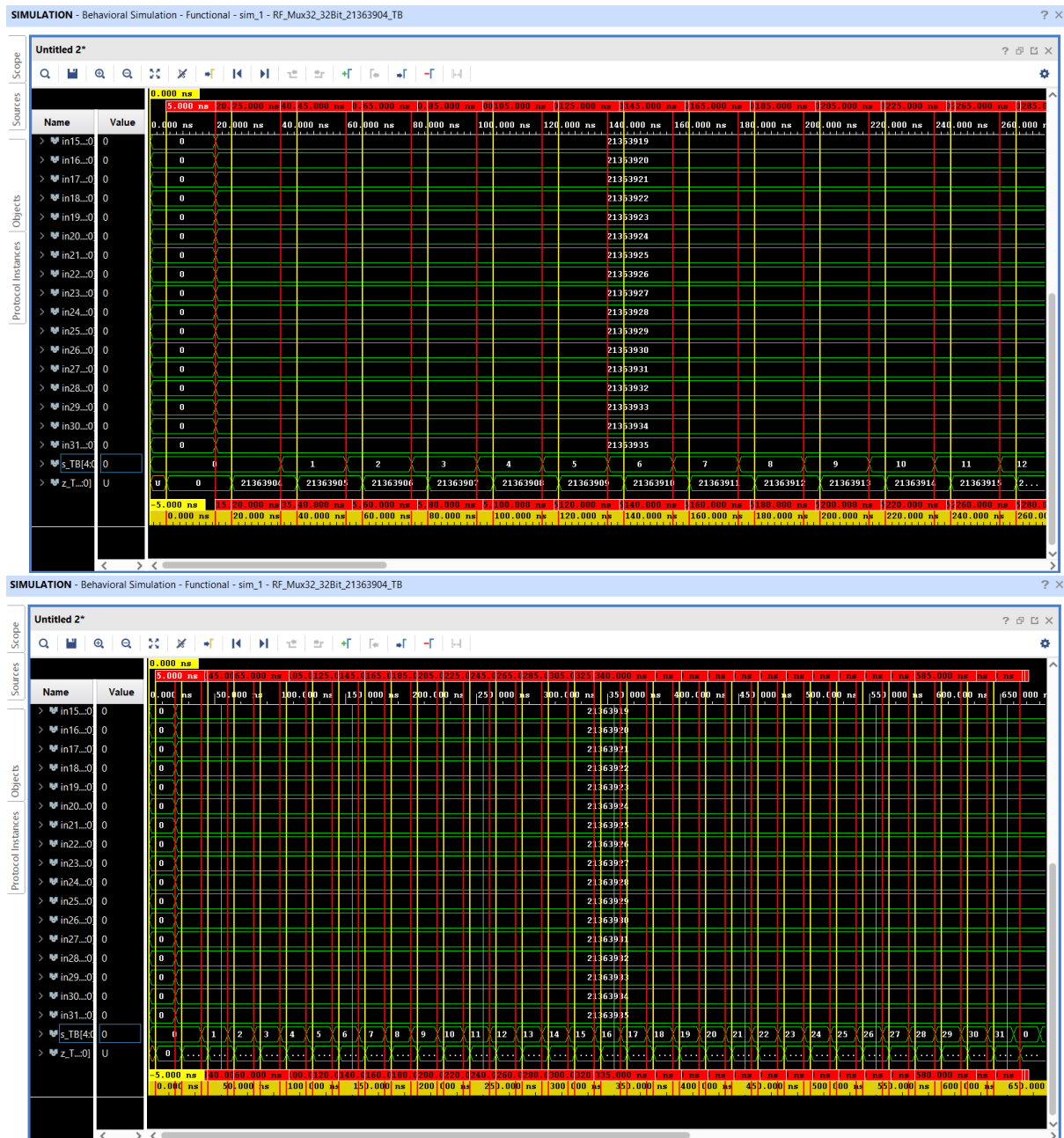


# RF\_Mux32\_32Bit\_21363904



The timing diagrams for the 32Bit Multiplexer are shown above. The first image is zoomed in on the first 13 output values in order to confirm they are correct; the second image is a zoomed-out version of every test case. The **red lines** mark where the 5Bit select value changes and the **yellow lines** show when the correct value appears at the output. The distance between the red and yellow lines is the propagation delay (5ns in this case).