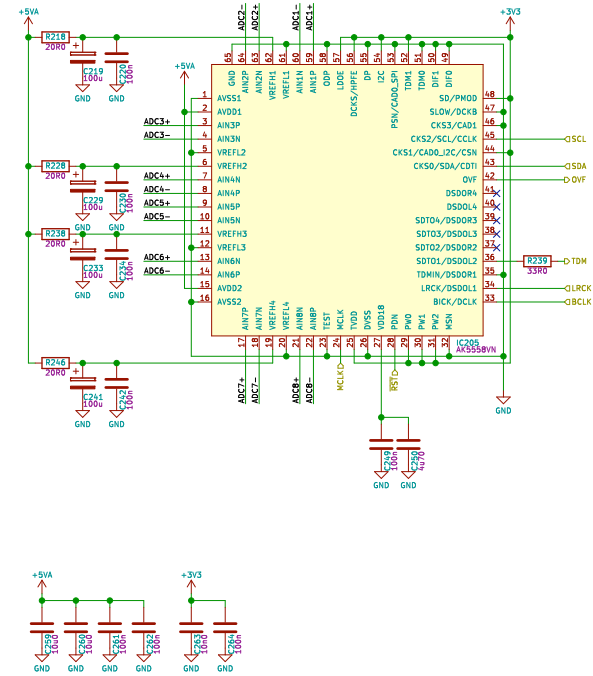
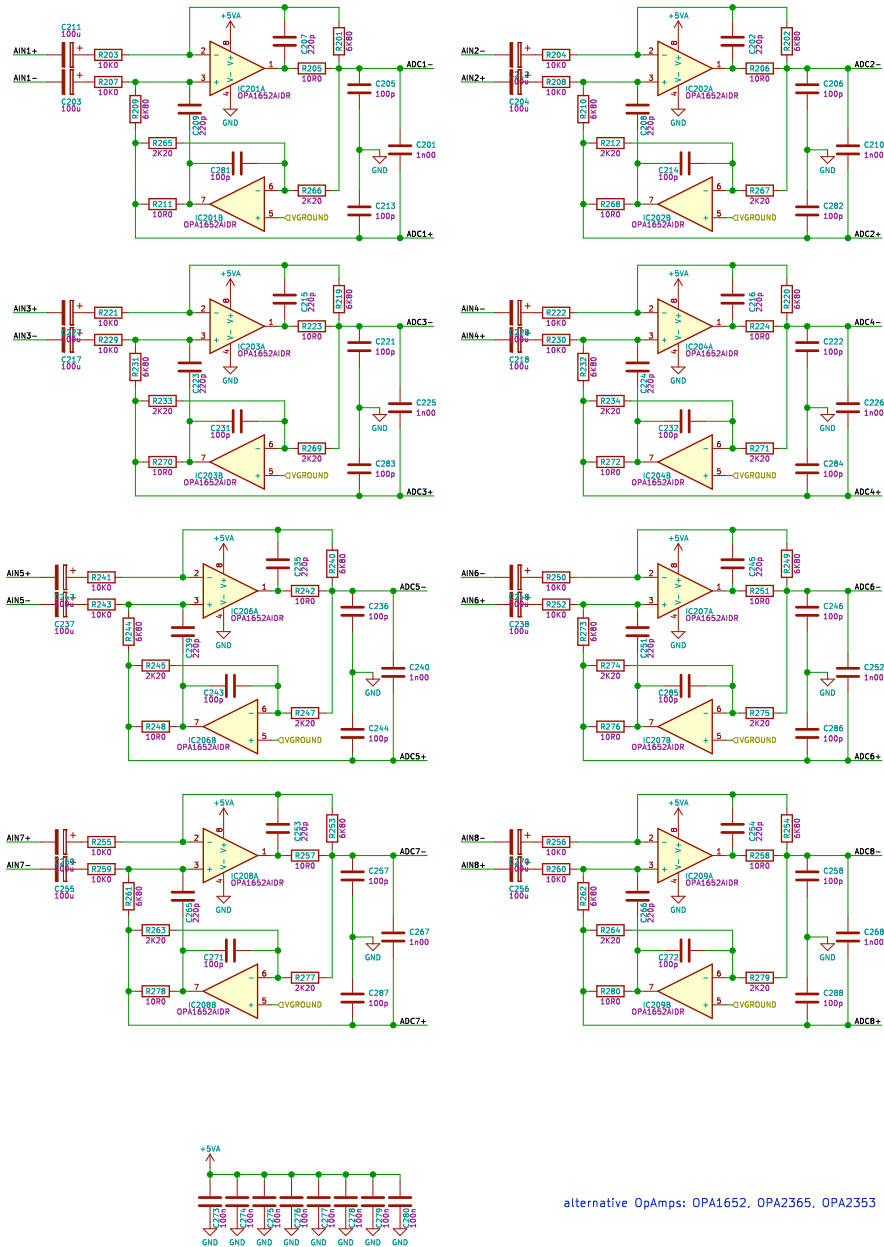
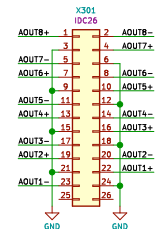
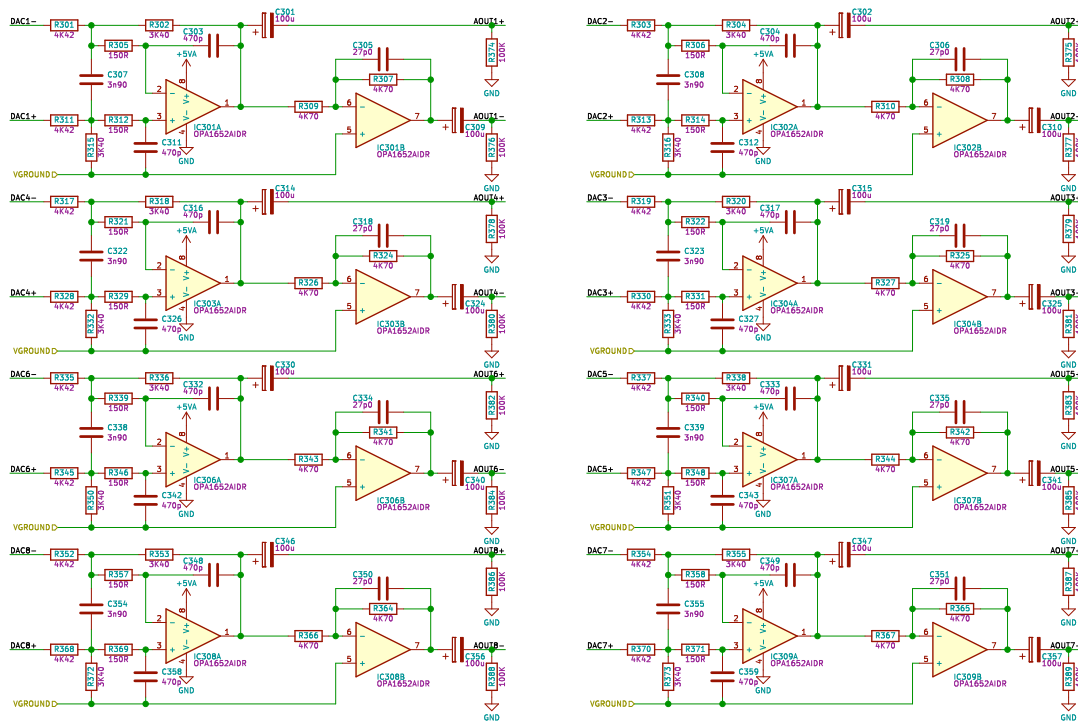
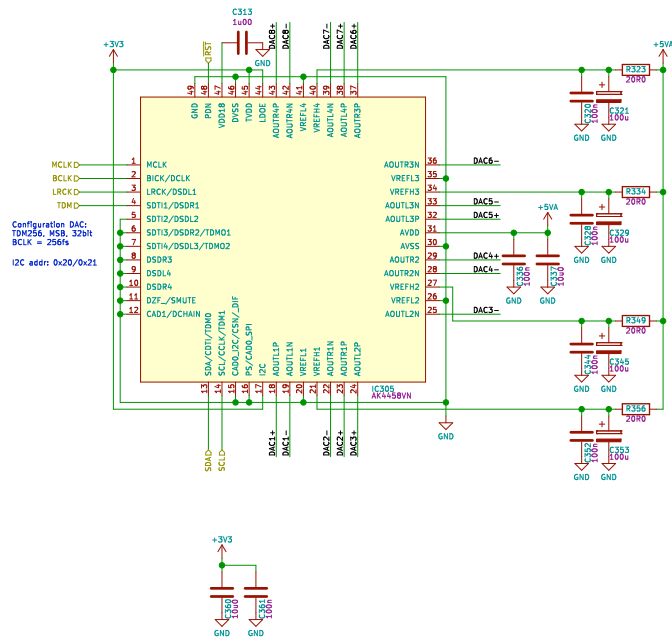


RFI auf Anschlussplatine

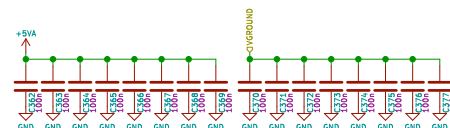


Configuration ADC:
 Slave- PCM, TDM256, MSB, 32bit,
 short delay, sharp rolloff,
 high pass enabled
 MCLK = 24.576Mhz, S12Hz
 I2C addr: 0x22/0x23



RFI auf Anschlussplatte

alternative OpAmps: OPA1652, OPA2365, OPA2353



auverdlon

Sheet: /DA-Wandler/

File: da.sch

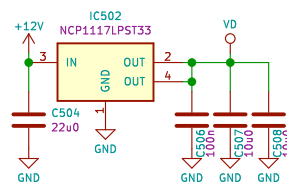
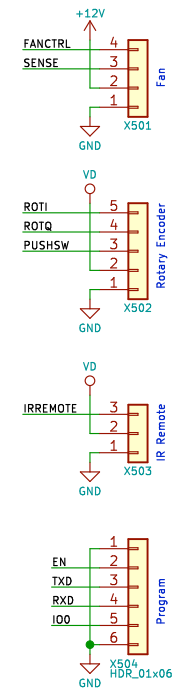
Title: freeDSP Aurora

Size: A3 Date: 24.7.2018

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