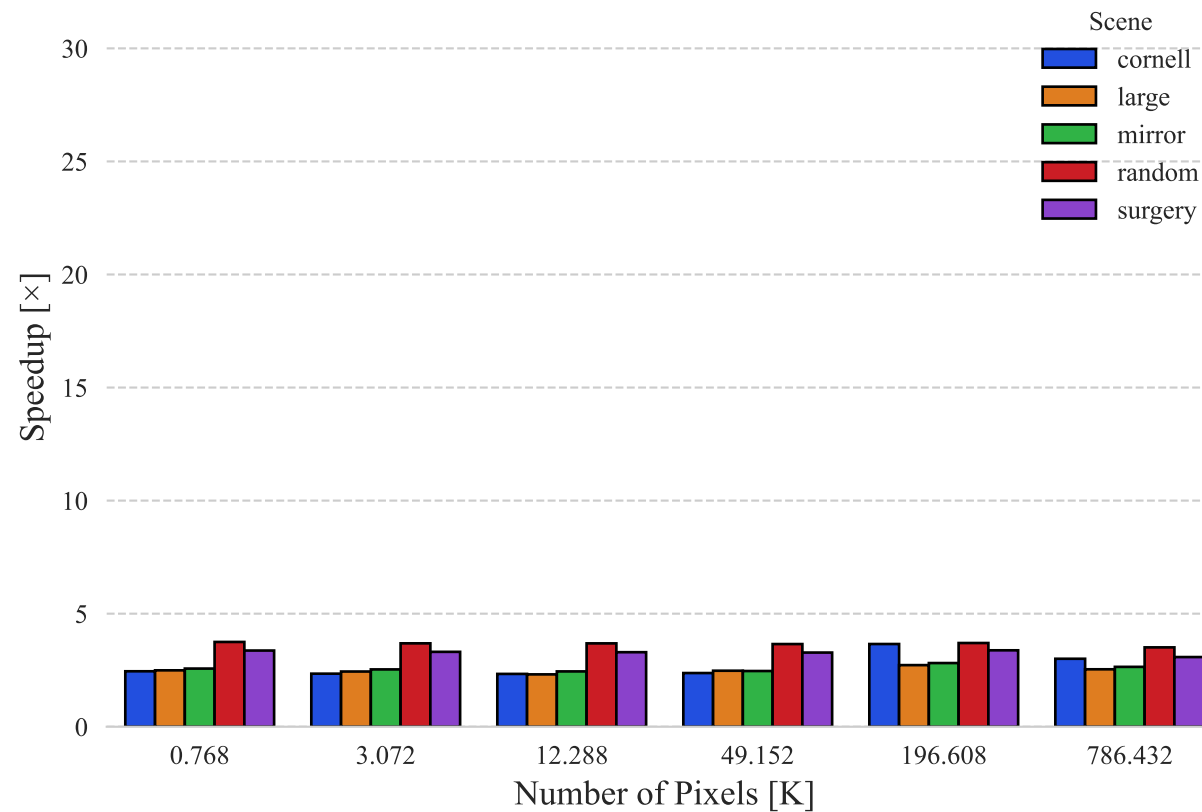


Sequential



SIMD

