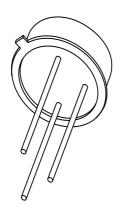
DISCRETE SEMICONDUCTORS

DATA SHEET



Jameco Part Number 38236 www.jameco.com 1-800-831-4242

2N2222; 2N2222A NPN switching transistors

Product specification Supersedes data of September 1994 File under Discrete Semiconductors, SC04 1997 May 29





NPN switching transistors

2N2222; 2N2222A

FEATURES

• High current (max. 800 mA)

• Low voltage (max. 40 V).

APPLICATIONS

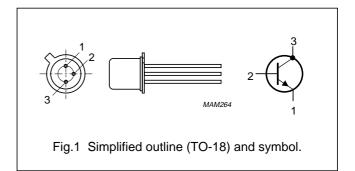
• Linear amplification and switching.

DESCRIPTION

NPN switching transistor in a TO-18 metal package. PNP complement: 2N2907A.

PINNING

PIN	DESCRIPTION		
1	emitter		
2	base		
3	collector, connected to case		



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	2N2222		_	60	V
	2N2222A		_	75	V
V _{CEO}	collector-emitter voltage	open base			
	2N2222		_	30	V
	2N2222A		_	40	V
I _C	collector current (DC)		_	800	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	500	mW
h _{FE}	DC current gain	I _C = 10 mA; V _{CE} = 10 V	75	_	
f _T	transition frequency	I _C = 20 mA; V _{CE} = 20 V; f = 100 MHz			
	2N2222		250	_	MHz
	2N2222A		300	_	MHz
t _{off}	turn-off time	$I_{Con} = 150 \text{ mA}; I_{Bon} = 15 \text{ mA}; I_{Boff} = -15 \text{ mA}$	_	250	ns

NPN switching transistors

2N2222; 2N2222A

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	2N2222		_	60	V
	2N2222A		_	75	V
V _{CEO}	collector-emitter voltage	open base			
	2N2222		_	30	V
	2N2222A		_	40	V
V _{EBO}	emitter-base voltage	open collector			
	2N2222		_	5	V
	2N2222A		_	6	V
I _C	collector current (DC)		_	800	mA
I _{CM}	peak collector current		_	800	mA
I _{BM}	peak base current		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	500	mW
		T _{case} ≤ 25 °C	_	1.2	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	200	°C
T _{amb}	operating ambient temperature		-65	+150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	in free air	350	K/W
R _{th j-c}	thermal resistance from junction to case		146	K/W

NPN switching transistors

2N2222; 2N2222A

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL PARAMETER		CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current				
	2N2222	I _E = 0; V _{CB} = 50 V	_	10	nA
		I _E = 0; V _{CB} = 50 V; T _{amb} = 150 °C	_	10	μΑ
I _{CBO}	collector cut-off current				
	2N2222A	$I_E = 0; V_{CB} = 60 \text{ V}$	_	10	nA
		I _E = 0; V _{CB} = 60 V; T _{amb} = 150 °C	_	10	μΑ
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = 3 V$	_	10	nA
h _{FE}	DC current gain	$I_C = 0.1 \text{ mA}; V_{CE} = 10 \text{ V}$	35	_	
		I _C = 1 mA; V _{CE} = 10 V	50	_	
		$I_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}$	75	_	
		I _C = 150 mA; V _{CE} = 1 V; note 1	50	_	
		I _C = 150 mA; V _{CE} = 10 V; note 1	100	300	
h _{FE}	DC current gain	$I_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}; T_{amb} = -55 ^{\circ}\text{C}$			
	2N2222A		35	_	
h _{FE}	DC current gain	$I_C = 500 \text{ mA}; V_{CE} = 10 \text{ V}; \text{ note 1}$			
	2N2222		30	_	
	2N2222A		40	_	
V_{CEsat}	collector-emitter saturation voltage				
	2N2222	$I_C = 150 \text{ mA}; I_B = 15 \text{ mA}; \text{ note 1}$	_	400	mV
		$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}; \text{ note 1}$	_	1.6	V
V_{CEsat}	collector-emitter saturation voltage				
	2N2222A	$I_C = 150 \text{ mA}$; $I_B = 15 \text{ mA}$; note 1		300	mV
		$I_C = 500 \text{ mA}$; $I_B = 50 \text{ mA}$; note 1	_	1	V
V_{BEsat}	base-emitter saturation voltage				
	2N2222	$I_C = 150 \text{ mA}$; $I_B = 15 \text{ mA}$; note 1		1.3	V
		$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}; \text{ note 1}$	_	2.6	V
V_{BEsat}	base-emitter saturation voltage				
	2N2222A	$I_C = 150 \text{ mA}; I_B = 15 \text{ mA}; \text{ note 1}$	0.6	1.2	V
		$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}; \text{ note 1}$	_	2	V
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 10 \text{ V}$; $f = 1 \text{ MHz}$	_	8	pF
C _e	emitter capacitance	$I_C = I_c = 0$; $V_{EB} = 500 \text{ mV}$; $f = 1 \text{ MHz}$			
	2N2222A			25	pF
f_{T}	transition frequency	$I_C = 20 \text{ mA}; V_{CE} = 20 \text{ V}; f = 100 \text{ MHz}$			
	2N2222		250	-	MHz
	2N2222A		300	-	MHz
F	noise figure	$I_C = 200 \mu\text{A}; V_{CE} = 5 V; R_S = 2 k\Omega;$			
	2N2222A	f = 1 kHz; B = 200 Hz	_	4	dB

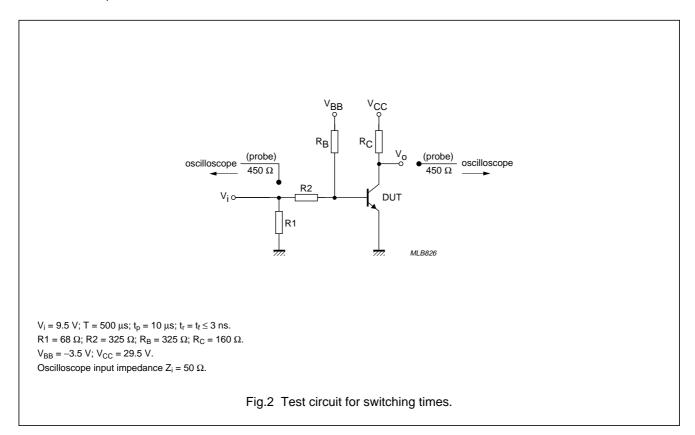
NPN switching transistors

2N2222; 2N2222A

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT			
Switching	Switching times (between 10% and 90% levels); see Fig.2							
t _{on}	turn-on time	$I_{Con} = 150 \text{ mA}; I_{Bon} = 15 \text{ mA}; I_{Boff} = -15 \text{ mA}$	_	35	ns			
t _d	delay time		_	10	ns			
t _r	rise time		_	25	ns			
t _{off}	turn-off time		_	250	ns			
t _s	storage time		_	200	ns			
t _f	fall time		_	60	ns			

Note

1. Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02.$



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1997 May 29

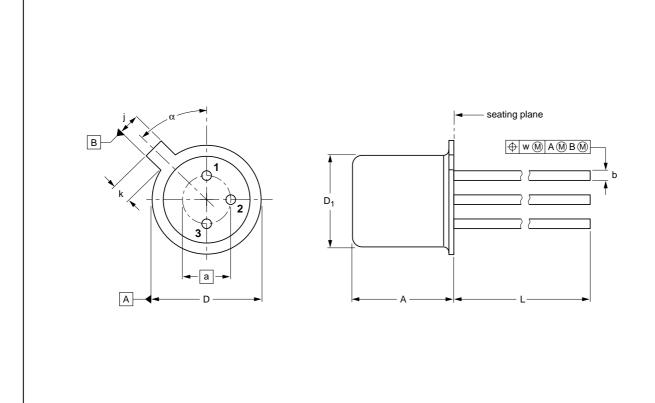
NPN switching transistors

2N2222; 2N2222A

PACKAGE OUTLINE

Metal-can cylindrical single-ended package; 3 leads

SOT18/13



DIMENSIONS (millimetre dimensions are derived from the original inch dimensions)

UNIT	A	а	b	D	D ₁	j	k	L	w	α
mm	5.31 4.74	2.54	0.47 0.41	5.45 5.30		1.03 0.94	1.1 0.9	15.0 12.7	0.40	45°

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT18/13	B11/C7 type 3	TO-18				97-04-18

NPN switching transistors

2N2222; 2N2222A

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

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