

# 1 Information

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- Title: The Legend of Bluespec: A Link to the components
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## 2 Project completeness

### 2.1 Is the project on schedule and if not, how many weeks behind or ahead?

Generally project is on track, it's almost exactly on schedule. Core of the project is working. There is still a bit of code debt in from of lack of automated testing. It's worth mentioning that the timeline starts at 15.10.2021, but I started working on the project at the end of October, because I was waiting for the confirmation. This means that I'm effectively about a week ahead of schedule, if we account for this shift of timeline.

### 2.2 What unexpected difficulties have arisen?

The biggest such difficulty is complexity of Bluespec type system, because functions are first class objects and can be passed around as values, and a lot of polymorphism. This makes quite difficult to do things in precise manner. This will mean that my project is not be able to check for type correctness of passed arguments, while initializing some objects. Fixing this would require parsing raw Bluespec code which might be beyond the scope of this project, for more complicated constructs, if user will choose to do unsupported things, checking of theirs correctness will be simply delayed until compilation.

### 2.3 If the project is behind, what actions have been taken to address this and when will progress be back on track?

Project is not behind, but because I have a lot of time to spare till the final deadline (roughly 3-4 months). I plan to finish evaluation do rough draft of the dissertation and start working on optional goal.

### 2.4 Briefly, what has been accomplished?

- Communication with bluetcl (bluetcl is the tool to extract data (functions/types) from packages) works.
- Most of bluetcl output for types and functions is parsable and converted to python classes. (What is lacking is a large scale test, and support for structs)
- Synthesizing of Bluespec code works, and supports all features for building top-level file like:
  - Adding packages
  - Adding typedefs
  - Initializing components
  - Adding direct connections
  - Adding connections via busses (which special treatment for AXI4 busses).
  - Creating routing functions for busses
- Parsing of description of top-level module in from of JSON works and generation of code from it works.