



Using the clock  
to trigger an interrupt

I/O





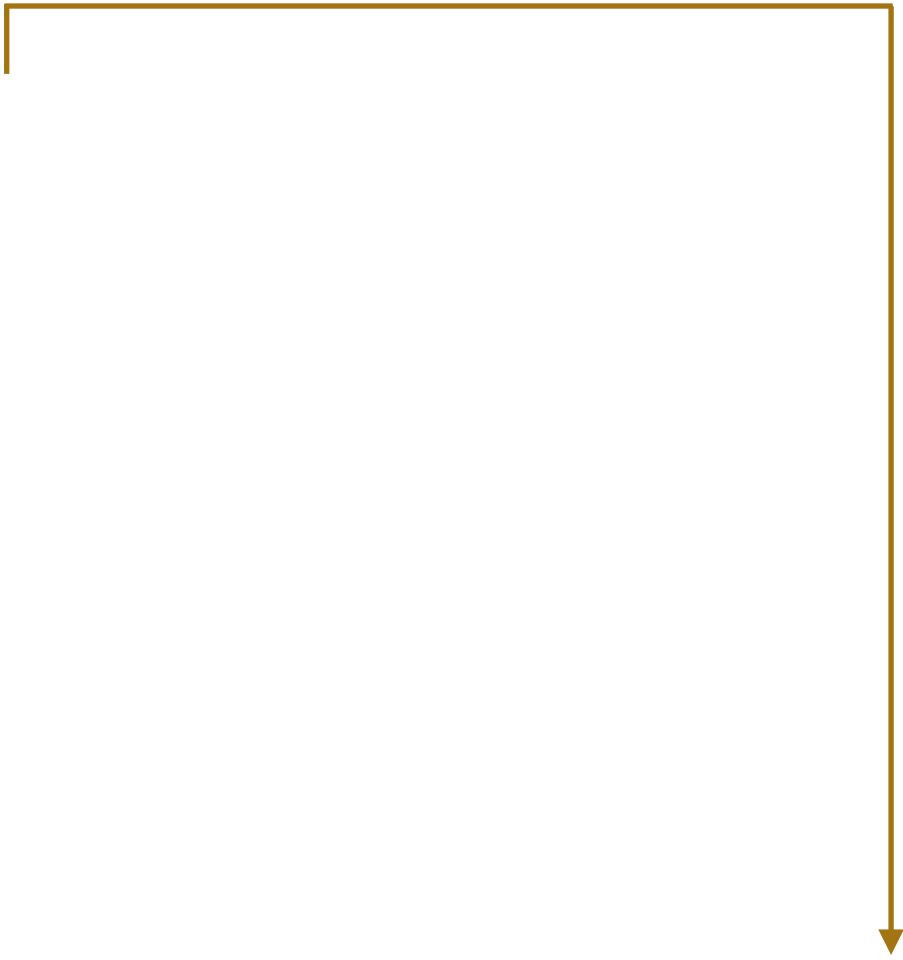




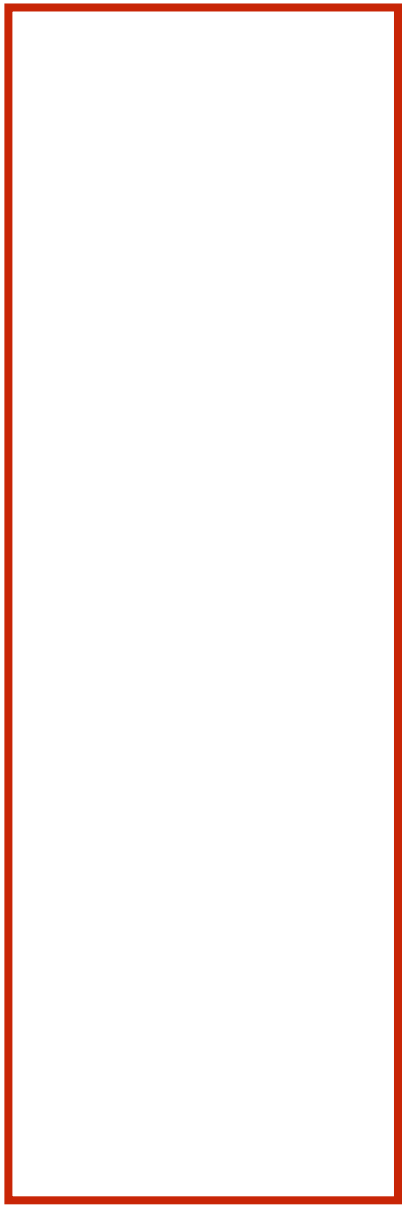


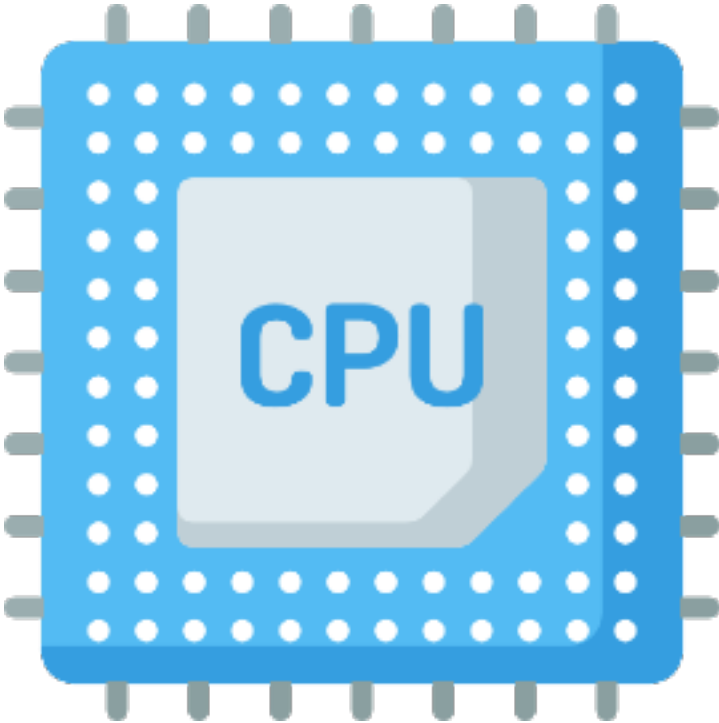


Boot



Interrupt







esp



eip



prog A

stack A



heap A



prog B

stack B

heap B

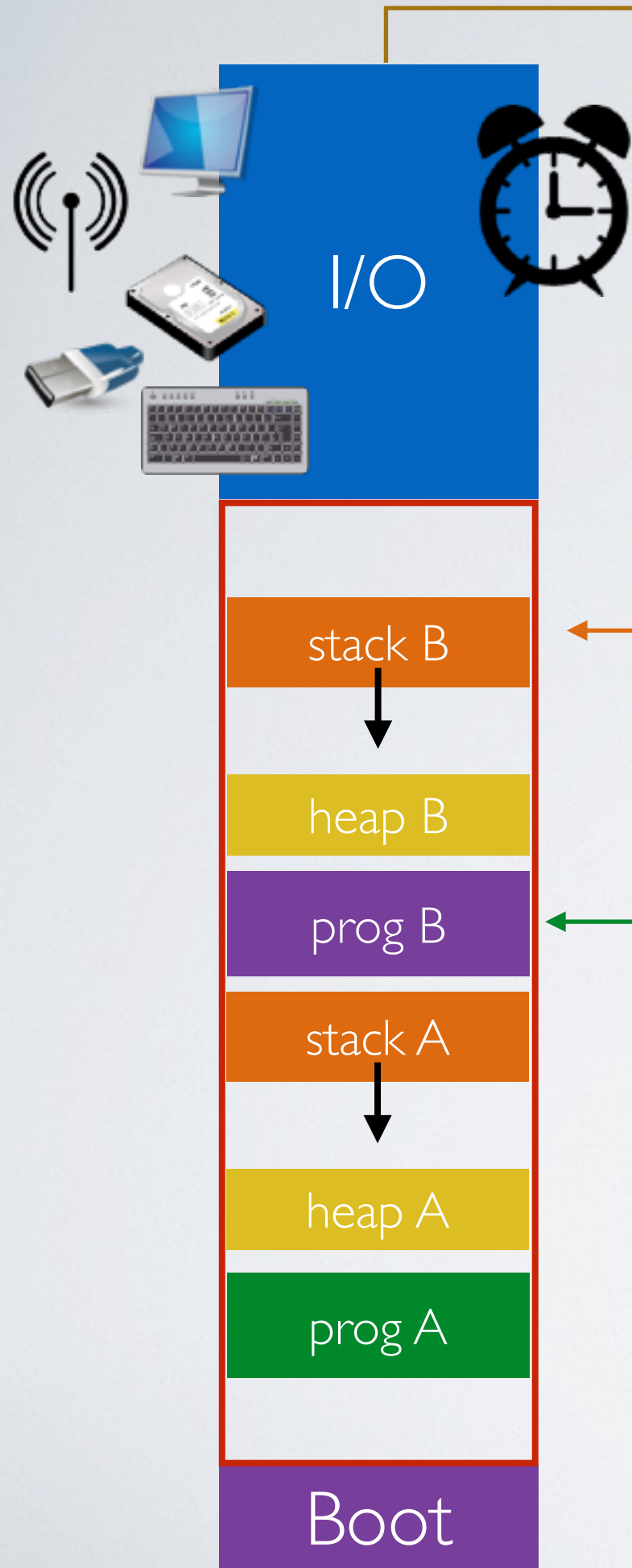


esp



The diagram illustrates the state of two registers: the stack pointer (esp) and the instruction pointer (eip). The esp register is shown as an orange arrow pointing to the left, with its label 'esp' positioned above it. The eip register is shown as a green arrow pointing to the left, with its label 'eip' positioned below it. The esp arrow is positioned above the eip arrow, and both arrows are horizontal, indicating their current values or directions of movement.

eip



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Interrupt

esp

eip

CPU



# Program States

created