



Achieving parallelism with  
multi-core processors

I/O





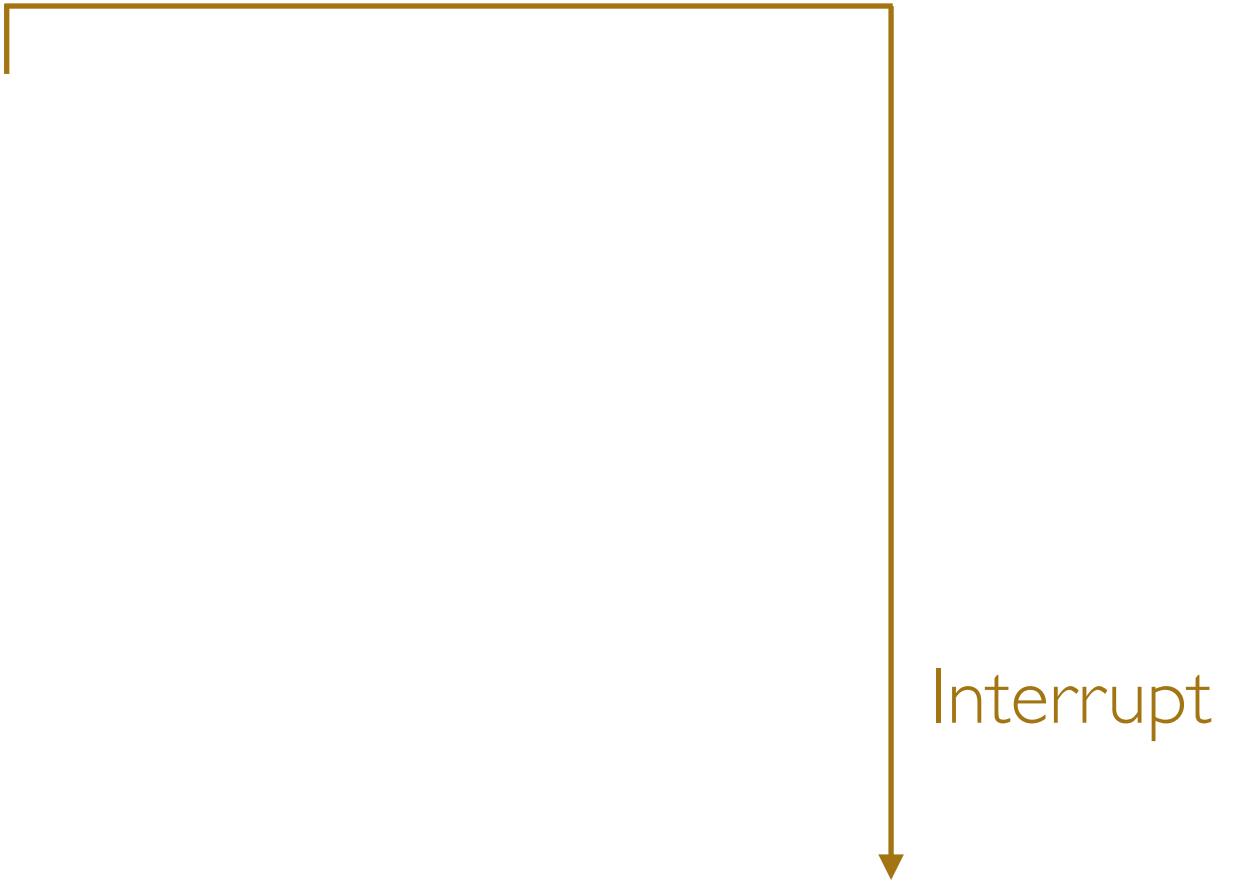


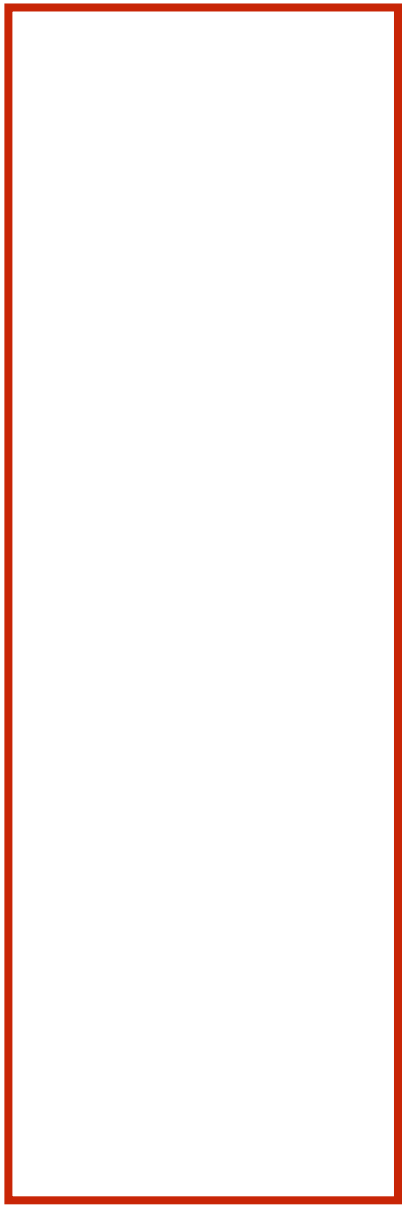






Boot





code (text)

stack

heap

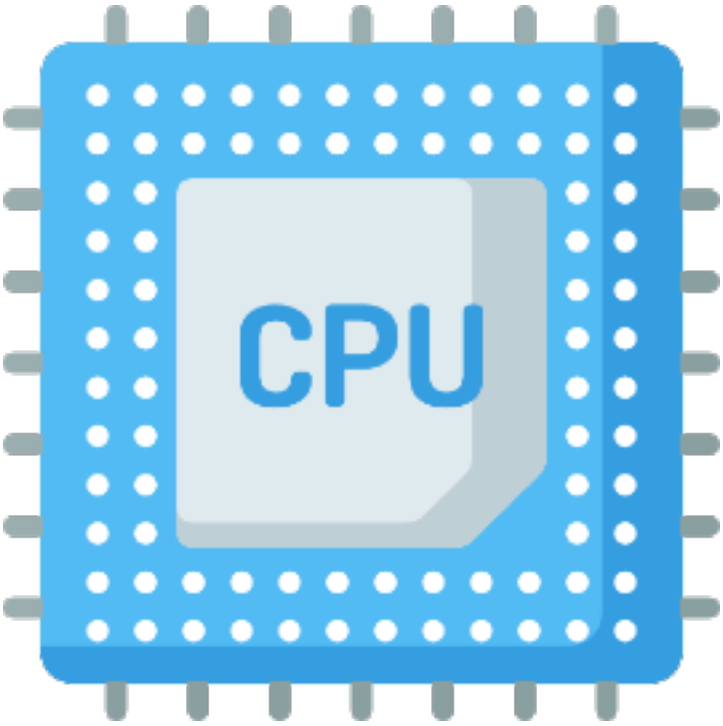
heap













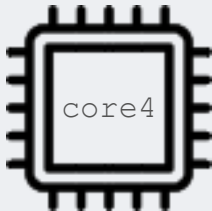
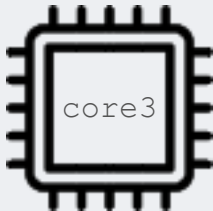
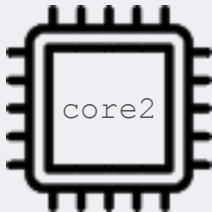
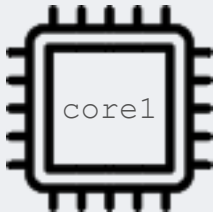


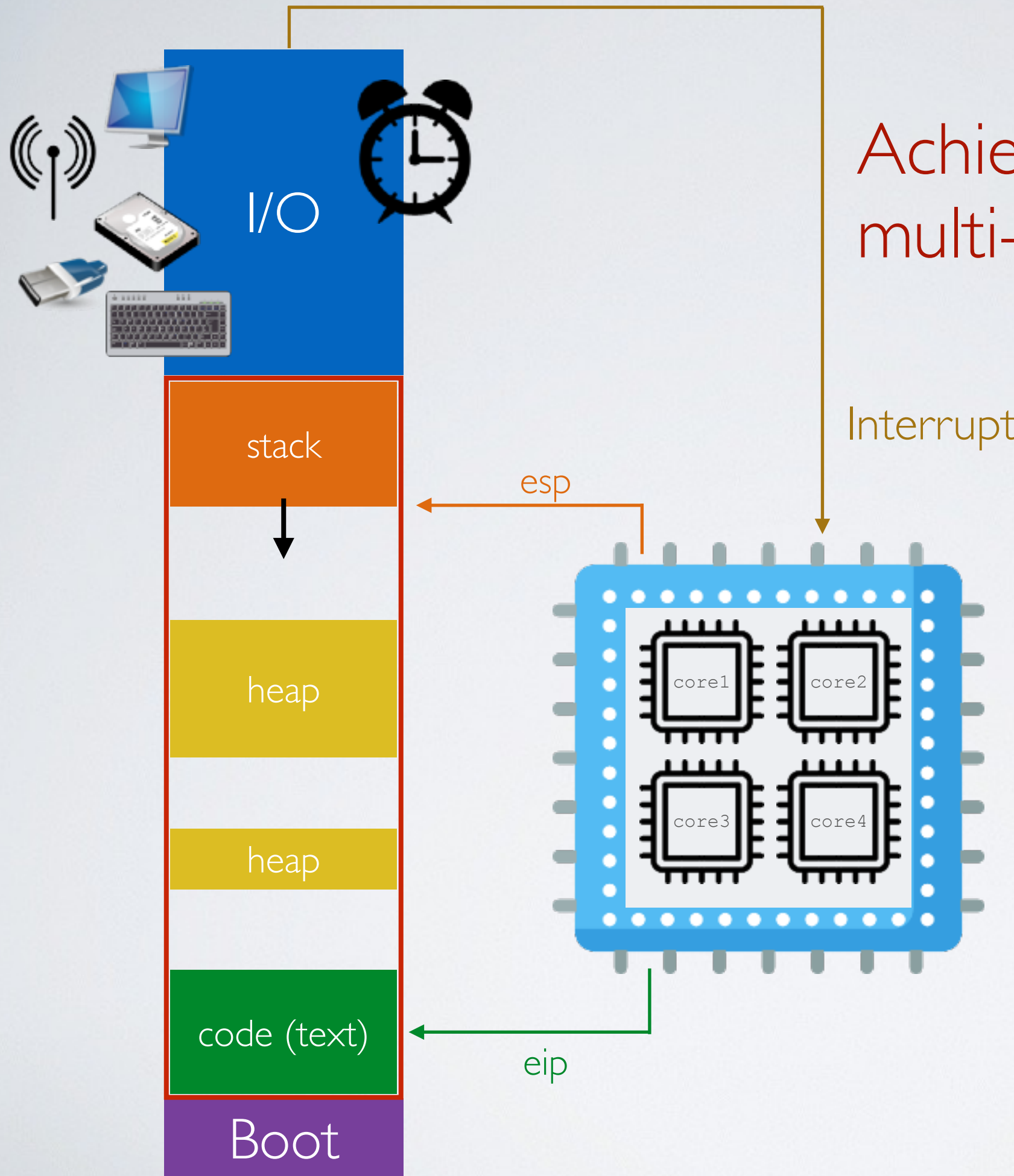
ESP











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The need for **user programs**