

# VALIDATION TEST OF INTERCONNECT IO BOX

#	Test	Stimulus	Measure	Action	Circuits	Signals Validation	Limits	Comments
1.0	ADCO	SV_PWR	ADCO	Open all relay (Default)	Current Module INA219	SV_PWR.ADC0	SV +/- 0.3V (ADCO = 2.5V +/- 0.2V)	Verify SV and ADC0. Check if led ON on Board to validate SV
2.0	Digital PORTS Test 0-dk55	PORT0 Output	PORT0 Input	Open all relay (Default)	Buffer	PORT0 PORT1	Set Port0 = 0x55, Read Port 0 = 0x55	Validate Port 0 and Port1
3.0	Digital PORTS Test 1-dk54	PORT1 Output	PORT1 Input	Open all relay (Default)		PORT0 PORT1	Set Port1 = 0x55, Read Port 1 = 0x54	Validate Port 0 and Port1
4.0	Digital PORTS Test 1-dk55	PORT1 Output	PORT1 Input	Open all relay (Default)		PORT0 PORT1	Set Port1 = 0x55, Read Port 0 = 0x55	Validate Port 0 and Port1
5.0	Digital PORTS Test 1-dkAA	PORT1 Output	PORT1 Input	Open all relay (Default)		PORT0 PORT1	Set Port1 = 0x55, Read Port 0 = 0x55	Validate Port 0 and Port1
6.0	Digital Handshake Test 1	TRIG output	TRIG output	Open all relay (Default)		TRIG_FLAG	Set TRIG = 1, Read TRIG = 1	Validate state of the signals
7.0	Digital Handshake Test 0	TRIG output	TRIG output	Open all relay (Default)		TRIG_FLAG	Set TRIG = 0, Read TRIG = 0	Validate state of the signals
8.0	Digital Signal Test 8-dk45	I3, I0R.S1, I0R.M1, I0R.M1, I0R Output	Pico	Open all relay (Default)		I3, I0R.S1, I0R.M1, I0R.M1, I0R	Set Signal to 0x55, Read Pico = 0x55	Validate independent signal
9.0	Digital Signal Test 8-dk46	I3, I0R.S1, I0R.M1, I0R.M1, I0R Output	Pico	Open all relay (Default)		I3, I0R.S1, I0R.M1, I0R.M1, I0R	Set Signal to 0x55, Read Pico = 0x55	Validate independent signal
10.0	Open Collector OC1 Close Test	SCPI command	ADCO	Close K16 (VM2), Drive OC1 - High		OC1_OUT	Read 0.2V +/- 0.2V	Open collector transistor activated
11.0	Open Collector OC1 Open Test	SCPI command	ADCO	Close K16 (VM2), Drive OC1 - Low		OC1_OUT	Read 5V +/-0.0-0.4V	Open collector transistor not activated
12.0	Open Collector OC2 Close Test	SCPI command	ADCO	Close K15 (VM3), Drive OC2 - High		OC2_OUT	Read 0.2V +/- 0.2V	Open collector transistor activated
13.0	Open Collector OC2 Open Test	SCPI command	ADCO	Close K15 (VM3), Drive OC2 - Low		OC2_OUT	Read 5V +/-0.0-0.4V	Open collector transistor not activated
14.0	Open Collector OC3 Close Test	SCPI command	ADCO	Close K15, K9 (VM4), Drive OC3 - High		OC3_OUT	Read 0.2V +/- 0.2V	Open collector transistor activated
15.0	Open Collector OC3 Open Test	SCPI command	ADCO	Close K15, K9 (VM4), Drive OC3 - Low		OC3_OUT	Read 5V +/-0.0-0.4V	Open collector transistor not activated
16.0	ADCI1	SV_PWR	ADCI1	Close K13	Current Module INA219	SV_PWR.ADC1	SV +/- 0.3V (ADCI1 = 2.5V +/- 0.4V)	Verify ADC1 input
17.0	DAC Output HI Voltage	DAC_VOUT set to 3V	ADCI1	Close K2, K13	DAC, Module Current Module INA219	DAC_VOUT.ADC0	ADCI1 - 3V +/-0.2-0.4V	Validate DAC output with high voltage
18.0	DAC Output Low Voltage	DAC_VOUT set to 0.25V	ADCI1	Close K2, K13		DAC_VOUT.ADC0	ADCI1 - 0.25V +/-0.08-0.020V	Validate DAC output with low voltage
19.0	Power measurement test Current	SV_PWR	INA219 current	Open all relay (Default)			Read 5V +/-0.3-0.4V	Validate 10 ohm current limit resistor
20.0	Power measurement test Shunt Voltage	SV_PWR	INA219 current	Open all relay (Default)			Read 5mV +/- 7 mV	Validate 10 ohm current limit resistor
21.0	Power measurement test Current after	SV_PWR	INA219 current	Close K4			Read 500mA +/- 50 mA	Validate 10 ohm current limit resistor
22.0	10 Ohms resistance test	SV_PWR	ADCO	Close K4 (10 ohm), K7, X11(P56),X15,X16(VMS)	Current Module INA219	PWR_RES_H, PWR_RES_L	Read 100mA +/- 5 mA	Validate 10 ohm current limit resistor
23.0	Low Power Relay NC1 Test	SV_PWR	INA219 current	Close K10 (P52), Open LPK2, Close LPK1		K1_LP_C1, K3_LP, NC1,K2_LP, NC1,K2_LP_C1	Read 2.5V +/-0.3-0.4V	Validate resistance contact of the two JPR in series
24.0	Low Power Relay NC1 Test	SV_PWR	INA219 current	Close K10 (P52), Open LPK2, Close LPK1		K1_LP_C1, K3_LP, NC1,K2_LP, NC1,K2_LP_C1	Read 50mA +/-20-5mA	Validate resistance contact
25.0	Low Power Relay NC1 Test	SV_PWR	INA219 current	Close K10 (P52), Open LPK2, Close LPK1		K1_LP_C1, K3_LP, NC1,K2_LP, NC1,K2_LP_C1	Read 0mA +/- 2mA	Validate resistance contact
26.0	Low Power Relay NC2 Test	SV_PWR	INA219 current	Close K8 (P53), Open LPK2, Close LPK1		K1_LP_C2, K3_LP, NC2,K2_LP, NC2,K2_LP_C2	Read 50mA +/-20-5mA	Validate resistance contact
27.0	Low Power Relay NC2 Test	SV_PWR	INA219 current	Close K8 (P53), Open LPK2, Close LPK1		K1_LP_C2, K3_LP, NC2,K2_LP, NC2,K2_LP_C2	Read 0mA +/- 2mA	Validate resistance contact
28.0	Low Power Relay NC2 Test	SV_PWR	INA219 current	Close K8 (P53), Open LPK2, Close LPK1		K1_LP_C2, K3_LP, NC2,K2_LP, NC2,K2_LP_C2	Read 0mA +/- 2mA	Validate resistance contact
29.0	HPK Close Test	SV_PWR	INA219 current	Close K4 (10 ohm), K8, K10 (P54), Close SSR	Current Module INA219	K3_HP_ND1,K3_HP_ND2,K3_HP_C1,K3_HP_C2	Read 100mA +/- 15mA	Validate open relay resistance contact
30.0	HPK Open Test	SV_PWR	INA219 current	Close K4 (10 ohm), K8, K10 (P54), Close SSR	Current Module INA219	K3_HP_ND1,K3_HP_ND2,K3_HP_C1,K3_HP_C2	Read 0mA +/- 0.2mA	Validate open relay resistance contact
31.0	SSR Close Test	SV_PWR	INA219 current	Close K4 (10 ohm), K8, K10 (P54), Close SSR	Current Module INA219	SSR1_PDS, SSR1_NEG	Read 250mA +/- 15mA	Validate close relay resistance contact
32.0	SSR Open Test	SV_PWR	INA219 current	Close K4 (10 ohm), K8, K10 (P54), Close SSR	Current Module INA219	SSR1_PDS, SSR1_NEG	Read 0mA +/- 0.2mA	Validate open relay resistance contact
33.0	Relay BK1-BK2 CH0+H Close Test	SV_PWR	INA219 current	K7, K8(P57), Close Relay BK1-CH0, BK2-CH0	Current Module INA219	BK1_COM_H, BK2_COM_H, BK1_CH0, BK2_CH0	Read 50mA +/- 5mA	Validate relay contact close
34.0	Relay BK1-BK2 BK2-CH0 H Open Test	SV_PWR	INA219 current	K7, K8(P57), Close Relay BK1-CH0, BK2-CH0	Current Module INA219	BK1_COM_H, BK2_COM_H, BK1_CH0, BK2_CH0	Read 0mA +/- 0.2mA	Validate relay contact close
35.0	Relay BK1-BK2 BK2-CH0 H Close Test	SV_PWR	INA219 current	K7, K8(P57), Close Relay BK1-CH0, BK2-CH0	Current Module INA219	BK1_COM_H, BK2_COM_H, BK1_CH0, BK2_CH0	Read 50mA +/- 5mA	Validate relay contact close
36.0	Relay BK1-BK2 BK2-CH0 L Open Test	SV_PWR	INA219 current	K7, K8, K12 (P58), Close Relay BK1-CH0, BK2-CH0	Current Module INA219	BK1_COM_L, BK2_COM_L, BK1_CH0, BK2_CH0	Read 0mA +/- 0.2mA	Validate relay contact close
37.0	Relay BK1-BK2 BK2-CH0 L Close Test	SV_PWR	INA219 current	K7, K8, K12 (P58), Close Relay BK1-CH0, BK2-CH0	Current Module INA219	BK1_COM_L, BK2_COM_L, BK1_CH0, BK2_CH0	Read 50mA +/- 5mA	Validate relay contact close
38.0	Relay BK1-BK2 BK2-CH0+H Open Test	SV_PWR	INA219 current	K7, K8, K12 (P58), Close Relay BK1-CH0, BK2-CH0	Current Module INA219	BK1_COM_L, BK2_COM_L, BK1_CH0, BK2_CH0	Read 0mA +/- 0.2mA	Validate relay contact close
39.0	Relay BK1-BK2 BK2-CH0+H Close Test	SV_PWR	INA219 current	K7, K8, K12 (P58), Close Relay BK1-CH0, BK2-CH0	Current Module INA219	BK1_COM_L, BK2_COM_L, BK1_CH0, BK2_CH0	Read 50mA +/- 5mA	Validate relay contact close
40.0	Repeat for other Channel CHV-CHV							
41.0	BK1-BK2 COM relay BK1-BK2 COM H Close	SV_PWR	INA219 current	K7, K8(P57), K14, Close Relay BK1-CH0, BK2-CH0, BK1-COM, BK2-COM	Current Module INA219	BK1_COM_H, BK2_COM_H, BK1_CH0, BK2_CH0	Read 50mA +/- 5mA	Validate relay contact close
42.0	BK1-BK2 COM relay BK1-BK2 COM H Open	SV_PWR	INA219 current	K7, K8(P57), K14, Close Relay BK1-CH0, BK2-CH0, BK1-COM, BK2-COM	Current Module INA219	BK1_COM_H, BK2_COM_H, BK1_CH0, BK2_CH0	Read 0mA +/- 0.2mA	Validate relay contact close
43.0	BK1-BK2 COM relay BK1-BK2 COM H Close	SV_PWR	INA219 current	K7, K8(P57), K14, Close Relay BK1-CH0, BK2-CH0, BK1-COM, BK2-COM	Current Module INA219	BK1_COM_H, BK2_COM_H, BK1_CH0, BK2_CH0	Read 50mA +/- 5mA	Validate relay contact close
44.0	BK1-BK2 COM relay BK1-BK2 COM L Open	SV_PWR	INA219 current	K7, K8, K12 (P58), Close Relay BK1-CH0, BK2-CH0, BK1-COM, BK2-COM	Current Module INA219	BK1_COM_L, BK2_COM_L, BK1_CH0, BK2_CH0	Read 0mA +/- 0.2mA	Validate relay contact close
45.0	BK1-BK2 COM relay BK1-BK2 COM L Close	SV_PWR	INA219 current	K7, K8, K12 (P58), Close Relay BK1-CH0, BK2-CH0, BK1-COM, BK2-COM	Current Module INA219	BK1_COM_L, BK2_COM_L, BK1_CH0, BK2_CH0	Read 50mA +/- 5mA	Validate relay contact close
46.0	BK1-BK2 COM relay BK1-BK2 COM L Open	SV_PWR	INA219 current	K7, K8, K12 (P58), Close Relay BK1-CH0, BK2-CH0, BK1-COM, BK2-COM	Current Module INA219	BK1_COM_L, BK2_COM_L, BK1_CH0, BK2_CH0	Read 0mA +/- 0.2mA	Validate relay contact close
47.0	BK1-BK2 COM relay BK1-BK2 COM L Close	SV_PWR	INA219 current	K7, K8, K12 (P58), Close Relay BK1-CH0, BK2-CH0, BK1-COM, BK2-COM	Current Module INA219	BK1_COM_L, BK2_COM_L, BK1_CH0, BK2_CH0	Read 50mA +/- 5mA	Validate relay contact close
48.0	BK1-BK2 COM relay BK1-BK2 COM L Open	SV_PWR	INA219 current	K7, K8, K12 (P58), Close Relay BK1-CH0, BK2-CH0, BK1-COM, BK2-COM	Current Module INA219	BK1_COM_L, BK2_COM_L, BK1_CH0, BK2_CH0	Read 0mA +/- 0.2mA	Validate relay contact close
49.0	Repeat for BK3-BK4							
50.0	2C Bus GPIO #6 Master	Send command to check lines using IO modes	Digital State	GPIO-IN-DEV0-GP07		2C, DATA Master Pico	Read 1	
51.0	2C Bus GPIO #7 Master	Send command to check lines using IO modes	Digital State	GPIO-IN-DEV0-GP07		2C, CLOCK Master Pico	Read 1	
52.0	Get Selftest Device status	Check I2C communication with selftest	Read I2C byte	COM-I2C-READ-LE17 01		2C, Communication	Read 0	
53.0	2C Bus GPIO #6 Selftest	Send command to read GPIO function of line	Read I2C byte	COM-I2C-READ-LE17 75.6		2C, DATA Selftest Pico	Read 3	
54.0	2C Bus GPIO #7 Selftest	Send command to read GPIO function of line	Read I2C byte	COM-I2C-READ-LE17 75.7		2C, CLOCK Selftest Pico	Read 3	
55.0	SPI Bus GPIO #2 in digital mode	Set Selftest GPIO2 = 0	Digital State	Read master Pico level on GPIO2 (GPIO-IN-DEV0-GP02)		SPI, CLK	Read 0	
56.0	SPI Bus GPIO #2 in digital mode	Set Selftest GPIO2 = 1	Digital State	Read master Pico level on GPIO2 (GPIO-IN-DEV0-GP02)		SPI, CLK	Read 1	
57.0	SPI Bus GPIO #3 in digital mode	Set Selftest GPIO3 = 0	Digital State	Read master Pico level on GPIO3 (GPIO-IN-DEV0-GP03)		SPI, TX	Read 0	
58.0	SPI Bus GPIO #3 in digital mode	Set Selftest GPIO3 = 1	Digital State	Read master Pico level on GPIO3 (GPIO-IN-DEV0-GP03)		SPI, TX	Read 1	
59.0	SPI Bus GPIO #4 in digital mode	Set Selftest GPIO4 = 0	Digital State	Read master Pico level on GPIO4 (GPIO-IN-DEV0-GP04)		SPI, RX	Read 0	
60.0	SPI Bus GPIO #4 in digital mode	Set Selftest GPIO4 = 1	Digital State	Read master Pico level on GPIO4 (GPIO-IN-DEV0-GP04)		SPI, RX	Read 1	
61.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 0	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 0	
62.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
63.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
64.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
65.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
66.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
67.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
68.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
69.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
70.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
71.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
72.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
73.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
74.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
75.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
76.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
77.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
78.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
79.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
80.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
81.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
82.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
83.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
84.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
85.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
86.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
87.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
88.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
89.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
90.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
91.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
92.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
93.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
94.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
95.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
96.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
97.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
98.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
99.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
100.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
101.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
102.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
103.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
104.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
105.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
106.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
107.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
108.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
109.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
110.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
111.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
112.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
113.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
114.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
115.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
116.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1	Digital State	Read master Pico level on GPIO5 (GPIO-IN-DEV0-GP05)		SPI, CS	Read 1	
117.0	SPI Bus GPIO #5 in digital mode	Set Selftest GPIO5 = 1</						