

# Higher-Order unification for free!

Reusing the meta-language unification for the object language

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## ABSTRACT

Specifying and implementing a proof system from scratch requires significant effort. Logical Frameworks and Higher Order Logic Programming Languages provide dedicated, high-level meta languages to facilitate this task in two ways: 1) variable binding and substitution are for free when meta language binders represent object logic ones; 2) proof construction, and proof search, are greatly simplified by leveraging the unification procedure provided by the meta language. Notable examples of meta languages are Elf [19], Twelf [21],  $\lambda$ Prolog [14], Beluga [22], Abella [8] and Isabelle [28] which have been used to implement or specify many formal systems such as First Order Logic [5], Set Theory [18], Higher Order Logic [17], and the Calculus of Constructions [4].

The object logic we are interested in is Coq's type theory [26]. We aim to develop a higher-order unification-based proof search procedure using the meta language Elpi [3], a dialect of  $\lambda$ Prolog. Elpi's equational theory includes  $\eta\beta$ -equivalence and features a higher-order unification procedure  $\approx_m$  for the pattern fragment [13]. Elpi offers an encoding of Coq terms that is suitable for meta programming [25, 24, 9, 6] but that restricts  $\approx_m$  to first-order unification problems only. We refer to this basic encoding as  $\mathcal{O}$ .

In this paper we propose a more well-behaved encoding called  $\mathcal{M}$ , and show how to translate unification problems from  $\mathcal{O}$  to corresponding ones in  $\mathcal{M}$ . Consequently, we derive  $\approx_o$ , the higher-order unification procedure of  $\mathcal{O}$  that honours  $\eta\beta$ -equivalence (for Coq functions), addresses problems within the pattern fragment, and allows for the use of heuristics to deal with problems outside the pattern fragment. Moreover, as  $\approx_o$  delegates most of the work to  $\approx_m$ , it can be used to efficiently simulate a logic program in  $\mathcal{O}$  by taking advantage of unification-related optimizations of the meta language, such as clause indexing.

## KEYWORDS

Logic Programming, Meta-Programming, Higher-Order Unification

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## 1 INTRODUCTION

We aim to implement a form of proof search known as type-class resolution [27, 23] for the Coq's system [26]. Type-class solvers are unification based proof search procedures reminiscent of Prolog, which back-chain lemmas taken from a database of “type-class instances”. Given this analogy with Logic Programming we want to leverage the Elpi meta programming language [25], a dialect of  $\lambda$ Prolog, already used to extend Coq in various ways [25, 24, 9, 6].

The use of a meta language facilitates the implementation of a proof system in two key ways. The first and most well-know one is that variable binding and substitution come for free. The second one is that these meta languages come equipped with some form of unification, a cornerstone of proof construction and proof search. In this paper we focus on this last aspect of the implementation of a Prolog-like proof system, precisely *how to reuse the higher-order unification procedure of the meta language in order to simulate a higher-order logic program for the object language*.

We take as an example the Decision and Finite type classes from the Stdpp library [12]. The class Decision identifies predicates equipped with a decision procedure, while Finite identifies types whose inhabitants can be enumerated in a (finite) list. The following three type-class instances state that: 1) the type of natural numbers smaller than  $n$ , called `fin n`, is finite; 2) the predicate `nfact n nf`, relating a natural number  $n$  to the number of its prime factors  $nf$ , is decidable; 3) the universal closure of a predicate has a decision procedure if its domain is finite and if the predicate is decidable.

**Instance** `fin_fin`:  $\forall n, \text{Finite } (\text{fin } n)$ . (\* r1 \*)

**Instance** `nfact_dec`:  $\forall n \text{ nf}, \text{Decision } (\text{nfact } n \text{ nf})$ . (\* r2 \*)

**Instance** `forall_dec`:  $\forall A \text{ P}, \text{Finite } A \rightarrow$  (\* r3 \*)

$\forall x:A, \text{Decision } (\text{P } x) \rightarrow \text{Decision } (\forall x:A, \text{P } x)$ .

Given this database, a type-class solver is expected to prove the following statement automatically:

`Decision` ( $\forall x: \text{fin } 7, \text{nfact } x \text{ } 3$ ) (\* g \*)

The proof found by the solver back-chains on rule 3 (the only rule about the  $\forall$  quantifier), and then solves the premises with rules 1 and 2 respectively. Note that rule 3 features a second-order parameter  $P$  that represents a function of type  $A \rightarrow \text{Prop}$  (a 1-argument predicate over  $A$ ). The solver infers a value for  $P$  by unifying the conclusion of rule 3 with the goal, and in particular, it has to solve the unification problem  $P \ x = \text{nfact } x \text{ } 3$ . This higher-order problem falls in the so-called pattern fragment and admits a unique solution:

$P = \lambda x. \text{nfact } x \text{ } 3$  ( $\rho$ )

In the rest of this paper, we refer to the pattern fragment as  $\mathcal{L}$  [13].

In order to implement such a search in Elpi, we shall describe the encoding of Coq terms and then the encoding of instances as rules. Elpi comes equipped with an Higher-Order Abstract Syntax

(HOAS [20]) datatype of terms, called `tm`, that includes (among others) the following constructors:

```

type lam  tm -> (tm -> tm) -> tm.    % lambda abstraction
type app  list tm -> tm.              % n-ary application
type all  tm -> (tm -> tm) -> tm.    % forall quantifier
type con  string -> tm.              % constants

```

Following the standard syntax of  $\lambda$ Prolog [14], the meta-level binding of a variable  $x$  in an expression  $e$  is written as  $\llbracket x \rrbracket e$ , while square brackets delimit a list of terms separated by comma. For example, the term  $\forall y:t, \text{nfact } y \ 3$  is encoded as follows:

```
all (con "t") y\ app [con "nfact", y, con "3"]
```

We now illustrate a naive encoding of the three instances above as higher-order logic programming rules: capital letters denote rule parameters; `:-` separates the rule's head from the premises, and `«pi w\ p»` introduces a fresh nominal constant  $w$  for the premise  $p$ .

```
finite (app [con "fin", N]).           (r1)
```

```
decision (app [con "nfact", N, NF]).   (r2)
```

```
decision (all A x\ app [P, x]) :- finite A, (r3)
```

```
pi w\ decision (app [P, w]).
```

Unfortunately this encoding of rule (r3) does not work since it uses the predicate  $P$  as a first order term: for the meta language its type is `tm`. If we try to back-chain the rule (r3) on the encoding of the goal (g) given here

```
decision (all (app [con "fin", con "7"]) x\ app [con "nfact", x, con "3"]) (g)
```

we obtain an unsolvable unification problem (p): the two lists of terms have different lengths!

```
app [con "nfact", x, con "3"] = app [P, x] (p)
```

The problem above has no solution in a first-order language where  $P$  can only stand for a predicate symbol, while in a higher-order language  $P$  can stand for any 1-argument predicate.

In this paper we study a more sophisticated encoding of Coq terms and rules that, on a first approximation, would reshape (r3) as follows:

```
decision (all A x\ Pm x) :- link Pm P A, finite A, (r3')
pi x\ decision (app [P, x]).
```

Since  $Pm$  is a higher-order unification variable of type `tm -> tm`, with  $x$  in its scope, the unification problem (p') admits one solution:

```
app [con "nfact", x, con "3"] = Pm x (p')
Pm = x\ app [con "nfact", x, con "3"] (σ)
```

Once the head of rule (r3') unifies with the goal (g), the premise  $\llbracket \text{link } Pm \ A \ P \rrbracket$  brings the assignment (σ) back to the domain `tm` of Coq terms, obtaining the expected solution (ρ):

```
P = lam A x\ app [con "nfact", x, con "3"]
```

This simple example is sufficient to show that the encoding we seek is not trivial and does not only concern the head of rules, but the entire sequence of unification problems that constitute the execution of a logic program.

In fact the solution for  $P$  above generates a (Coq)  $\beta$ -redex in the second premise (the predicate under the `pi w\`). In turn, this redex prevents rule (r2) from backchaining properly since the following unification problem has no solution:

```
app [ lam A (a\ app [con "nfact", a, con "3"]) , x] =
app [ con "nfact" , N, NF]
```

The root cause of the problems we outlined in this example is a subtle mismatch between the equational theories of the meta language and the object language, which in turn makes the unification procedures of the meta language weak. The equational theory of the meta-language Elpi encompasses  $\eta\beta$ -equivalence and its unification procedure can solve higher-order problems in the pattern fragment. Although the equational theory of Coq is much richer, for efficiency and predictability reasons automatic proof search procedures typically employ a unification procedure that only captures a  $\eta\beta$ -equivalence and only operates in  $\mathcal{L}$ . The similarity is striking, but one needs to exercise some caution in order to simulate a higher-order logic program in Coq using the unification of Elpi.

*Contributions.* In this paper we identify a minimal language  $\mathcal{O}$  in which the problems sketched in the introduction can be formally described. We detail an encoding of a logic program in  $\mathcal{O}$  to a strongly related logic program in  $\mathcal{M}$ , the language of the meta language, and we show that the higher-order unification procedure of the meta language  $\approx_m$  can be used to simulate a higher-order unification procedure  $\approx_o$  for the object language that features  $\eta\beta$ -conversion. We show how  $\approx_o$  can be extended with heuristics to deal with problems outside the pattern fragment.

Section 2 states the problem formally and gives the intuition behind our solution; section 3 sets up a basic simulation of first-order logic programs, section 4 and section 5 extend it to higher-order logic programs in the pattern fragment while section 7 goes beyond the pattern fragment. Section 8 discusses the implementation in Elpi. The  $\lambda$ Prolog code discussed in the paper can be accessed at the address <https://github.com/FissoreD/ho-unif-for-free>.

## 2 PROBLEM STATEMENT AND SOLUTION

Even if we encountered the problem working on Coq's Calculus of Inductive Constructions, we devise a minimal setting to ease its study. In this setting, we have a (first order) language  $\mathcal{O}$  with a rich equational theory and a  $\mathcal{M}$  language with a simpler one.

### 2.1 Preliminaries: $\mathcal{O}$ and $\mathcal{M}$

In order to reason about unification we provide a description of the  $\mathcal{O}$  and  $\mathcal{M}$  languages where unification variables are first-class terms, i.e. they have a concrete syntax as shown in fig. 1. Unification variables in  $\mathcal{O}$  (fuva term constructor) have no explicit scope: the "arguments" of a higher-order variable are given via the `fapp` constructor. For example the term  $\llbracket P \ x \rrbracket$  is represented as  $\llbracket \text{fapp } [ \text{fuva } N, \ x ] \rrbracket$ , where  $N$  is the memory address of  $P$  and  $x$  is a bound variable. In  $\mathcal{M}$  the representation of  $\llbracket P \ x \rrbracket$  is instead  $\llbracket \text{uva } N [x] \rrbracket$ , since unification variables are higher-order and come equipped with an explicit scope.

```

kind fm type.          kind tm type.
type fapp list fm -> fm. type app list tm -> tm.
type flam (fm -> fm) -> fm. type lam (tm -> tm) -> tm.
type fcon string -> fm.   type con string -> tm.
type fuva addr -> fm.     type uva addr -> list tm -> tm.

```

Figure 1: The  $\mathcal{O}$  and  $\mathcal{M}$  languages

*Notational conventions.* When we write  $\mathcal{M}$  terms outside code blocks we follow the usual  $\lambda$ -calculus notation, reserving  $f, g, a, b$  for constants,  $x, y, z$  for bound variables and  $X, Y, Z, F, G, H$  for unification variables. However, we need to distinguish between the “application” of a unification variable to its scope and the application of a term to a list of arguments. We write the scope of unification variables in subscript while we use juxtaposition for regular application. Here are few examples:

```
f a      app [con "f", con "a"]
λx.λy.Fxy  lam x\ lam y\ uva F [x, y]
λx.Fx a    lam x\ app [uva F [x], con "a"]
λx.Fx x    lam x\ app [uva F [x], x]
```

When it is clear from the context, we shall use the same syntax for  $\mathcal{O}$  terms (although we never subscript unification variables). We use  $s, s_1, \dots$  for terms in  $\mathcal{O}$  and  $t, t_1, \dots$  for terms in  $\mathcal{M}$ .

## 2.2 Equational theories and unification

**2.2.1 Term equality:  $=_o$  and  $=_m$ .** For both languages, we extend the equational theory over ground terms to the full language by adding reflexivity for unification variables, i.e. a unification variable is equal to itself but different to any other term.

The first four rules are common to both equalities and define the usual congruence over terms. Since we use an HOAS encoding, they also capture  $\alpha$ -equivalence. In addition to that,  $=_o$  has rules for  $\eta$  and  $\beta$ -equivalence.

```
type (=o) fm -> fm -> o.      (=o)
fcon X =o fcon X.
fapp A =o fapp B :- forall2 (=o) A B.
flam F =o flam G :- pi x\ x =o x => F x =o G x.  (λλ)
fuva N =o fuva N.
flam F =o T :-                  (ηl)
  pi x\ beta T [x] (T' x), x =o x => F x =o T' x.
T =o flam F :-                  (ηr)
  pi x\ beta T [x] (T' x), x =o x => T' x =o F x.
fapp [flam X | L] =o T :- beta (flam X) L R, R =o T. (βl)
T =o fapp [flam X | L] :- beta (flam X) L R, T =o R. (βr)

type (=m) tm -> tm -> o.
con C =m con C.
app A =m app B :- forall2 (=m) A B.
lam F =m lam G :- pi x\ x =m x => F x =m G x.
uva N A =m uva N B :- forall2 (=m) A B.
```

Note that the symbol  $|$  separates the head of a list from the tail; that `forall2` applies a binary predicate to two lists (of equal length); and that `=>` is  $\lambda$ Prolog syntax for implication and is used to augment the current program with an extra rule. Both procedures use implication to assume that the fresh nominal constants introduced by `pi` are equal to themselves.

The main point in showing these equality tests is to remark how weaker  $=_m$  is, and to identify the four rules that need special treatment in the implementation of  $=_o$ . For brevity, we omit the code of `beta`: it is sufficient to know that «`beta F L R`» computes in  $R$  the weak-head normal form of «`fapp [F | L]`».

*Substitution:  $\rho$ s and  $\sigma$ t.* We write  $\sigma = \{ X \mapsto t \}$  for the substitution that assigns the term  $t$  to the variable  $X$ . We write  $\sigma t$  for the

application of the substitution to a term  $t$ , and  $\sigma X = \{ \sigma t \mid t \in X \}$  when  $X$  is a set of terms. We write  $\sigma \subseteq \sigma'$  when  $\sigma$  is more general than  $\sigma'$ . We shall use  $\rho$  for  $\mathcal{O}$  substitutions, and  $\sigma$  for the  $\mathcal{M}$  ones. For brevity, in this section, we consider the substitution for  $\mathcal{O}$  and  $\mathcal{M}$  identical. We defer to section 3.1 a more precise description pointing out their differences.

*Term unification:  $\approx_o$  vs.  $\approx_m$ .*  $\mathcal{M}$ 's unification signature is:

```
type (≈m) tm -> tm -> subst -> subst -> o.
```

We write  $\sigma t_1 \approx_m \sigma t_2 \mapsto \sigma'$  when  $\sigma t_1$  and  $\sigma t_2$  unify with substitution  $\sigma'$ . Note that  $\sigma'$  is a refined (i.e. extended) version of  $\sigma$ ; this is reflected by the signature above that relates two substitutions. We write  $t_1 \approx_m t_2 \mapsto \sigma'$  when the initial substitution  $\sigma$  is empty. We write  $\mathcal{L}$  as the set of terms that are in the pattern-fragment, i.e. every unification variable is applied to a list of distinct names.

The specification of a “good” unification procedure restricted to a domain  $\mathcal{D}$  is the following:

PROPOSITION 2.1 (GOOD UNIFICATION). *A good unification  $\approx$  for equality  $=$  in the domain  $\mathcal{D}$  satisfies the following properties:*

$$\{ t_1, t_2 \} \subseteq \mathcal{D} \Rightarrow t_1 \approx t_2 \mapsto \rho \Rightarrow \rho t_1 = \rho t_2 \quad (1)$$

$$\{ t_1, t_2 \} \subseteq \mathcal{D} \Rightarrow \rho t_1 = \rho t_2 \Rightarrow \exists \rho', t_1 \approx t_2 \mapsto \rho' \wedge \rho' \subseteq \rho \quad (2)$$

The meta language of choice is expected to provide an implementation of  $\approx_m$  that is a good unification for  $=_m$  in  $\mathcal{L}$ .

Even if we provide an implementation of the object-language unification  $\approx_o$  in section 3.6, our real goal is to simulate the execution of an entire logic-program.

## 2.3 The problem: logic-program simulation

We represent a logic program *run* in  $\mathcal{O}$  as a sequence of  $n$  steps. Each step  $p$  consists in unifying two terms,  $\mathbb{P}_{p_l}$  and  $\mathbb{P}_{p_r}$ , taken from the list of all unification problems  $\mathbb{P}$ . We write  $\mathbb{P}_p = \{ \mathbb{P}_{p_l}, \mathbb{P}_{p_r} \}$  and  $s \in \mathbb{P} \Leftrightarrow \exists p, s \in \mathbb{P}_p$ . The composition of these steps starting from the empty substitution  $\rho_0$  produces the final substitution  $\rho_n$ , which is the result of the logic program execution.

$$\text{step}_o(\mathbb{P}, p, \rho) \mapsto \rho' \stackrel{\text{def}}{=} \rho \mathbb{P}_{p_l} \approx_o \rho \mathbb{P}_{p_r} \mapsto \rho'$$

$$\text{run}_o(\mathbb{P}, n) \mapsto \rho_n \stackrel{\text{def}}{=} \bigwedge_{p=1}^n \text{step}_o(\mathbb{P}, p, \rho_{p-1}) \mapsto \rho_p$$

In order to simulate a logic program we start by compiling  $\mathbb{P}$  to  $\mathcal{Q}$ , i.e. each  $\mathcal{O}$ -term  $s \in \mathbb{P}$  is translated to a  $\mathcal{M}$ -term  $t \in \mathcal{Q}$ . We write this translation as  $\langle s \rangle \mapsto (t, m, l)$ . The implementation of the compiler is detailed in sections 3, 5 and 7, here we just point out that it additionally produces a variable map  $m$  and a list of links  $l$ . The variable map connects unification variables in  $\mathcal{M}$  to variables in  $\mathcal{O}$  and is used to “decompile” the substitution, that is written  $\langle \sigma, m, l \rangle^{-1} \mapsto \rho$ . Links are an accessory piece of information whose description is deferred to section 2.4.

Then we simulate each run in  $\mathcal{O}$  with a run in  $\mathcal{M}$  as follows:

$$\text{step}_m(\mathcal{Q}, p, \sigma, \mathbb{L}) \mapsto (\sigma'', \mathbb{L}') \stackrel{\text{def}}{=}$$

$$\sigma \mathbb{Q}_{p_l} \approx_m \sigma \mathbb{Q}_{p_r} \mapsto \sigma' \wedge \text{progress}(\mathbb{L}, \sigma') \mapsto (\mathbb{L}', \sigma'')$$

$$\text{run}_m(\mathbb{P}, n) \mapsto \rho_n \stackrel{\text{def}}{=}$$

$$\mathcal{Q} \times \mathbb{M} \times \mathbb{L}_0 = \{ (t, m, l) \mid s \in \mathbb{P}, \langle s \rangle \mapsto (t, m, l) \}$$

$$\bigwedge_{p=1}^n \text{step}_m(\mathcal{Q}, p, \sigma_{p-1}, \mathbb{L}_{p-1}) \mapsto (\sigma_p, \mathbb{L}_p)$$

$$\langle \sigma_n, \mathbb{M}, \mathbb{L}_n \rangle^{-1} \mapsto \rho_n$$

By analogy with  $\mathbb{P}$ , we write  $\mathbb{Q}_{p_l}$  and  $\mathbb{Q}_{p_r}$  for the two  $\mathcal{M}$  terms being unified at step  $p$ , and we write  $\mathbb{Q}_p$  for the set  $\{\mathbb{Q}_{p_l}, \mathbb{Q}_{p_r}\}$ . The  $\text{step}_m$  procedure is made of two sub-steps: a call to the meta-language unification and a check for progress on the set of links, that intuitively will compensate for the weaker equational theory honored by  $\approx_m$ . Procedure  $\text{run}_m$  compiles all terms in  $\mathbb{P}$ , then executes each step, and finally decompiles the solution. We claim:

PROPOSITION 2.2 (RUN EQUIVALENCE).  $\forall \mathbb{P}, \forall n$ , if  $\mathbb{P} \subseteq \mathcal{L}$

$$\text{run}_o(\mathbb{P}, n) \mapsto \rho \wedge \text{run}_m(\mathbb{P}, n) \mapsto \rho' \Rightarrow \forall s \in \mathbb{P}, \rho s =_o \rho' s$$

That is, the two executions give equivalent results if all terms in  $\mathbb{P}$  are in the pattern fragment. Moreover:

PROPOSITION 2.3 (SIMULATION FIDELITY). *In the context of  $\text{run}_m$ , if  $\mathbb{P} \subseteq \mathcal{L}$  we have that  $\forall p \in 1 \dots n$ ,*

$$\text{step}_o(\mathbb{P}, p, \rho_{p-1}) \mapsto \rho_p \Leftrightarrow \text{step}_m(\mathbb{Q}, p, \sigma_{p-1}, \mathbb{L}_{p-1}) \mapsto (\sigma_p, \mathbb{L}_p)$$

In particular, this property guarantees that a *failure* in the  $\mathcal{O}$  run is matched by a failure in  $\mathcal{M}$  at the same step. We consider this property very important from a practical point of view since it guarantees that the execution traces are strongly related, and in turn, this enables a user to debug a logic program in  $\mathcal{O}$  by looking at its execution trace in  $\mathcal{M}$ .

We also claim that  $\text{run}_m$  handles terms outside  $\mathcal{L}$  in the following sense:

PROPOSITION 2.4 (FIDELITY RECOVERY). *In the context of  $\text{run}_m$ , if  $\rho_{p-1}\mathbb{P}_p \subseteq \mathcal{L}$  (even if  $\mathbb{P}_p \not\subseteq \mathcal{L}$ ) then*

$$\text{step}_o(\mathbb{P}, p, \rho_{p-1}) \mapsto \rho_p \Leftrightarrow \text{step}_m(\mathbb{Q}, p, \sigma_{p-1}, \mathbb{L}_{p-1}) \mapsto (\sigma_p, \mathbb{L}_p)$$

In other words, if the two terms involved in a step re-enter  $\mathcal{L}$ , then  $\text{step}_m$  and  $\text{step}_o$  are again related, even if  $\mathbb{P} \not\subseteq \mathcal{L}$  and hence proposition 2.3 does not apply. Indeed, the main difference between proposition 2.3 and proposition 2.4 is that the assumption of the former is purely static, it can be checked upfront. When this assumption is not satisfied, one can still simulate a logic program and have guarantees of fidelity if, at run time, decidability of higher-order unification is restored.

This property has practical relevance since in many logic programming implementations, including Elpi, the order in which unification problems are tackled does matter. The simplest example is the sequence  $F \approx \lambda x.a$  and  $F a \approx a$ : the second problem is not in  $\mathcal{L}$  and has two unifiers, namely  $\sigma_1 = \{F \mapsto \lambda x.x\}$  and  $\sigma_2 = \{F \mapsto \lambda x.a\}$ . The first problem picks  $\sigma_2$ , making the second problem re-enter  $\mathcal{L}$ .

*Backtracking.* We omit it from our model of logic-program execution since it plays a very minor role, orthogonal to higher-order unification. We point out that each *run* corresponds to a (proof search) branch in the logic program that either fails at some point, or succeeds. A computation that succeeds by backtracking, exploring multiple branches, could be modeled as a set of runs with a (possibly non-empty) common prefix.

## 2.4 The solution (in a nutshell)

A term  $s$  is compiled to a term  $t$  where every “problematic” subterm  $e$  is replaced by a fresh unification variable  $h$  with an accessory *link* that represents a suspended unification problem  $h \approx_m e$ . As a result,

$\approx_m$  is “well behaved” on  $t$ , in the sense that it does not contradict  $=_o$  as it would otherwise do on the “problematic” sub-terms. We now define “problematic” and “well behaved” more formally. We use the  $\diamond$  symbol since it stands for “possibly” in modal logic and all problematic terms are characterized by some uncertainty.

Definition 2.5 ( $\diamond\beta$ ).  $\diamond\beta$  is the set of terms of the form  $F x_1 \dots x_n$  such that  $x_1 \dots x_n$  are distinct names (of bound variables).

An example of a  $\diamond\beta$  term is the application  $F x$ . This term is problematic since the application node of its syntax tree cannot be used to justify a unification failure, i.e. by properly instantiating  $F$ , the term head constructor may become a  $\lambda$ , or a constant, or a name, or remain an application.

Definition 2.6 ( $\diamond\eta$ ).  $\diamond\eta$  is the set of terms  $\lambda x.s$  such that  $\exists \rho, \rho \lambda x.s$  is an eta expansion.

An example of a term  $s$  in  $\diamond\eta$  is  $\lambda x.\lambda y.F y x$  since the substitution  $\rho = \{F \mapsto \lambda x.\lambda y.f y x\}$  makes  $\rho s = \lambda x.\lambda y.f x y$ , which is the eta-long form of  $f$ . This term is problematic since its leading  $\lambda$  abstraction cannot justify a unification failure against a constant  $f$ .

Definition 2.7 ( $\diamond\mathcal{L}$ ).  $\diamond\mathcal{L}$  is the set of terms of the form  $F t_1 \dots t_n$  such that  $t_1 \dots t_n$  are not distinct names.

These terms are problematic for the very same reason terms in  $\diamond\beta$  are, but they cannot be handled directly by the unification of the meta language, which is only required to handle terms in  $\mathcal{L}$ . Still, given  $s \in \diamond\mathcal{L}$  there may exist a substitution  $\rho$  such that  $\rho s \in \mathcal{L}$ .

We write  $\mathcal{P}(t)$  for the set of sub-terms of  $t$ , and we write  $\mathcal{P}(X) = \bigcup_{t \in X} \mathcal{P}(t)$  when  $X$  is a set of terms.

Definition 2.8 (Well behaved set). Given a set of terms  $X$ ,

$$\mathcal{W}(X) \Leftrightarrow \forall t \in \mathcal{P}(X), t \notin (\diamond\beta \cup \diamond\eta \cup \diamond\mathcal{L})$$

We write  $\mathcal{W}(t)$  as a short for  $\mathcal{W}(\{t\})$ . We claim our compiler validates the following properties:

PROPOSITION 2.9 ( $\mathcal{W}$ -ENFORCING). *Given a term  $s$  in  $\mathcal{O}$ ,*

$$\langle s \rangle \mapsto (t, m, l) \Rightarrow \mathcal{W}(t)$$

PROPOSITION 2.10 (COMPILER-USEFUL). *Given two terms  $s_1$  and  $s_2$ , if  $\exists \rho, \rho s_1 =_o \rho s_2$ , then*

$$\langle s_i \rangle \mapsto (t_i, m_i, l_i) \text{ for } i \in \{1, 2\} \Rightarrow t_1 \approx_m t_2 \mapsto \sigma$$

In other words the compiler outputs terms in  $\mathcal{W}$  even if its input is not. And if two terms can be unified in the source language then their images can also be unified in the target language. Note that the property holds for any substitution, not necessarily a most general one: when applied to terms in  $\mathcal{W}$  the meta-language unification  $\approx_m$  agrees with the object-language equality  $=_o$ . Note that a compiler that translate all terms to unification variables would satisfy this property, but would certainly fail to verify proposition 2.3.

PROPOSITION 2.11 ( $\mathcal{W}$ -PRESERVATION).  $\forall \mathbb{Q}, \forall \mathbb{L}, \forall p, \forall \sigma, \forall \sigma'$

$$\mathcal{W}(\sigma \mathbb{Q}) \wedge \sigma \mathbb{Q}_{p_l} \approx_m \sigma \mathbb{Q}_{p_r} \mapsto \sigma' \Rightarrow \mathcal{W}(\sigma' \mathbb{Q})$$

$$\mathcal{W}(\sigma \mathbb{Q}) \wedge \text{progress}(\mathbb{L}, \sigma) \mapsto (\_, \sigma') \Rightarrow \mathcal{W}(\sigma' \mathbb{Q})$$

Proposition 2.11 is key to proving propositions 2.2 and 2.3. Informally, it says that the problematic terms moved on the side by the compiler are not reintroduced by  $\text{step}_m$ , hence  $\approx_m$  can continue



to operate properly. In sections 4, 5 and 7 we describe how to recognize terms in  $\Diamond\beta$ ,  $\Diamond\eta$  and  $\Diamond\mathcal{L}$  and how progress takes care of links and how it preserves  $\mathcal{W}$  and ensures propositions 2.2 to 2.4.

We prove proposition 2.3 (SIMULATION FIDELITY) in sections 5 to 7 and proposition 2.2 (RUN EQUIVALENCE) only in section 4, where links are not present yet. In order to prove proposition 2.2 in the presence of links one needs to refine proposition 2.3 by relating the two substitutions  $\rho_p$  and  $\sigma_p$ : they are equivalent only by using decompilation to take into account terms in  $\mathbb{L}$ . Then, by induction on the number of steps, proposition 2.3 implies proposition 2.2. We discuss proposition 2.4 (FIDELITY RECOVERY) in section 7.

### 3 BASIC COMPILATION AND SIMULATION

#### 3.1 Memory map ( $\mathbb{M}$ ) and substitution ( $\rho$ and $\sigma$ )

Unification variables are identified by a (unique) memory address. The memory and its associated operations are described below:

```
typeabbrev (mem A) (list (option A)).
type set? addr -> mem A -> A -> o.
type unset? addr -> mem A -> o.
type assign addr -> mem A -> A -> mem A -> o.
type new mem A -> addr -> mem A -> o.
```

If a memory cell is none, then the corresponding unification variable is not set. assign sets an unset cell to the given value, while new finds the first unused address and sets it to none.

Since each  $\mathcal{M}$  unification variable occurs together with a scope, its assignment needs to be abstracted over it to enable the instantiation of the same assignment to different scopes. This is expressed by the inctx container, and in particular its abs binding constructor.

```
kind inctx type -> type.
type abs (tm -> inctx A) -> inctx A.
type val A -> inctx A.
typeabbrev assignment (inctx tm).
typeabbrev subst (mem assignment).
```

A solution to a  $\mathcal{O}$  variable is a plain term, that is, fsubst is an abbreviation for mem fm.

The compiler establishes a mapping between variables of the two languages.

```
kind fvariable type.
type fv addr -> fvariable.
kind hvariable type.
type hv addr -> arity -> hvariable.
kind mapping type.
type (<=>) fvariable -> hvariable -> mapping.
typeabbrev mmap (list mapping).
```

Each hvariable is stored in the mapping together with its arity (a number) so that the code of (malloc) below can preserve:

INVARIANT 1 (UNIFICATION-VARIABLE ARITY). *Each variable  $A$  in  $\mathcal{M}$  has a (unique) arity  $N$  and each occurrence (uva  $A$   $L$ ) is such that  $L$  has length  $N$ .*

```
type m-alloc fvariable -> hvariable -> mmap -> mmap ->
  subst -> subst -> o.
m-alloc Fv Hv M M S S :- mem M (Fv <=> Hv), !.
m-alloc Fv Hv M [Fv <=> Hv|M] S S1 :- Hv = hv N _, new S N S1.
```

When a single fvariable occurs multiple times with different numbers of arguments, the compiler generates multiple mappings for it, on a first approximation, and then ensures the mapping are bijective by introducing  $\eta$ -link; this detail is discussed in section 6.

It is worth examining the code of deref, which applies the substitution to a  $\mathcal{M}$  term. Notice how assignments are moved to the current scope, i.e. the abs-bound variables are renamed with the names in the scope of the unification variable occurrence.

```
type deref subst -> tm -> tm -> o.
deref _ (con C) (con C).
deref S (app A) (app B) :- map (deref S) A B.
deref S (lam F) (lam G) :-
  pi x\ deref S x x => deref S (F x) (G x).
deref S (uva N L) R :- set? N S A,
  move A L T, deref S T R.
deref S (uva N A) (uva N B) :- unset? N S,
  map (deref S) A B.
```

Note that move strongly relies on invariant 1: the length of the arguments of all occurrences of a unification variable is the same. Hence, they have the same simple type for the meta-level, and therefore the number of abs nodes in the assignment matches that length. This guarantees that move never fails.

```
type move assignment -> list tm -> tm -> o.
move (abs Bo) [H|L] R :- move (Bo H) L R.
move (val A) [] A :- !.
```

We write  $\sigma = \{ A_{xy} \mapsto y \}$  for the assignment «abs  $x \backslash \text{abs } y \backslash y$  » and  $\sigma = \{ A \mapsto \lambda x. \lambda y. y \}$  for «lam  $x \backslash \text{lam } y \backslash y$  ».

#### 3.2 Links ( $\mathbb{L}$ )

As mentioned in section 2.4, the compiler replaces terms in  $\Diamond\eta$ ,  $\Diamond\beta$ , and  $\Diamond\mathcal{L}$  with fresh variables linked to the problematic terms. Terms in  $\Diamond\beta$  do not need a link since  $\mathcal{M}$  variables faithfully represent the problematic term thanks to their scope.

```
kind baselink type.
type link-eta tm -> tm -> baselink.
type link-llam tm -> tm -> baselink.
typeabbrev links (list (inctx baselink)).
```

The right-hand side of a link, the problematic term, can occur under binders. To accommodate this situation, the compiler wraps baselink using the inctx container (see  $\cdot \vdash \cdot$  also used for subst).

INVARIANT 2 (LINK LEFT HAND SIDE). *The left-hand side of a suspended link is a variable.*

New links are suspended by construction. If the left-hand side is assigned during a step, then the link is considered for progress and possibly eliminated. This is discussed in section 5 and section 7.

In the examples we represent links as equations under a context. The equality sign is subscripted with the kind of baselink. For example  $x \vdash A_x =_{\mathcal{L}} F_x$  corresponds to:

```
abs x\ val (link-llam (uva A [x]) (app[uva F [x],con "a"]))
```

#### 3.3 Compilation

The simple compiler described in this section serves as a basis for the extensions in sections 4, 5 and 7. Its main task is to beta

normalize the term and map one syntax tree to the other. In order to bring back the substitution from  $\mathcal{M}$  to  $\mathcal{O}$  the compiler builds a “memory map” connecting the  $\mathcal{O}$  variables to the  $\mathcal{M}$  ones using (*malloc*).

The signature of the *comp* predicate below allows for the generation of links (suspended unification problems), which plays no role in this section but plays a major role in sections 4, 5 and 7. With respect to section 2, the signature also allows for updates to the substitution.

```

type comp fm -> tm -> mmap -> mmap -> links -> links ->
  subst -> subst -> o.
comp (fcon C) (con C) M M L L S S.
comp (flam F) (lam F1) M1 M2 L1 L2 S1 S2 :- (cλ)
  comp-lam F F1 M1 M2 L1 L2 S1 S2.
comp (fuva A) (uva B [J]) M1 M2 L L S1 S2 :-
  m-alloc (fv A) (hv B (arity z)) M1 M2 S1 S2.
comp (fapp A) (app A1) M1 M2 L1 L2 S1 S2 :- (c@)
  fold6 comp A A1 M1 M2 L1 L2 S1 S2.

type compile fm -> tm -> mmap -> mmap -> links -> links ->
  subst -> subst -> o.
compile F G M1 M2 L1 L2 S1 S2 :-
  beta-normal F F', comp F' G M1 M2 L1 L2 S1 S2.

```

The code above uses that possibility in order to allocate space for the variables, i.e. it sets their memory address to none (a detail not worth mentioning in the previous sections).

```

type comp-lam (fm -> fm) -> (tm -> tm) ->
  mmap -> mmap -> links -> links -> subst -> subst -> o.
comp-lam F G M1 M2 L1 L3 S1 S2 :-
  pi x y\ (pi M L S\ comp x y M M L L S S) => (Hλ)
  comp (F x) (G y) M1 M2 L1 (L2 y) S1 S2,
  close-links L2 L3.

```

In the code above, the syntax `pi x y\...` is syntactic sugar for iterated *pi* abstraction, as in `pi x\ pi y\...`. The auxiliary predicate *fold6* folds a predicate with three accumulators (the memory map, the links and the substitution) over a lists to obtain a new list and the final values of the accumulators.

The auxiliary function *close-links* tests if the bound variable *v* really occurs in the link. If it does, the link is wrapped into an additional *abs* node binding *v*. In this way links generated deep inside the compiled terms can be moved outside their original context of binders.

```

type close-links (tm -> links) -> links -> o.
close-links (v\[X v] L v) [abs X|R] :- close-links L R.
close-links (_\[]) [].

```

### 3.4 Execution

A step in  $\mathcal{M}$  consists of unifying two terms and reconsidering all links for progress. If either of these tasks fails, we consider the entire step to fail. It is at this granularity that we can relate steps in the two languages.

```

type hstep tm -> tm -> links -> links -> subst -> subst -> o.
hstep T1 T2 L1 L2 S1 S3 :-
  (T1 ≈m T2) S1 S2,
  progress L1 L2 S2 S3.

```

Note that the infix notation  $((A \approx_m B) C D)$  is syntactic sugar for  $((\approx_m) A B C D)$ .

Reconsidering links is a fixpoint process because the progress of a link can update the substitution, which may then enable another link to progress.

```

type progress links -> links -> subst -> subst -> o.
progress L L3 S S3 :-
  progress1 L L1 S S1,
  occur-check-links L1,
  scope-check L1 L2 S1 S2
  if (L = L2, S = S2) (L3 = L2, S3 = S2)
  (progress L2 L3 S2 S3).

```

**3.4.1 Progress.** In the base compilation scheme, *progress1* is the identity function on both the links and the substitution, so the fixpoint trivially terminates. Sections 5 and 7 add rules to *progress1* and explain why the don't hinder termination.

**3.4.2 Scope check.** The predicate *scope-check* replaces each link of the form  $\Gamma \vdash X_{x_1 \dots x_n} =_{\eta} t$  with a link  $x_1 \dots x_n \vdash X_{x_1 \dots x_n} =_{\eta} t'$  whenever  $\{x_1 \dots x_n\} \subset \Gamma$ . The term  $t' = \lambda x. Y_{x_1 \dots x_n, x}$  is obtained after executing  $\lambda x. Y_{x_1 \dots x_n, x} \approx_m t$  using a fresh *Y*. This unification ensures that  $t'$  cannot contain variables in  $\Gamma / \{x_1 \dots x_n\}$ , and if it fails then progress hence step<sub>*m*</sub> fails. In turn this grants fidelity in the case where the lhs of an  $\eta$ -link is pruned of a name that occurs (rigidly) in *t*: links represent suspended unification problems *that may succeed*.

**3.4.3 Occur check.** Since compilation moves problematic terms out of the sight of  $\approx_m$ , that procedure can only perform a partial occur check. For example, the unification problem  $X \approx_m f Y$  cannot generate a cyclic substitution alone, but should be disallowed if  $\mathbb{L}$  contains a link like  $\vdash Y =_{\eta} \lambda z. X_z$ : we don't know yet if *Y* will feature a lambda in head position, but we surely know it contains *X*. The procedure *occur-check-links* is in charge of performing this check that is needed in order to guarantee proposition 2.3 (SIMULATION FIDELITY).

### 3.5 Substitution decompilation

Decompiling the substitution involves three steps.

First and foremost, problematic terms stored in  $\mathbb{L}$  have to be moved back into the game: a suspended link must be turned into a valid assignment. This operation is possible thanks to invariant 2 (LINK LEFT HAND SIDE), which ensures that no link causes an occur-check (3.4.3) and the fact that  $\mathbb{L}$  is duplicate-free (see section 6).

The second step allocates in the memory of  $\mathcal{O}$  new variables used to implement the pruning of higher-order variables. For example,  $F x y = F x z$  requires allocating a variable *G* in order to express the assignment  $F_{ab} \mapsto G_a$ .

The final step is to decompile each assignment. The *val* node carries a term that is easy to decompile since  $\mathbb{M}$  is a bijection. Since the *flam* node carries no additional information (other than the function body), each *abs* node can be decompiled to a *flam* one.

However, if the *flam* node was carrying more information, as is the case for Coq where it holds the type of the bound variable, one would need to store this piece of information in the memory map, were we store the arity (that is a very simple function type).

PROPOSITION 3.1 (COMPILATION ROUND TRIP). *If  $\langle s \rangle \mapsto (t, m, l)$  and  $l \in \mathbb{L}$  and  $m \in \mathbb{M}$  and  $\sigma = \{A \mapsto t\}$  and  $X \mapsto A^n \in \mathbb{M}$  then  $\langle \sigma, \mathbb{M}, \mathbb{L} \rangle^{-1} \mapsto \rho$  and  $\rho X =_o \rho s$ .*

We omit to sketch the proof of this property for brevity.

### 3.6 Definition of $\approx_o$ and its properties

We already have all the ingredients to show the code of  $\approx_o$ .

```
type ( $\approx_o$ ) fm -> fm -> fsubst -> o.
(A  $\approx_o$  B) F :-
  compile A A' [] M1 [] L1 [] S1,
  compile B B' M1 M2 L1 L2 S1 S2,
  hstep A' B' L2 L3 S2 S3,
  decompile M2 L3 S3 [] F.
```

So far the compiler is very basic. It does not really enforce that the terms passed to  $\text{step}_m$  are in  $\mathcal{W}$ , and indeed makes no use of the higher-order capabilities of the meta language (all generated variables have an empty scope). Still, we can prove that  $\approx_o$  is a good “first-order” unification algorithm if the input already happens to be in  $\mathcal{W}$ . Later, when the compiler will ensure proposition 2.9 ( $\mathcal{W}$ -ENFORCING) and the proof will be refined to cover for the new cases.

THEOREM 3.2 (PROPERTIES OF  $\approx_o$  IN  $\mathcal{W}$ ). *The implementation of  $\approx_o$  above is a good unification for  $=_o$  (as per proposition 2.1) in the domain  $\mathcal{W}$ .*

PROOF SKETCH. In this setting,  $=_m$  is as strong as  $=_o$  on ground terms. What we have to show is that whenever two different  $\mathcal{O}$  terms can be made equal by a substitution  $\rho$ , we can produce this  $\rho$  by finding a  $\sigma$  via  $\approx_m$  on the corresponding  $\mathcal{M}$  terms and by decompiling it. If we look at the syntax of  $\mathcal{O}$ , the only interesting case is  $\langle \text{fuva } X \approx_o s \rangle$ . In this case, after compilation, we have  $\langle \text{uva } Y [] \approx_m t \rangle$  that succeeds with  $\sigma = \{Y \mapsto t\}$  and  $\sigma$  is decompiled to  $\rho = \{X \mapsto s\}$  by proposition 3.1.  $\square$

THEOREM 3.3 (FIDELITY IN  $\mathcal{W}$ ). *Proposition 2.2 (RUN EQUIVALENCE) and proposition 2.3 (SIMULATION FIDELITY) hold if  $\mathcal{W}(\mathbb{P})$ .*

PROOF SKETCH. Since  $\text{progress1}$  is a no-op then  $\text{step}_o$  and  $\text{step}_m$  are the same. By theorem 3.2,  $\approx_m$  is equivalent to  $\approx_o$  in  $\mathcal{W}$ .  $\square$

### 3.7 Notational conventions

In the following sections we use the following notation for input and output of the compiler.  $\mathbb{P}$  represents the input unification problems in  $\mathcal{O}$  while  $\mathbb{Q}$  their corresponding compiled version with memory mapping  $\mathbb{M}$  and links  $\mathbb{L}$ . For example:

$$\begin{aligned} \mathbb{P} &= \{ p_1 \approx_o p_2 \quad p_3 \approx_o p_4 \} \\ \mathbb{Q} &= \{ t_1 \approx_m t_2 \quad t_3 \approx_m t_4 \} \\ \mathbb{M} &= \{ X_1 \mapsto A_1^x \quad X_2 \mapsto A_2^y \} \\ \mathbb{L} &= \{ \Gamma \vdash a =_\eta b \} \end{aligned}$$

We index each sub-problem, sub-mapping, and sub-link with its position starting from 1 and counting from left to right, top to bottom. For example,  $\mathbb{Q}_2$  corresponds to the  $\mathcal{M}$  problem  $t_3 \approx_m t_4$ .

As we deal with each category of problematic terms we weaken the assumptions of theorem 3.3 using the following definitions:

Definition 3.4 ( $\mathcal{W}_\beta$ ).  $\mathcal{W}_\beta(X) \Leftrightarrow \forall t \in \mathcal{P}(X), t \notin (\diamond \eta \cup \diamond \mathcal{L})$

Definition 3.5 ( $\mathcal{W}_{\beta\eta}$ ).  $\mathcal{W}_{\beta\eta}(X) \Leftrightarrow \forall t \in \mathcal{P}(X), t \notin \diamond \mathcal{L}$

## 4 HANDLING OF $\diamond \beta$

The basic compiler given in the previous section is unable to make the following higher-order unification problem succeed.

$$\begin{aligned} \mathbb{P} &= \{ \lambda x. (f \cdot (X \ x) \ a) \approx_o \lambda x. (f \cdot x \ a) \} \\ \mathbb{Q} &= \{ \lambda x. (f \cdot (A \ x) \ a) \approx_m \lambda x. (f \cdot x \ a) \} \\ \mathbb{M} &= \{ X \mapsto A^0 \} \end{aligned}$$

The unification problem  $\mathbb{Q}_1$  fails while trying to unify  $A \ x$  and  $x$ , which is equivalent to  $\langle \text{app } [\text{uva } A [], x] \rangle$  versus  $x$ . In order to exploit the higher-order unification algorithm of the meta language, we compile the  $\mathcal{O}$  term  $X \ x$  into the  $\mathcal{M}$  term  $A_x$ .

### 4.1 Compilation and decompilation

We add the following rule before rule ( $c_\circ$ ), where pattern-fragment is a predicate checking if a list of terms is a list of distinct names.

```
comp (fapp [fuva A|Ag]) (uva B Ag1) M1 M2 L L S1 S2 :-
  pattern-fragment Ag, !,
  fold6 comp Ag Ag1 M1 M1 L L S1 S1,
  len Ag Arity,
  m-alloc (fv A) (hv B (arity Arity)) M1 M2 S1 S2.
```

Note that compiling  $\text{Ag}$  cannot create new mappings nor links, since  $\text{Ag}$  is made of bound variables, and the hypothetical rule ( $H_\lambda$ ) loaded by  $\text{comp-lam}$  grants this property.

*Decompilation.* No change to commit-link since no link is added.

*Progress.* No change to progress since no link is added.

LEMMA 4.1 (PROPERTIES OF  $\approx_o$  IN  $\mathcal{W}_\beta$ ). *Given the updated compilation scheme, the  $\approx_o$  of section 3.6 is a good unification (as per proposition 2.1) for  $=_o$  in the domain  $\mathcal{W}_\beta$ .*

PROOF SKETCH. If we look at the  $\mathcal{O}$  terms, there is one more interesting case, namely  $\text{fapp}[\text{fuva } X|\mathbb{W}] \approx_o s$  when  $\mathbb{W}$  are distinct names compiled to  $\vec{w}$ . In this case the  $\mathcal{M}$  problem is  $Y_{\vec{w}} \approx_m t$  that succeeds with  $\sigma = \{Y_{\vec{w}} \mapsto t[\vec{w}/\vec{y}]\}$ , which in turn is decompiled to  $\rho = \{Y \mapsto \lambda \vec{y}. s[\vec{w}/\vec{y}]\}$ . Thanks to  $\beta_l$  ( $\lambda \vec{y}. s[\vec{w}/\vec{y}]) \vec{w} =_o s$ .  $\square$

LEMMA 4.2 ( $\mathcal{W}$ -PARTIAL-ENFORCEMENT).  $\forall s, \langle s \rangle \mapsto (t, m, l)$ , if  $\mathcal{W}_\beta(s)$  then  $\mathcal{W}(t)$ .

In other word the compiler eliminates all subterms that are in  $\diamond \beta$ .

THEOREM 4.3 (FIDELITY IN  $\mathcal{W}_\beta$ ). *Proposition 2.2 (RUN EQUIVALENCE) and proposition 2.3 (SIMULATION FIDELITY) hold if  $\mathcal{W}_\beta(\mathbb{P})$ .*

PROOF SKETCH. Thanks to lemma 4.2  $\approx_m$  is as powerful as  $\approx_o$  in  $\mathcal{W}_\beta$ , as well as in  $\mathcal{W}$  by theorem 3.3.  $\square$

## 5 HANDLING OF $\diamond \eta$

A term  $\lambda x. t \ x$  is said to be the  $\eta$ -expansion of  $t$  if  $x$  does not occur free in  $t$ , and conversely we say that  $t$  is the  $\eta$ -contraction of  $\lambda x. t \ x$ . The equational theory of  $\mathcal{O}$  identifies these terms, but the current compilation scheme does not, as shown by the following example:

while  $\lambda x.X x \approx_o f$  does admit the solution  $\rho = \{ X \mapsto f \}$ , the corresponding problem in  $\mathbb{Q}$  does not.

$$\begin{aligned}\mathbb{P} &= \{ \lambda x.X x \approx_o f \} \\ \mathbb{Q} &= \{ \lambda x.A_x \approx_m f \} \\ \mathbb{M} &= \{ X \mapsto A^1 \}\end{aligned}$$

The reason is that  $\text{lam } x \backslash \text{uva } A \text{ [} x \text{]}$  and  $\text{con "f"}$  start with different, rigid, term constructors.

In order to guarantee proposition 2.2, we detect lambda abstractions that can disappear by  $\eta$ -contraction (section 5.1), and we modify the compiler so that it generates fresh unification variables in their place and moves the problematic term from  $\mathbb{Q}$  to  $\mathbb{L}$  (section 5.2). The compilation of the problem  $\mathbb{P}$  above is refined to:

$$\begin{aligned}\mathbb{P} &= \{ \lambda x.X x \approx_o f \} \\ \mathbb{Q} &= \{ A \approx_m f \} \\ \mathbb{M} &= \{ X \mapsto B^1 \} \\ \mathbb{L} &= \{ \vdash A =_\eta \lambda x.B_x \}\end{aligned}$$

As per invariant 2 the  $\eta$ -link left-hand side is a variable while the right-hand side is a term in  $\diamond\eta$  that has the following property:

**INVARIANT 3 ( $\eta$ -link RHS).** *The rhs of any  $\eta$ -link has the shape  $\lambda x.t$  and  $t$  is  $\mathcal{W}$ .*

Each  $\eta$ -link is kept in the link store  $\mathbb{L}$  during execution and is activated under some conditions. Activation is implemented in section 5.3 by extending the `progress1` predicate defined in section 3.4.

## 5.1 Detection of $\diamond\eta$

When compiling a term  $t$ , we need to determine if any subterm  $s \in \mathcal{P}(t)$  that is of the form  $\lambda x.r$  can be an  $\eta$ -expansion, i.e. if there exists a substitution  $\rho$  such that  $\rho(\lambda x.r) =_o s$ . The detection of lambda abstractions that can “disappear” is not as trivial as it may seems. Here a few examples:

$$\begin{aligned}\lambda x.f(A x) &\in \diamond\eta \quad \rho = \{ A \mapsto \lambda x.x \} \\ \lambda x.f(A x) x &\in \diamond\eta \quad \rho = \{ A \mapsto \lambda x.a \} \\ \lambda x.f x(A x) &\notin \diamond\eta \\ \lambda x.\lambda y.f(A x)(B y x) &\in \diamond\eta \quad \rho = \{ A \mapsto \lambda x.x; B \mapsto \lambda y.\lambda x.y \}\end{aligned}$$

The first two examples are easy, and show how a unification variable can expose or erase a variable in its scope, turning the resulting term into an  $\eta$ -expansion or not.

The third example shows that when a variable occurs outside the scope of a unification variable, it cannot be erased and can hence prevent a term from being an  $\eta$ -expansion.

The last example shows the recursive nature of the check we need to implement. The term starts with a spine of two lambdas, hence the whole term is in  $\diamond\eta$  iff the inner term  $\lambda y.f(A x)(B y x)$  is in  $\diamond\eta$  itself. If it is, it could  $\eta$ -contract to  $f(A x)$  making  $\lambda x.f(A x)$  a potential  $\eta$ -expansion.

We can now detail how  $\diamond\eta$  terms are detected.

**Definition 5.1 (may-contract-to).** A  $\beta$ -normal term  $s$  may-contract-to a name  $x$  if there exists a substitution  $\rho$  such that  $\rho s =_o x$ .

**LEMMA 5.2.** *A  $\beta$ -normal term  $s = \lambda x_1 \dots x_n.t$  may-contract-to  $x$  only if one of the following three conditions holds:*

- (1)  $n = 0$  and  $t = x$ ;

- (2)  $t$  is the application of  $x$  to a list of terms  $l$  and each  $l_i$  may-contract-to  $x_i$  (e.g.  $\lambda x_1 \dots x_n.x x_1 \dots x_n =_o x$ );
- (3)  $t$  is a unification variable with scope  $W$ , and for any  $v \in \{x, x_1 \dots x_n\}$ , there exists a  $w \in W$ , such that  $w$  may-contract-to  $v$  (if  $n = 0$  this is equivalent to  $x \in W$ ).

**PROOF SKETCH.** Since our terms are in  $\beta$ -normal form there is only one rule that can play a role (namely  $\eta_l$ ), hence if the term  $s$  is not exactly  $x$  (case 1) it can only be an  $\eta$ -expansion of  $x$ , or a unification variable that can be assigned to  $x$ , or a combination of both. If  $s$  begins with a lambda, then the lambda can only disappear by  $\eta$  contraction. In that case, the term  $t$  under the spine of binders  $x_1 \dots x_n$  can either be  $x$  applied to terms that may-contract-to these variables (case 2), or a unification variable that can be assigned to the application  $x x_1 \dots x_n$  (case 3).  $\square$

**Definition 5.3 (occurs-rigidly).** A name  $x$  occurs-rigidly in a  $\beta$ -normal term  $t$ , if  $\forall \rho, x \in \mathcal{P}(\rho t)$

In other words,  $x$  occurs-rigidly in  $t$  if it occurs in  $t$  outside of the scope of a unification variable  $X$ ; otherwise, an instantiation of  $X$  can make  $x$  disappears from  $t$ . Moreover, note that  $\eta$ -contracting  $t$  cannot make  $x$  disappear, since  $x$  is not a locally bound variable inside  $t$ .

We can now describe the implementation of  $\diamond\eta$  detection:

**Definition 5.4 (maybe-eta).** Given a  $\beta$ -normal term  $s = \lambda x_1 \dots x_n.t$ , maybe-eta  $s$  holds if any of the following holds:

- (1)  $t$  is a constant or a name applied to the arguments  $l_1 \dots l_m$  such that  $m \geq n$  and for every  $i$  such that  $m - n < i \leq m$  the term  $l_i$  may-contract-to  $x_i$ , and no  $x_i$  occurs-rigidly in  $l_1 \dots l_{m-n}$ ;
- (2)  $t$  is a unification variable with scope  $W$  and for each  $x_i$  there exists a  $w_j \in W$  such that  $w_j$  may-contract-to  $x_i$ .

**LEMMA 5.5 ( $\diamond\eta$  DETECTION).** *If  $t$  is a  $\beta$ -normal term and  $t \in \diamond\eta$  then maybe-eta  $t$  holds.*

**PROOF SKETCH.** Follows from definition 5.3 and lemma 5.2  $\square$

Remark that the converse of lemma 5.5 does not hold: there exists a term  $t$  satisfying the criteria (1) of definition 5.4 that is not in  $\diamond\eta$ , i.e. there exists no substitution  $\rho$  such that  $\rho t$  is an  $\eta$ -expansion. A simple counter example is  $\lambda x.f(A x)(A x)$  since  $x$  does not occur-rigidly in the first argument of  $f$ , and the second argument of  $f$  may-contract-to  $x$ . In other words  $A x$  may either use or discard  $x$ , but our analysis does not take into account that the same term cannot have two contrasting behaviors.

As we will see in the rest of this section, this is not a problem since it does not break proposition 2.2 nor proposition 2.3.

## 5.2 Compilation and decompilation

**Compilation.** The following rule is inserted just before rule ( $c_\lambda$ ) from the code in section 3.3.

```
comp (flam F) (uva A Scope) M1 M2 L1 L3 S1 S3 :-
  maybe-eta (flam F) [], !,
  alloc S1 A S2,
  comp-lam F F1 M1 M2 L1 L2 S2 S3,
  get-scope (lam F1) Scope,
  L3 = [val (link-eta (uva A Scope) (lam F1)) | L2].
```



Whenever  $\text{flam } F$  is detected to be in  $\Diamond\eta$  it is compiled to  $\text{lam } F_1$  and replaced by the fresh variable  $A$ . This variable sees all the names free in  $\text{lam } F_1$  and is connected to  $\text{lam } F_1$  via a  $\eta$ -link. Invariant 2 (LINK LEFT HAND SIDE) holds for this link. Moreover:

**COROLLARY 5.6.** *The rhs of any  $\eta$ -link has exactly one lambda abstraction, hence the rule above respects invariant 3 ( $\eta$ -link RHS).*

**PROOF SKETCH.** By contradiction, suppose that the rule above is applied and that the rhs of the link is  $\lambda x.\lambda y.t$ , where  $x$  and  $y$  occur in  $t$ . If *maybe-eta*  $\lambda y.t$  holds then the recursive call to *comp* (made by *comp-lam*) must have put a fresh variable in its place, so this case is impossible. Otherwise, if *maybe-eta*  $\lambda y.t$  does not hold, then also *maybe-eta*  $\lambda x.\lambda y.t$  does not hold either, contradicting the assumption that the rule was applied.  $\square$

**LEMMA 5.7 ( $\mathcal{W}$ -PARTIAL-ENFORCEMENT).**  $\forall s, \langle s \rangle \mapsto (t, m, l)$ , if  $\mathcal{W}_{\beta\eta}(s)$  then  $\mathcal{W}(t)$ .

**PROOF SKETCH.** By lemma 5.5  $\square$

**Decompilation.** The decompilation of a  $\eta$ -link is performed by unifying the lhs with the rhs. Note that this unification never fails, since the lhs is a flexible term not appearing in any other  $\eta$ -link (by definition 5.9).

### 5.3 Progress

$\eta$ -links are meant to delay the unification of “problematic” terms until we know for sure if their head lambdas can be  $\eta$ -contracted or not.

**Definition 5.8 ( $\eta$ -progress-lhs).** A link  $\Gamma \vdash X =_{\eta} t$  is removed from  $\mathbb{L}$  when  $X$  becomes rigid. Let  $y \in \Gamma$ , there are two cases:

- (1) if  $X = a$  or  $X = y$  or  $X = f a_1 \dots a_n$  we unify the  $\eta$ -expansion of  $X$  with  $t$ , that is we run  $\lambda x.X x \simeq_m t$
- (2) if  $X = \lambda x.s$  we run  $X \simeq_m t$ .

**Definition 5.9 ( $\eta$ -progress-deduplicate).** A link  $\Gamma \vdash X_{\vec{s}} =_{\eta} T$  is removed from  $\mathbb{L}$  when another link  $\Delta \vdash X_{\vec{r}} =_{\eta} T'$  is in  $\mathbb{L}$ . By invariant 1 the length of  $\vec{s}$  and  $\vec{r}$  is the same; hence we can move the term  $T'$  from  $\Delta$  to  $\Gamma$  by renaming its bound variables, i.e.  $T'' = T'[\vec{r}/\vec{s}]$ . We then run  $T \simeq_m T''$  (under the context  $\Gamma$ ).

**LEMMA 5.10.** *Let  $\lambda x.t$  be the rhs of a  $\eta$ -link, then  $\mathcal{W}(t)$ , enforcing invariant 3.*

**PROOF SKETCH.** By lemma 5.7.  $\square$

**LEMMA 5.11.** *Given a  $\eta$ -link  $\Gamma \vdash X =_{\eta} \lambda x.t$ , the unification done by  $\eta$ -progress-lhs is between terms in  $\mathcal{W}$*

**PROOF SKETCH.** Let  $\sigma$  be a substitution such that  $\mathcal{W}(\sigma X)$  (since  $\simeq_m$  preserves  $\mathcal{W}$ ). By invariant 3, we have  $\mathcal{W}(t)$ . If  $\sigma X = \lambda x.r$ , then  $r$  is unified with  $t$  and both terms are in  $\mathcal{W}$ . Otherwise,  $\lambda x.Xx$  is unified with  $\lambda x.t$ , and  $Xx$  and  $t$  are both in  $\mathcal{W}$ .  $\square$

**LEMMA 5.12.** *The unification done by  $\eta$ -progress-deduplicate is between terms in  $\mathcal{W}$ .*

**PROOF.** Given two  $\eta$ -links  $\Gamma_1 \vdash X =_{\eta} \lambda x.t$  and  $\Gamma_2 \vdash Y =_{\eta} \lambda x.t'$  the unification is performed between  $t$  and  $t'$ . By lemma 5.7 both terms are in  $\mathcal{W}$ .  $\square$

**LEMMA 5.13.** *The progress of  $\eta$ -link guarantees proposition 2.11 ( $\mathcal{W}$ -PRESERVATION)*

**PROOF SKETCH.** By lemmas 5.11 and 5.12, every unification performed by the activation of a  $\eta$ -link is done between terms in  $\mathcal{W}$ .  $\square$

**LEMMA 5.14.** *progress terminates.*

**PROOF SKETCH.** Rules 5.8 and 5.9 remove one link from  $\mathbb{L}$ , hence they cannot be applied indefinitely. Moreover each rule only relies on terminating operations such as  $\simeq_m$ ,  $\eta$ -contraction,  $\eta$ -expansion, relocation (a recursive copy of a finite term).  $\square$

**THEOREM 5.15 (FIDELITY IN  $\mathcal{W}_{\beta\eta}$ ).** *Given a list of unification problems  $\mathbb{P}$  such that  $\mathcal{W}_{\beta\eta}(\mathbb{P})$ , if the memory map is bijective then the introduction of  $\eta$ -link guarantees proposition 2.3 (SIMULATION FIDELITY).<sup>1</sup>*

**PROOF SKETCH.** We want to prove that  $\text{step}_o$  succeeds iff  $\text{step}_m$  succeeds, where  $\text{step}_m$  is made by a unification step  $u$  and a link progression  $p$ . Without loss of generality we consider a  $O$  unification problem of the form  $s_1 \simeq_o s_2$  where  $s_1 \in \Diamond\eta$  and is compiled to a variable  $X$  with the accessory link  $\Gamma \vdash X =_{\eta} \lambda x.t_1$ . The unification  $u$  always succeeds, since  $X$  is a fresh variable, we only have to consider the link progression  $p$ . Two cases should be analyzed:

- $s_2 \notin \Diamond\eta$ , in this case  $s_2$  is compiled to  $t_2$  and the unification step  $u$  assigns  $t_2$  to  $X$  and triggers  $\eta$ -progress-lhs. If  $s_1$  and  $s_2$  unify thanks to  $\eta_1$  then progression  $p$  unifies  $\lambda x.t_1$  with the  $\eta$ -expansion of  $t_2$  namely  $\lambda x.t_2 x$ , otherwise they unify thanks to rule  $\lambda\lambda$  and the link progress  $p$  unifies  $\lambda x.t_1$  with  $t_2$ .  
Alternatively, if  $s_1$  and  $s_2$  fail to unify, it cannot be because of the  $\lambda$  constructor in the head of  $t_1$ , since rule  $\lambda\lambda$  or rule  $\eta_1$  try to unify under it, eventually finding two different subterms  $s'_1$  and  $s'_2$ . By theorem 4.3,  $\simeq_m$  fails on terms  $t'_1$  and  $t'_2$  even if  $\eta$ -progress-lhs  $\eta$ -expands  $t_2$ .
- $s_2 \in \Diamond\eta$ , in this case, the unification problem becomes  $X \simeq_m Y$  with the extra link  $\vdash Y =_{\eta} \lambda x.t_2$ . After the unification of  $X$  and  $Y$ ,  $\eta$ -progress-deduplicate is fired and, once the lambdas are crossed,  $t_1$  and  $t_2$  are unified. This unification succeeds iff  $s_1 \simeq_o s_2$  succeeds by theorem 4.3.

**COROLLARY 5.16 (PROPERTIES OF  $\simeq_o$  IN  $\mathcal{W}_{\beta\eta}$ ).** *Given the updated compilation scheme, the  $\simeq_o$  of section 3.6 is a good unification (as per proposition 2.1) for  $\simeq_o$  in the domain  $\mathcal{W}_{\beta\eta}$ .*

**Example of  $\eta$ -progress-lhs.** The example at the beginning of section 5, once  $\sigma = \{ A \mapsto f \}$ , triggers  $\eta$ -progress-lhs since the link becomes  $\vdash f =_{\eta} \lambda x.B_x$  and the lhs is a constant. This rule runs  $\lambda x.f x \simeq_m \lambda x.B_x$ , resulting in  $\sigma = \{ A \mapsto f ; B_x \mapsto f \}$ . Since  $X$  is mapped to  $B_x$  and  $B_x$  is decompiled into  $\lambda x.f x$ , by the definition of  $\eta$ -link decompilation  $\sigma$  is decompiled to  $\rho = \{ X \mapsto \lambda x.f x \}$ ,

<sup>1</sup>The bijective property of  $\mathbb{M}$  is proved in section 6

## 6 MAKING $\mathbb{M}$ A BIJECTION

We want to allow for partial application of functions. As a consequence the same unification variable  $X$  can be used multiple times with different arities. When it is the case the memory map  $\mathbb{M}$  generated by the compiler is not a bijection. For example:

$$\begin{aligned}\mathbb{P} &= \{ \lambda x. (X x) \approx_o a \quad X \approx_o \lambda x. a \} \\ \mathbb{Q} &= \{ \quad \quad \quad A \approx_m a \quad C \approx_m \lambda x. a \} \\ \mathbb{M} &= \{ X \mapsto C^0 \quad X \mapsto B^1 \} \\ \mathbb{L} &= \{ \vdash A =_\eta \lambda x. B_x \}\end{aligned}$$

in the unification problems  $\mathbb{P}$  above,  $X$  is used with arity 1 in  $\mathbb{P}_1$  and with arity 0 in  $\mathbb{P}_2$ . In order to preserve invariant 1 (UNIFICATION-VARIABLE ARITY) the compiler did generate two entries in  $\mathbb{M}$  for  $X$ , namely  $B$  and  $C$ . However, there is no connection between these two variables and any incompatible assignments to them would not be detected, in turn breaking proposition 2.3. To address this issue, we post-process the compiler's output to ensure the following property:

**PROPOSITION 6.1 ( $\mathbb{M}$  IS A BIJECTION).** *After compilation, for each  $O$ -variable  $X$  in  $\mathbb{P}$  and for each  $M$ -variable  $A$  in  $\mathbb{Q}$  there is exactly one entry  $X \mapsto A^n$  (for an arity  $n$ ).*

Note that the execution in  $\mathcal{M}$  may require allocating new variables as explained in section 3.5. The property above does not concern these variables since they are fresh and have no corresponding variable in  $\mathcal{O}$ .

The core procedure of this post-processing step is *align-arity* that is iterated by map-deduplication:

**Definition 6.2 (align-arity).** Given two mappings  $m_1 : X \mapsto A^m$  and  $m_2 : X \mapsto C^n$  where  $m < n$  and  $d = n - m$ , *align-arity*  $m_1 m_2$  generates the following  $d$  links, one for each  $i$  such that  $0 \leq i < d$ ,

$$x_0 \dots x_{m+i} \vdash B_{x_0 \dots x_{m+i}}^i =_\eta \lambda x_{m+i+1}. B_{x_0 \dots x_{m+i+1}}^{i+1}$$

where  $B^i$  is a fresh variable of arity  $m + i$ , and  $B^0 = A$  as well as  $B^d = C$ .

The intuition is that we  $\eta$ -expand the occurrence of the variable with lower arity to match the higher arity. Since each  $\eta$ -link can add exactly one lambda, we need as many links as the difference between the two arities.

**Definition 6.3 (map-deduplication).** For all mappings  $m_1, m_2 \in \mathbb{M}$  such that  $m_1 : X \mapsto A^m$  and  $m_2 : X \mapsto C^n$  and  $m < n$  we remove  $m_1$  from  $\mathbb{M}$  and add to  $\mathbb{L}$  the result of *align-arity*  $m_1 m_2$ .

**THEOREM 6.4 (FIDELITY WITH MAP-DEDUPLICATION).** *Given a list of unification problems  $\mathbb{P}$ , such that  $\mathcal{W}_{\beta\eta}(\mathbb{P})$ , if  $\mathbb{P}$  contains the same  $O$  variable used at different arities, then map-deduplication guarantees proposition 2.3 (SIMULATION FIDELITY)*

**PROOF SKETCH.** Let  $X$  a  $O$  variable appearing in two different subterms  $s_1$  and  $s_2$ . In  $s_1$ ,  $X$  is applied to the list of terms  $x_1 \dots x_m$  and, in  $s_2$ , it is applied to  $y_1 \dots y_n$ . Here,  $m < n$ . By *map-deduplication*, the compilation generates two distinct  $M$  variables for  $X$ , say  $A$  and  $C$ , related by a chain  $C$  of  $\eta$ -links, with length  $n - m$ . We prove that if  $A$  and  $C$  are assigned respectively to the  $\mathcal{W}$  terms  $\lambda x_1 \dots x_p. t_1$  and  $\lambda x_1 \dots x_q. t_2$ , then a unification failure occurs iff these terms fails to unify. By  $\eta$ -progress-lhs, the assignment of  $A$  activates  $p$  links of  $C$ . In particular, when  $p \geq n$ , then the entire chain

collapses and unification is performed between  $\lambda x_n \dots x_p. t_1$  and  $\lambda x_1 \dots x_q. t_2$ . By theorem 4.3 and since we work with  $\mathcal{W}$  terms, this unification succeeds iff it does in  $\mathcal{O}$ . If, otherwise,  $p < n$ , then two cases should be considered:  $t_1$  is a rigid term, then,  $t_2$  must be an eta expansion of  $t_1$ , implying  $q = 0$ . If it is not the case, then the successive progressions made by  $\eta$ -progress-lhs would eventually cause a failure. If, on the other hand,  $t_1$  is a unification variable, scope-check guarantees that  $t_2$  does not use  $x_1 \dots x_p$  as free variables, causing otherwise a unification failure. After this check,  $m - n - p$  links of  $C$  remain suspended, as we expect: decompilation is charged wake all links in  $\mathbb{L}$  and eventually relate  $t_1$  with  $t_2$ .  $\square$

*Example of map-deduplication.* If we take back the example given at the beginning of this section, *map-deduplication* produces the following result:

$$\begin{aligned}\mathbb{P} &= \{ \lambda x. (X x) \approx_o a \quad X \approx_o \lambda x. a \} \\ \mathbb{Q} &= \{ \quad \quad \quad A \approx_m a \quad C \approx_m \lambda x. a \} \\ \mathbb{M} &= \{ X \mapsto B^1 \} \\ \mathbb{L} &= \{ \vdash C =_\eta \lambda x. B_x \quad \vdash A =_\eta \lambda x. B_x \}\end{aligned}$$

Note that  $X \mapsto C^0$  disappears from  $\mathbb{M}$  in favour of the auxiliary  $\eta$ -link  $\vdash C =_\eta \lambda x. B_x$ . The resolution of  $\mathbb{Q}_1$  assigns  $a$  to  $A$ . This wakes up  $\mathbb{L}_2$  by  $\eta$ -progress-lhs, assigning  $\langle a x \rangle$  to  $B_x$ . The resolution of  $\mathbb{Q}_2$  instantiates  $C$  to  $\lambda x. a$ , which, in turn triggers  $\mathbb{L}_1$  by  $\eta$ -progress-lhs. This link performs  $\lambda x. a \approx_m \lambda x. (a x)$ , which, as expected, fails.

## 7 HANDLING OF $\diamond \mathcal{L}$

As observed in [15] it is worth handling terms in  $\diamond \mathcal{L}$  since, in practice, these terms often re-enter  $\mathcal{L}$  at runtime.

In the following example problem  $\mathbb{P}_2$ , namely  $X a \approx_o a$ , admits two different solutions:  $\rho_1 = \{ X \mapsto \lambda x. x \}$  and  $\rho_2 = \{ X \mapsto \lambda x. a \}$ . The unification algorithm alone cannot chose the right one, since no solution is more general than the other, but for the first problem the choice is obvious, and in turn this choice makes  $\mathbb{P}_2 \subseteq \mathcal{L}$ :

$$\begin{aligned}\mathbb{P} &= \{ X \approx_o \lambda x. a \quad (X a) \approx_o a \} \\ \mathbb{Q} &= \{ A \approx_m \lambda x. a \quad (A a) \approx_m a \} \\ \mathbb{M} &= \{ X \mapsto A^0 \}\end{aligned}$$

In order to support this scenario we have to improve a little our compiler and progress routines. In particular  $\mathbb{Q}_1$  generates  $\sigma = \{ A \mapsto \lambda x. a \}$  making  $\sigma \mathbb{Q}_2$  equal to  $(\lambda x. a) a \approx_m a$  that  $\approx_m$  cannot solve since it lacks rules  $\beta_l$  and  $\beta_r$ .

To address this problem the compiler must recognize and replace  $\diamond \mathcal{L}$  terms with fresh variables and generate a new kind of links that we call  $\mathcal{L}$ -link.

In addition to invariant 2 (LINK LEFT HAND SIDE), the term on the rhs of a  $\mathcal{L}$ -link has the following property:

**INVARIANT 4 ( $\mathcal{L}$ -link RHS).** *The rhs of any  $\mathcal{L}$ -link has the shape  $X_{s_1 \dots s_n} t_1 \dots t_m$  where  $X$  is a unification variable in  $\mathcal{L}$  (with scope  $s_1 \dots s_n$ ) and  $t_1 \dots t_m$  is a list of terms such that  $m > 0$  and  $t_1$  is either a variable occurring in  $s_1 \dots s_n$  or a term other than a variable.*

Note that the shape of such as rhs is  $\langle \text{app } [\text{uva } X \ S \mid \mathbb{L}] \rangle$ , where  $S$  is  $[s_1, \dots, s_n]$  and  $\mathbb{L}$  is  $[t_1, \dots, t_m]$ .

## 7.1 Compilation and decompilation

We insert this rule just before rule ( $c_{@}$ ):

```
comp (fapp [fuva A|Ag]) (uva B Scope) M1 M3 L1 L3 S1 S4 :- !,
  pattern-fragment-prefix Ag Pf Extra, alloc S1 B S2,
  len Pf Ar, m-alloc (fv A) (hv C (arity Ar)) M1 M2 S2 S3,
  fold6 comp Pf Pf1 M2 M2 L1 L1 S3 S3,
  fold6 comp Extra Extra1 M2 M3 L1 L2 S3 S4,
  Beta = app [uva C Pf1 | Extra1],
  get-scope Beta Scope,
  L3 = [val (link-lam (uva B Scope) Beta) | L2].
```

The list  $Ag$  is split into two parts:  $Pf$  is in  $\mathcal{L}$ , and can be empty;  $Extra$  cannot be empty and is such that «append  $Pf Extra Ag$ ». The rhs of the  $\mathcal{L}$ -link is the application of a fresh variable  $C$  having in scope all names in  $Pf1$  (the compilation of  $Pf$ ). The variable  $B$ , returned as the compiled term, is a fresh variable having in scope all the free variables occurring in  $Pf1$  and  $Extra1$  (the compilation of  $Extra$ ). This construction enforces invariant 4.

LEMMA 7.1 ( $\mathcal{W}$ -ENFORCEMENT).  $\forall s, \langle s \rangle \mapsto (t, m, l)$ , then  $\mathcal{W}(t)$ .

PROOF SKETCH. By lemmas 4.2 and 5.7 and the rule above.  $\square$

*Decompilation.* All  $\mathcal{L}$ -link should be solved before decompilation. If any  $\mathcal{L}$ -link remains in  $\mathbb{L}$ , decompilation fails.

## 7.2 Progress

Let  $l$  be a  $\mathcal{L}$ -link of the form  $\Gamma \vdash T =_{\mathcal{L}} X_{s_1 \dots s_n} \cdot t_1 \dots t_m$

*Definition 7.2 ( $\mathcal{L}$ -progress-refine).* Let  $\sigma$  be a substitution such that  $\sigma t_1$  is a name  $s$  not occurring in  $s_1 \dots s_n$ . If  $m = 1$ , then  $l$  is removed and the lhs is unified with  $X_{s_1 \dots s_n} s$ . If  $m > 1$ , then  $l$  is replaced by the link  $\Gamma \vdash T =_{\mathcal{L}} Y_{s_1 \dots s_n} s \cdot t_2 \dots t_m$  (where  $Y$  is a fresh variable of arity  $n + 1$ ) and link  $\Gamma \vdash X_{s_1 \dots s_n} =_{\eta} \lambda x. Y_{s_1 \dots s_n} x$  is added to  $\mathbb{L}$ .

*Definition 7.3 ( $\mathcal{L}$ -progress-rhs).* Link  $l$  is removed from  $\mathbb{L}$  if  $X_{s_1 \dots s_n}$  is instantiated to a term  $t$  and  $t \cdot t_1 \dots t_m \beta$ -reduces to a  $t' \in \mathcal{L}$ .

*Definition 7.4 ( $\mathcal{L}$ -progress-fail).* progress fails whenever:

- (1) there exists another link  $l' \in \mathbb{L}$  with the same lhs as  $l$ ; or
- (2) the lhs of  $l$  become rigid.

We relax this condition and accommodate for heuristics in 7.3.

Finally we introduce the following  $\eta$ -link progress rule.

*Definition 7.5 ( $\eta$ -progress-rhs).* A link  $\Gamma \vdash X =_{\eta} T$  is removed from  $\mathbb{L}$  when either

- *maybe-eta*  $T$  does not hold (anymore). In this case  $X$  is unified with  $T$
- $T \eta$ -contracts to a term  $T'$  not starting with the  $\text{lam}$  constructor. In this case  $X$  is unified with  $T'$

The idea behind this rule is to instantiate the lhs variable of a  $\eta$ -link whenever its rhs is for sure a  $\mathcal{W}$  term.

LEMMA 7.6. progress terminates

PROOF SKETCH. Let  $l = \Gamma \vdash T =_{\mathcal{L}} X_{s_1 \dots s_n} \cdot t_1 \dots t_m$  a  $\mathcal{L}$ -link in the store  $\mathbb{L}$ . The progression made by  $\mathcal{L}$ -progress-rhs terminates:

$l$  is removed from  $\mathbb{L}$  after having performed terminating instructions. If  $l$  is not activated by  $\mathcal{L}$ -progress-rhs, then  $X$  is a variables. The activation of  $\mathcal{L}$ -progress-refine replaces  $l$  with the new  $\mathcal{L}$ -link  $\Gamma \vdash T =_{\mathcal{L}} Y_{s_1 \dots s_n} s \cdot t_2 \dots t_m$ . This progression can be triggered at most  $m$  times, since at each time the number of terms applied to  $X$  decreases. In the end  $l_m$  will be removed from  $\mathbb{L}$  after the unification between lhs with rhs. We also note that each  $\mathcal{L}$ -progress-refine generates a  $\eta$ -link, yet, according to lemma 5.14, this does not affect termination.  $\mathcal{L}$ -progress-fail terminates since it stop progression with failure. Finally,  $\eta$ -progress-rhs performs terminating operations and, if it permits succeed, the considered  $\eta$ -link is removed from  $\mathbb{L}$ , ensuring termination.  $\square$

THEOREM 7.7 (FIDELITY IN  $\mathcal{O}$ ). The introduction of  $\mathcal{L}$ -link guarantees proposition 2.4 (FIDELITY RECOVERY) if  $\mathbb{P} \not\subseteq \mathcal{L}$ .

PROOF SKETCH. Let  $\mathbb{P}_i$  be the first problem in  $\mathbb{P}$ , such that it is not in  $\mathcal{L}$  and let  $\sigma$  be the substitution obtained solving  $Q_1 \dots Q_{i-1}$ . After the execution of step  $m$  for the  $i - 1$  time, each variable  $X$  corresponding to a  $\diamond \eta$  subterm in the original  $\mathcal{O}$  unification problem, is instantiated if it is known for sure that  $X$  is no more a  $\diamond \eta$ . If  $\sigma Q_i$  is in  $\mathcal{L}$ , then by definitions 7.2 and 7.3 the associated  $\mathcal{L}$ -link is solved and removed. Note that, in this case, all calls to  $\approx_m$  are between terms in  $\mathcal{L}$  and by theorem 4.3 fidelity is guaranteed. If  $\sigma Q$  is still in  $\diamond \mathcal{L}$ , then by definition 7.4 unification fails as the corresponding unification in  $\mathcal{O}$  would.  $\square$

## 7.3 Relaxing definition 7.4 ( $\mathcal{L}$ -PROGRESS-FAIL)

Working with terms in  $\mathcal{L}$  is sometime too restrictive [1] and we could find in literature a few strategies to go beyond  $\mathcal{L}$  without implementing Huet's algorithm [11]. Some implementations of  $\lambda$ Prolog [16] such as Teyjus [15] delay the resolution of  $\diamond \mathcal{L}$  unification problems until the substitution makes them reenter  $\mathcal{L}$ . Other systems apply heuristics like preferring projection over mimic, and commit to that solution. This is the case for the unification algorithm Coq uses in its type-class solver [23],

In this section we show how we can implement these strategies by simply adding (or removing) rules to the progress predicate. In the example below  $\mathbb{P}_1$  is in  $\diamond \mathcal{L}$ .

$$\begin{aligned} \mathbb{P} &= \{ (X \ a) \approx_o \ a \quad X \approx_o \ \lambda x. Y \} \\ \mathbb{Q} &= \{ \quad A \approx_m \ a \quad B \approx_m \ \lambda x. C \} \\ \mathbb{M} &= \{ \quad Y \mapsto C^0 \quad X \mapsto B^0 \} \\ \mathbb{L} &= \{ \vdash A =_{\mathcal{L}} (B \ a) \} \end{aligned}$$

If we want the object-language unification to delay the first unification problem (waiting for  $X$  to be instantiated), we can relax definition 7.4. Instead of failing when the lhs of  $\mathbb{L}_1$  becomes rigid (equal to  $a$ ), we keep it in  $\mathbb{L}$  until the head of its rhs also become rigid. In this case, since both the lhs and rhs have rigid heads, they can be unified. While this relaxed rule does not break proposition 2.3 (SIMULATION FIDELITY) per se, the occur-check-links procedure becomes incomplete since invariant 2 (LINK LEFT HAND SIDE) is broken. Also note that delaying unification outside  $\mathcal{L}$  can leave  $\mathcal{L}$ -link for the decompilation phase. Therefore commit-links should be modified accordingly.

If instead we want  $\approx_o$  to follow the second strategy and pick an arbitrary solution we can modify progress by applying the desired heuristic instead of failing. For instance, in  $X \ a \ b = Y \ b$ , the last



argument of the two terms is the same and unification can succeed by assigning  $Xa$  to  $Y$ . This heuristic is used by [23].

## 8 ACTUAL IMPLEMENTATION IN ELPI

In this paper we did study a compiler and a simulation loop on a minimal language. The actual implementation uses the Coq-Elpi meta language to both compile the sequence of problems (the rules) and execute them, that is Elpi plays the role of  $\mathcal{M}$  as well.

The main difference is that we cannot implement  $\text{run}_m$  since it comes as part of the runtime of the programming language. In particular the runtime iterates  $\approx_m$ , but  $\text{step}_m$  also needs to check links for progress. Luckily Elpi extends  $\lambda\text{Prolog}$  with constraints (suspended goals) and Constraint Handling Rules (CHR) to operate on them [10, 7]. An important design choice is that constraints are suspended on a user computed list of unification variables, and are resumed *as soon as one of them is assigned*, before any other existing goal is considered. In turn this property allows for a timely activation of links that grants proposition 2.3 (SIMULATION FIDELITY). For brevity we only provide two pseudo-code snippets. The first one depicts how  $\eta$ -link are suspended or make progress.

```
link-eta L R :- not (var L), !, eta-progress-lhs L R.
link-eta L R :- not (maybe-eta R), !, eta-progress-rhs L R.
link-eta L R :- declare_constraint (link-eta L R) [L,R].
```

The second snippet illustrates the deduplication of  $\eta$ -link. The syntax  $\langle N \triangleright G \ ?- P \rangle$  denotes a  $\lambda\text{Prolog}$  sequent, that is a goal  $P$  under a set  $G$  of hypothetical rules (introduced by  $\Rightarrow$ ) and where the program context binds (via the  $\pi$  operator)  $N$  heigenvariables. Sequents are presented to CHR with their higher order unification variables replaced by “frozen constants”, that is  $A_{xyz}$  becomes  $\langle \text{uvar } f_A \ [x,y,z] \rangle$  for a fresh constant  $f_A$ . Frozen constants are “defrost” when they are part of a new goal (see [10, section 4.3]).

```
rule (N1  $\triangleright$  G1  $\ ?- \text{link-eta } (\text{uvar } X \text{ LX1}) \ T1$ ) T1 % match
    / (N2  $\triangleright$  G2  $\ ?- \text{link-eta } (\text{uvar } X \text{ LX2}) \ T2$ ) T2 % remove
    | (relocate LX1 LX2 T2 T2') % condition
    <=> (N1  $\triangleright$  G1  $\ ?- \ T1 = T2'$ ) % new goal
```

The first directive matches a constraint whose lhs is a variable  $X$ ; the second matches and removes a constraint on the same variable; the third relocates  $T2$  and the latter crafts the new goal that unifies  $T1$  with  $T2'$  under  $G1$  and the set of heigenvariables  $N1$ .

## 9 RELATED WORK AND CONCLUSION

Object-language terms can be unified using different strategies.

The first approach that comes to mind consist in implementing  $\approx_o$  as a regular routine, i.e. write rules as follows

```
decision X :- unify X (all A x\ app [P, x]), finite A,
    pi x\ decision (app [P, x]).
```

where  $\text{unify}$  is a regular predicate. This method fails to take advantage of the logic programming engine provided by the meta language since it removes all data from rule's heads and makes indexing degrade. Additionally, implementing a unification procedure in the meta language is likely to be significantly slower compared to the built-in one.

Another possibility is to avoid having application and abstraction nodes in the syntax tree, and use the meta language ones, as in:

```
decision (all A x\ P x) :- finite A, pi x\ decision (P x).
```

However, this encoding has two big limitations. First it is not always feasible to adopt it for Coq due to the fact that the type system of the meta language is too limited to accommodate for that one the object language, e.g. Coq can typecheck variadic functions [2]. Second, the encoding of Coq terms provided by Elpi is primarily used for meta programming, i.e. to extend the Coq system. Consequently, it must be able to manipulate terms that are not known in advance without relying on introspection primitives such as Prolog's  $\text{functor}/3$  and  $\text{arg}/3$ . To avoid that constants cannot be symbols of the meta language, but must rather live in an open world, akin to the `string` data type used in the `con` constructor.

In the literature we could find a related encoding of the Calculus of Constructions (CC) [4]. The goal of that work is to exhibit a logic program performing proof checking for CC and hence relate the proof system of intuitionistic higher-order logic (that animates  $\lambda\text{Prolog}$  programs) with the one of CC. The encoding is hence tailored toward a different goal, for example it utilizes three relations to represent the equational theory of CC, and that choice alone makes things harder for us. Section 6 contains a discussion about the use of the unification procedure of the meta language in presence of non ground goals, but the authors are not interested in exploiting a decidable fragment of higher-order unification but are rather leaning towards an interactive system where the user is given control over the search procedure.

Another work that is, somewhat surprisingly, only superficially related to ours is the type-class engine built in Isabelle's meta language. In [29] classes identify (simple) types, they are not higher order predicates as in Coq, hence the solver does not require a higher-order unification procedure.

*The approach presented in this paper* addresses all the concerns mentioned earlier. It takes advantage of the unification capabilities of the meta language at the price of handling problematic sub terms on the side. As a result our encoding takes advantage of indexing data structures and mode analysis for clause filtering. It is worth mentioning that we replace terms with variables only when it is strictly needed, leaving the rest of the term structure intact and hence indexable. Some preliminary benchmarks on the `Stdpp` library [12] show encouraging results: our type-class solver has some overhead on small goals but is consistently faster than the Coq one starting from goals made of XXX nodes. Moreover our approach is flexible enough to accommodate different strategies and heuristics to handle terms outside the pattern fragment. Finally, our encoding is sufficiently shallow to: 1) allow to lift forms of static analysis for the meta language, such as determinacy, also to the object language; 2) take advantage of future improvements to the logic-programming engine of Elpi, such as tabled search.

We considered mechanizing our results with Abella [8], especially in the early phase of this work, but unfortunately we could not. The main reason is that the logic of Abella does not let one quantify over predicates nor use the cut operator. Eliminating cut or higher order combinators like `map` or `forall2` is surely possible, but has a high cost in verbosity given how pervasively they are used. Moreover, and more importantly, it creates a distance between the verified and the actual code, which we found to hinder our exploration.

D:  
how  
many?



We did, however, establish a test suite with about a hundred cases. It is available at <https://github.com/FissoreD/ho-unif-for-free>.

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