

TSPi Quality Plan: Form SUMQ

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Part/Level	Iteration 1	Cycle	1

Summary Rates	Plan	Actual
LOC/hour	750	725
% Reuse (% of total LOC)	-	-
% New Reuse (% of N&C LOC)	-	-
Percent Defect-Free (PDF)		
In compile	-	-
In unit test	-	-
In build and integration	-	-
In system test	-	-
Defect/page		
Requirements inspection	-	-
HLD inspection	-	-
Defects/KLOC		
DLD review	-	-
DLD inspection	-	-
Code review	1	0.001
Compile	1	0.001
Code inspection	0.8	0.024
Unit test	0.8	0.001
Build and integration	-	-
System test	-	-
Total development	-	-
Defect Ratios		
Code review/Compile	-	-
DLD review/Unit test	-	-
Development time ratios (%)		
Requirements inspection/Requirements	-	-
HLD inspection/HLD	-	-
DLD/code	-	-
DLD review/DLD	-	-
Code review/code	-	-
A/FR		
Review rates		
DLD lines/hour	-	-
Code LOC/hour	-	-
Inspection rates		
Requirement pages/hour	-	-
HLD pages/hour	0.55	0.57
DLD lines/hour	-	-
Code LOC/hour	-	-

(continued)

TSPi Quality Plan: Form SUMQ (continued)

Defect-injection Rates (Defects/Hr.)

	Plan	Actual
Requirements	-	-
HLD	-	-
DLD	-	-
Code	-	-
Compile	-	-
Unit test	-	-
Build and integration	-	-
System test	-	-

Defect-removal Rates (Defects/Hr.)

Requirements inspection	-	-
HLD inspection	-	-
DLD review	-	-
DLD inspection	-	-
Code review	-	1
Compile	-	-
Code inspection	-	-
Unit test	-	0.95
Build and integration	-	-
System test	-	-
	-	-

Phase Yields

Requirements inspection	-	-
HLD inspection	-	-
DLD review	-	-
Test development	-	-
DLD inspection	-	-
Code review	-	-
Compile	-	-
Code inspection	-	-
Unit test	-	-
Build and integration	-	-
System test	-	-

Process Yields

% before compile	-	-
% before unit test	-	-
% before build and integration	-	-
% before system test	-	80
% before system delivery	-	-