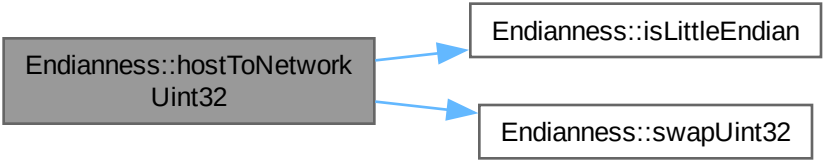


Endianness::hostToNetwork  
Uint32



```
graph LR; A[Endianness::hostToNetwork<br/>Uint32] --> B[Endianness::isLittleEndian]; A --> C[Endianness::swapUint32];
```

Endianness::isLittleEndian

Endianness::swapUint32