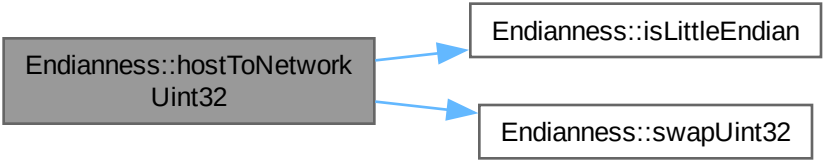


Endianness::hostToNetwork
Uint32



```
graph LR; A[Endianness::hostToNetwork  
Uint32] --> B[Endianness::isLittleEndian]; A --> C[Endianness::swapUint32];
```

The diagram illustrates a branching logic for the `Endianness::hostToNetwork Uint32` operation. A central gray box on the left contains the text `Endianness::hostToNetwork Uint32`. Two blue arrows originate from the right side of this box. The top arrow points to a white box containing `Endianness::isLittleEndian`. The bottom arrow points to another white box containing `Endianness::swapUint32`.

Endianness::isLittleEndian

Endianness::swapUint32