

### Profiling GPU Shaders for Profile-Guided Optimizations

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July 25, 2019

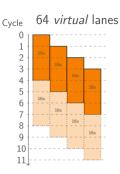


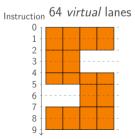


#### **GPUs**

Hardware

- ► SIMD-units with 16 lanes
- ► Pipeline with 4 stages
- ► Virtually 64 SIMD lanes
- Diverging control flow by masking lanes (SIMT)
- ► AMD Radeon VII has 240 SIMD units





#### Vulkan

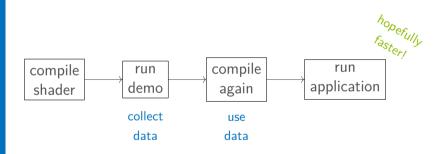
Software

- Graphics and compute standard for GPUs
- ► Shaders are loaded in SPIR-V
- ► Compilation to ISA happens in driver



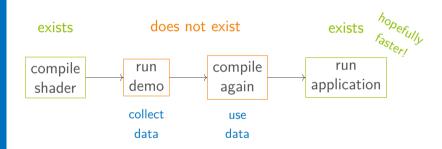
### Workflow

Profile-Guided Optimization



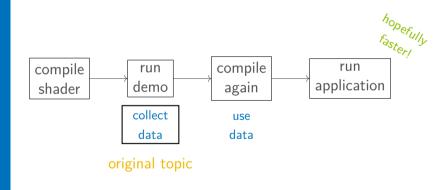
#### Current State

Profile-Guided Optimization



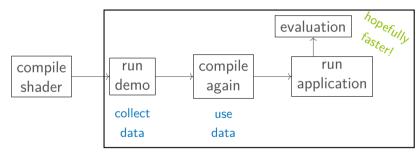
### This Thesis

Profile-Guided Optimization



#### This Thesis

Profile-Guided Optimization

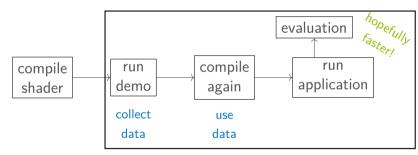


new topic

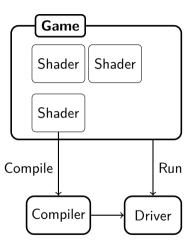
#### This Thesis

Profile-Guided Optimization

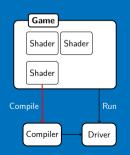
#### everything works 🔉



new topic

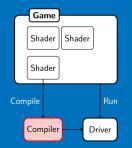


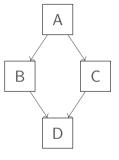
#### GLSL/SPIR-V

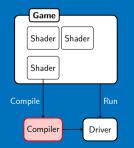


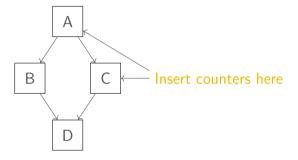
- ► GLSL gets precompiled to SPIR-V
- ► SPIR-V is passed to driver

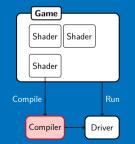
```
if (inputPos.x < 0.5) {
   outColor = vec4(1.0, 0.0, 0.0, 1.0);
} else {
   outColor = vec4(0.0, 0.0, 1.0, 1.0);
}</pre>
```

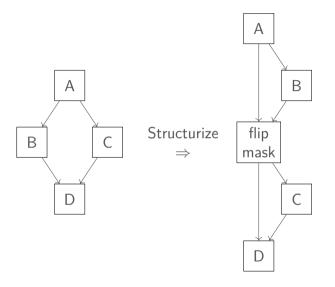


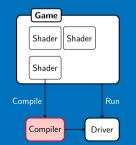


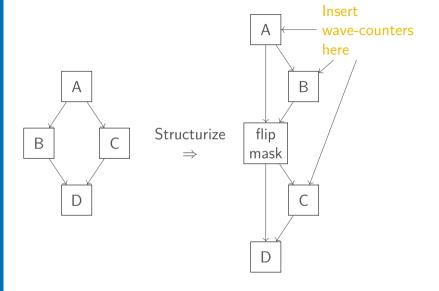




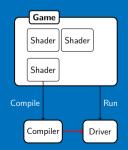






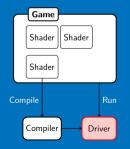


#### **ELF**



► ELF file contains metadata and sections for counters

Save Counter



- ► Counters are saved when the pipeline is destroyed and every 10 s
- ► Fetch counters from GPU memory
- ► Write counters and metadata to file

Result



- Declares pixel shader as hot and vertex shader as unlikely
- ► Changes basic block ordering

Some diagrams here

#### Future Work



### ► Find dynamically uniform variables

- ► Create some interesting statistics, e.g. unused basic blocks, uniform branches
- ► More benchmarks
- ► (More optimizations)

# Questions?