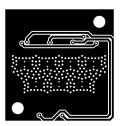
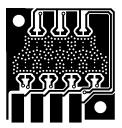
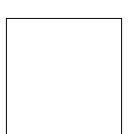
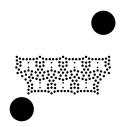
- 2. solder
- 3. component
- 4. outline
- 5. componentmask
- 6. soldermask
- 7. plated-drill
- 8. topsilk
- 9. bottomsilk
- 10. toppaste
- 11. topassembly
- 12. bottomassembly
- 13. fab

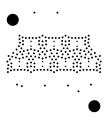


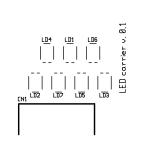




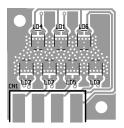


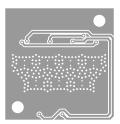








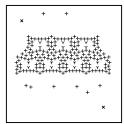




There are 3 different drill sizes used in this layout, 201 holes total

Symbol Diam. (Inch) Count Plated?

_					
	Y	0.015	<u>14</u>	YES	Title: LED carrier v. 0.1 - Fabrication Drawing
	+	0.020	<u>185</u>	YES	Author: David Ellsworth
	×	0.125	2	<u>YES</u>	Date: Sun Jun 28 01:40:46 2009 UTC



Board outline is the centerline of this path