

Fabrication information

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.025mm	SM-002	Solder Mask	GTS
Nickel, Gold	Top Surface Finish	0.017mm		Surface Finish	
CF-004	Top Layer	0.017mm		Signal	GTL
Prepreg		0.210mm	PP-006	Dielectric	
CF-004	Layer 1	0.015mm		Signal	G1
Core		1.065mm	Core-043	Dielectric	
CF-004	Layer 2	0.015mm		Signal	G2
Prepreg		0.210mm	PP-006	Dielectric	
CF-004	Bottom Layer	0.017mm		Signal	GBL
Nickel, Gold	Bottom Surface Finish	0.017mm		Surface Finish	
Surface Material	Bottom Solder	0.025mm	SM-002	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

Total thickness: 1.635mm

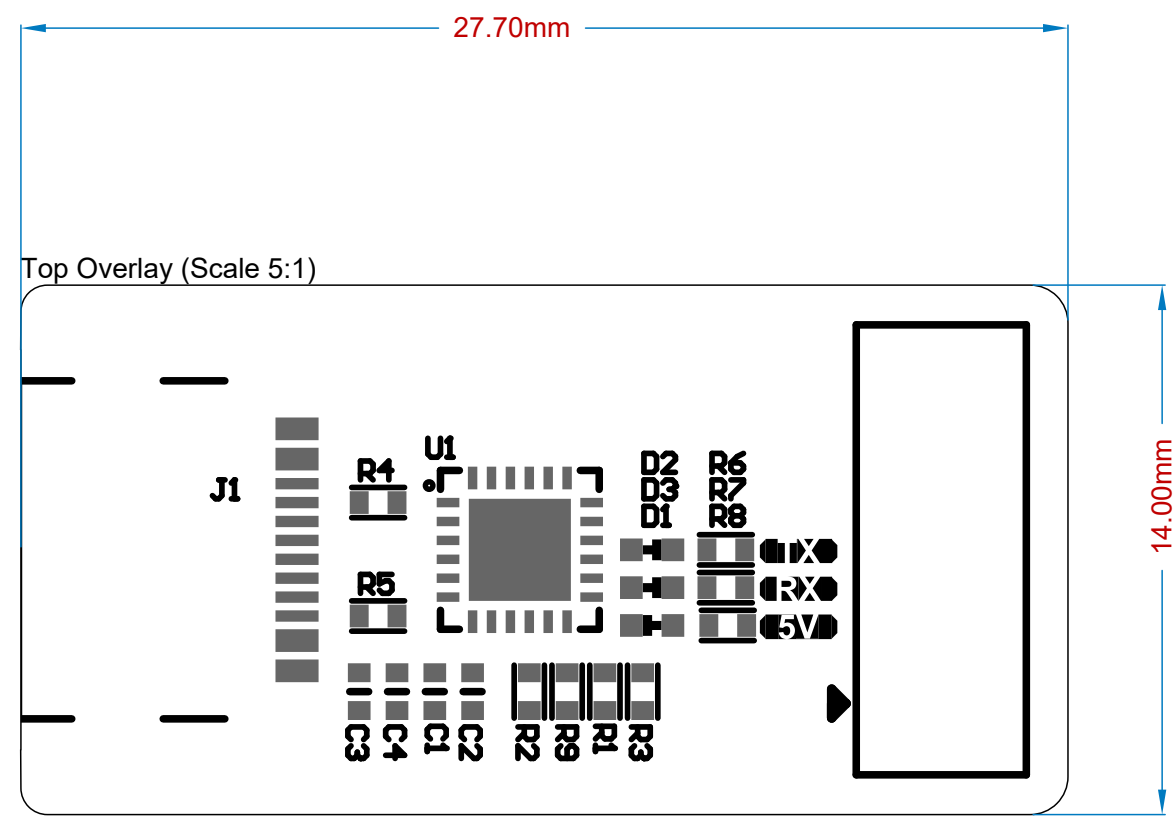
Fabrication Specifications

BASE MATERIAL:	FR-4
TG:	≥145
FINISH	
SURFACE FINISH:	See layer stack
HARD GOLD (CONTACT AREA):	No
ROHS COMPLAINT:	Yes
LEADFREE:	Yes
MARKING	
BASE MATERIAL UL MARKING:	Yes, on Bottom
DATECODE:	Yes, on Bottom
SILKSCREEN	
TOP:	Yes
BOTTOM:	Yes
COLOR:	White
SOLDERMASK	
TOP:	Yes
BOTTOM:	Yes
COLOR:	Matte black
VIA'S	
SOLDERRESIST ON VIA:	Yes
PLUGGING:	No

Transmission Line Structure Table

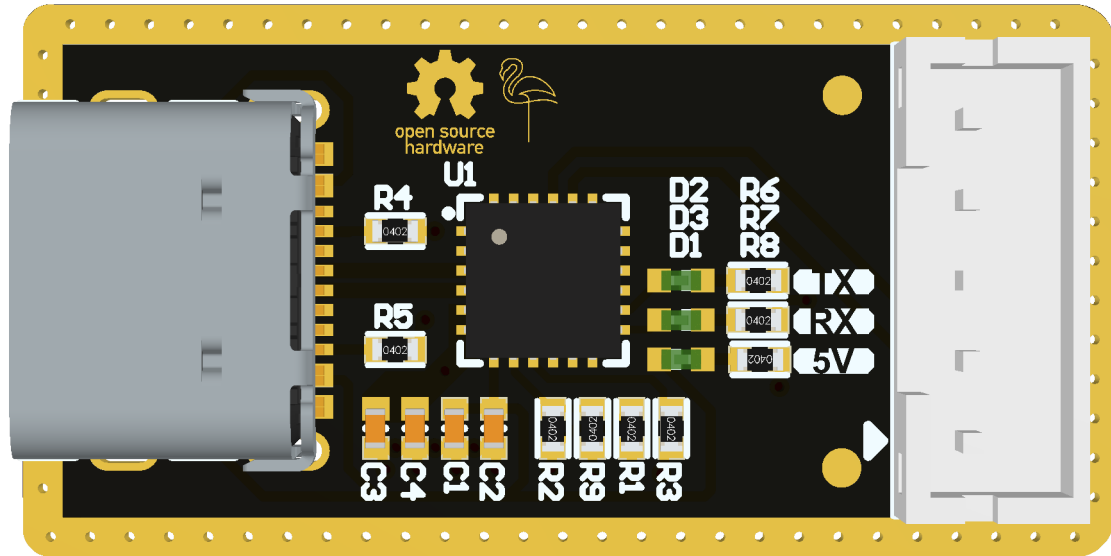
Impedance Id	Transmission Line	Target Impedance	Calculated Impedance	Trace layer	Lower Trace Width	Upper Trace Width	Reference layers	Substack
1	Coated Microstrip	50	50.01	Top Layer	0.39mm	0.39mm	Layer 1	Board Layer Stack
2	Offset Stripline	50	50.00	Layer 1	0.29mm	0.29mm	Top Layer,Layer 2	Board Layer Stack
3	Offset Stripline	50	50.00	Layer 2	0.29mm	0.29mm	Layer 1,Bottom Layer	Board Layer Stack
4	Coated Microstrip	50	50.01	Bottom Layer	0.39mm	0.39mm	Layer 2	Board Layer Stack

Component placement - TOP & BOT



Realistic view - TOP & BOT

Realistic View



Realistic View

