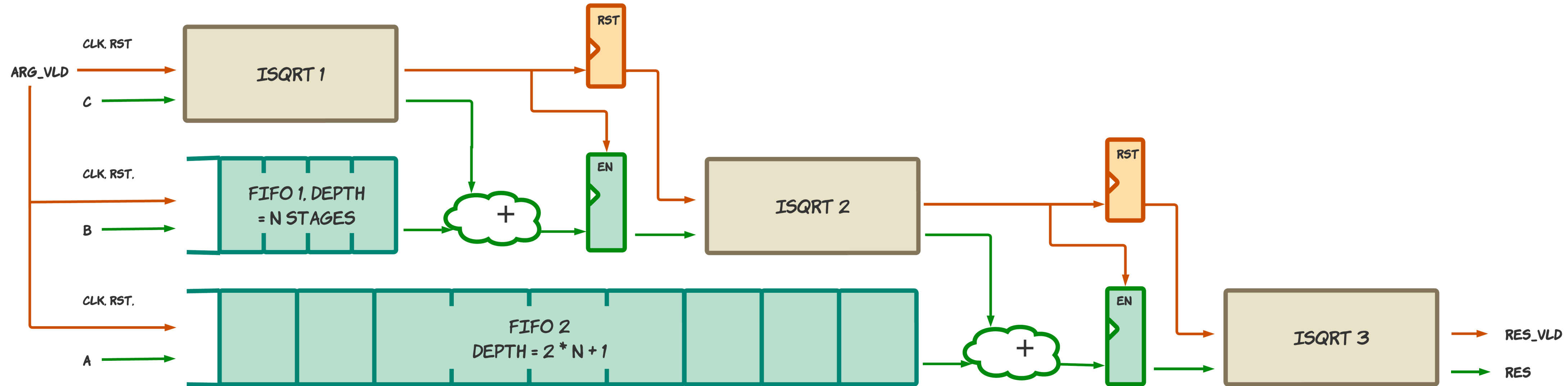
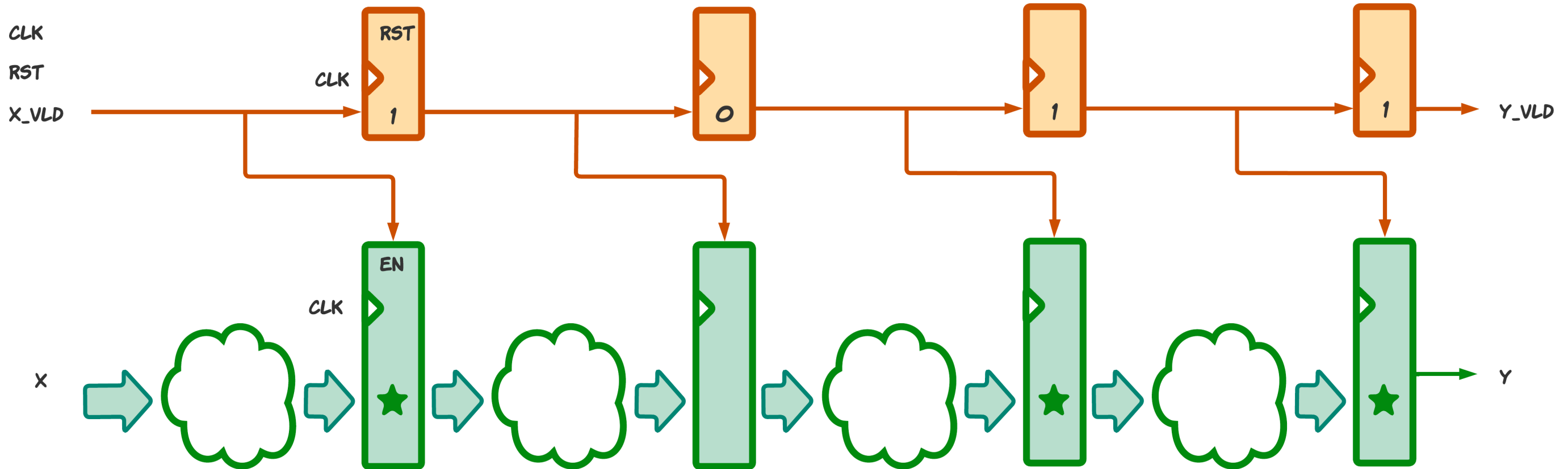


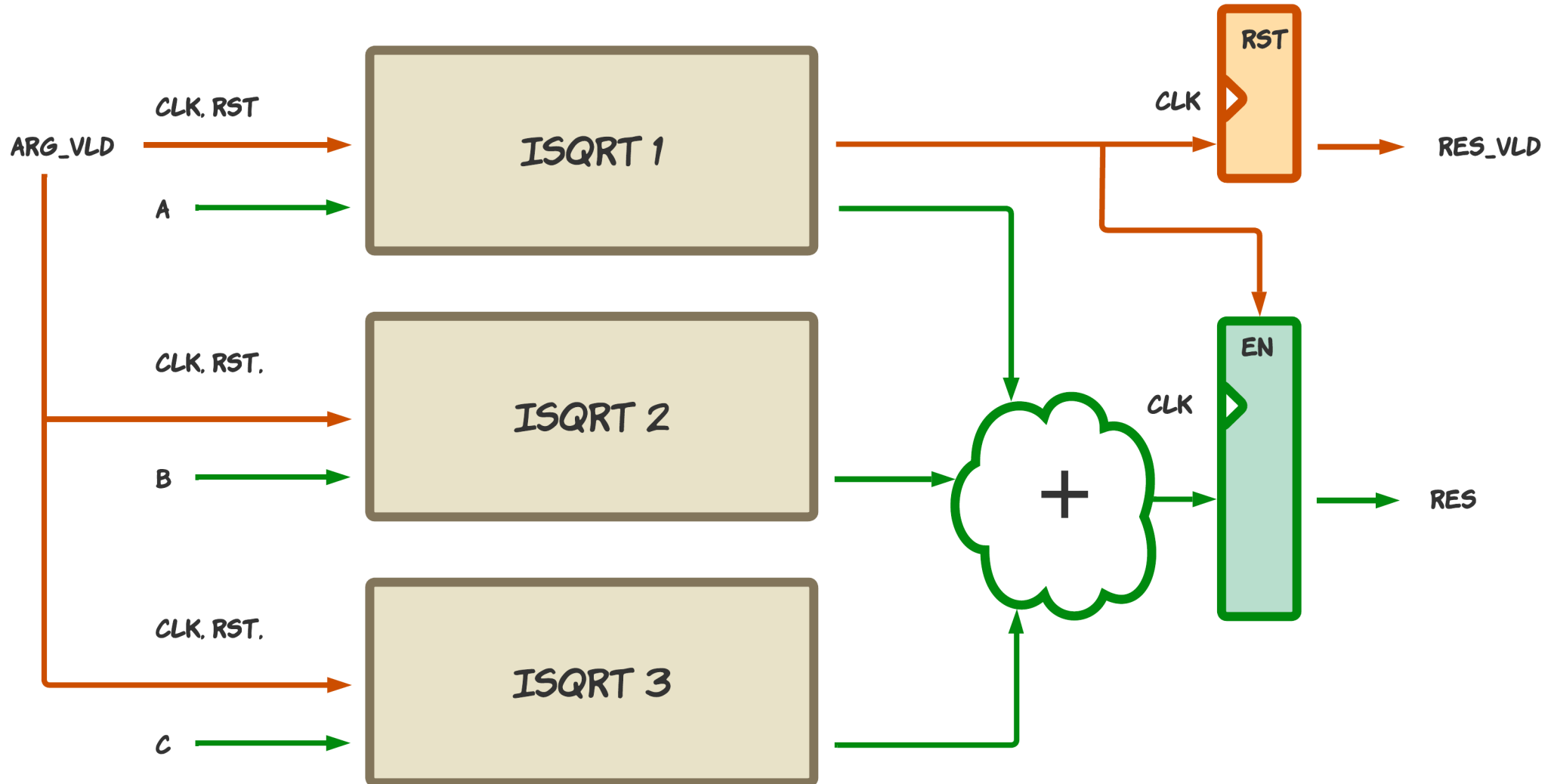
PIPELINED SQRT (A + SQRT (B + SQRT (C)))H
WITH 3 ISQRT PIPELINED MODULES
AND TWO FIFOS



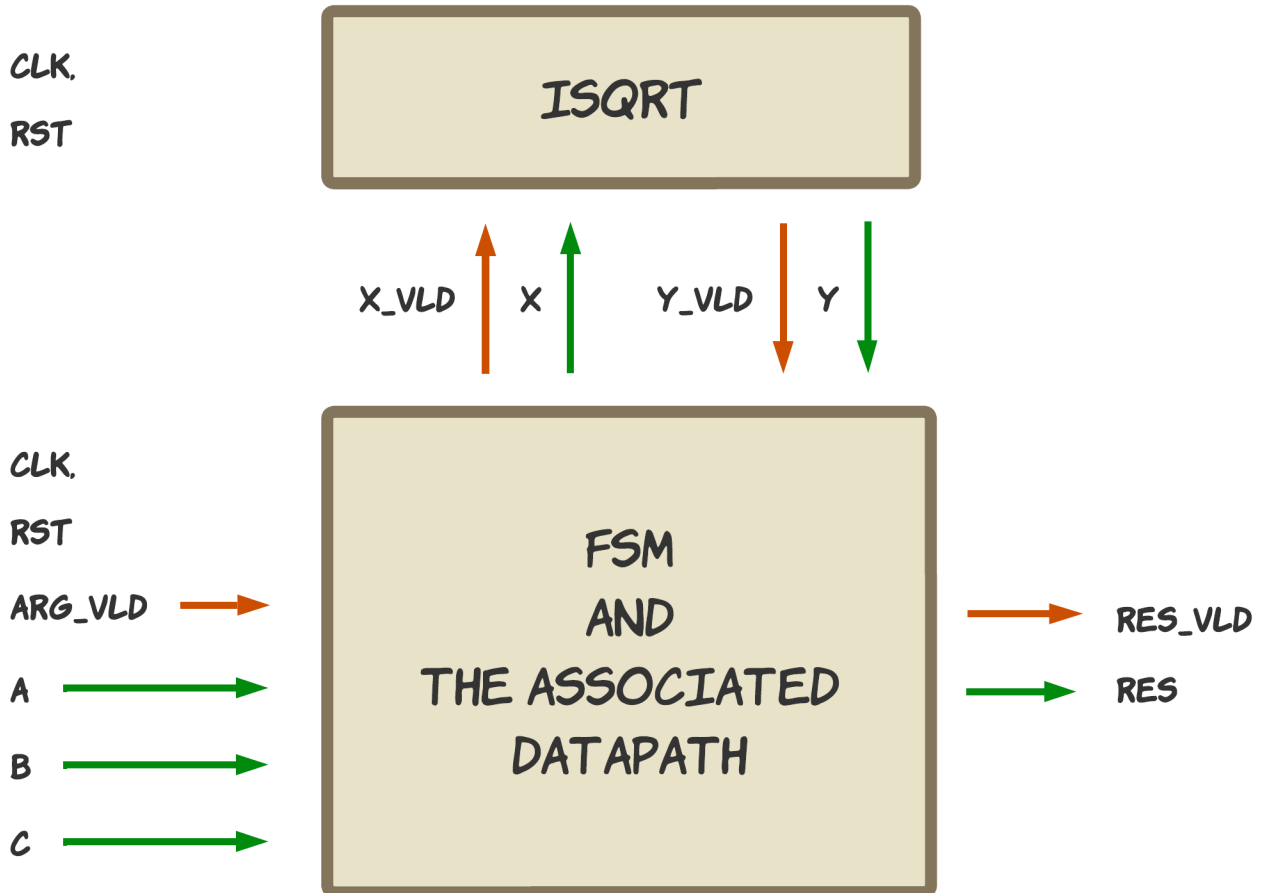
ISQRT PIPELINE



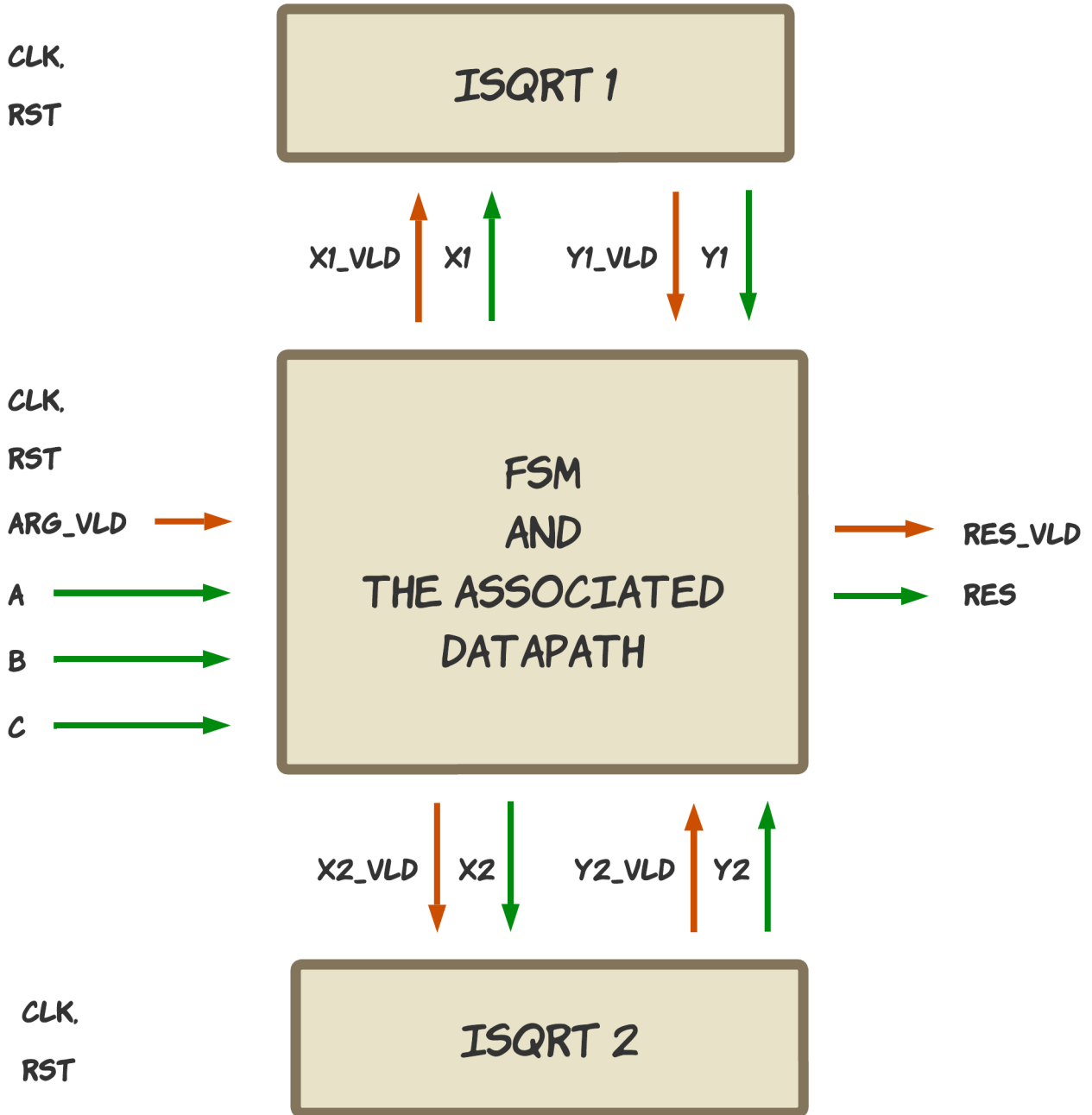
PIPELINED SQRT (A) + SQRT (B) + SQRT (C) WITH 3 ISQRT PIPELINED MODULES



IMPLEMENTING A FORMULA WITH FSM THAT CONTROLS ONE ISQRT MODULE

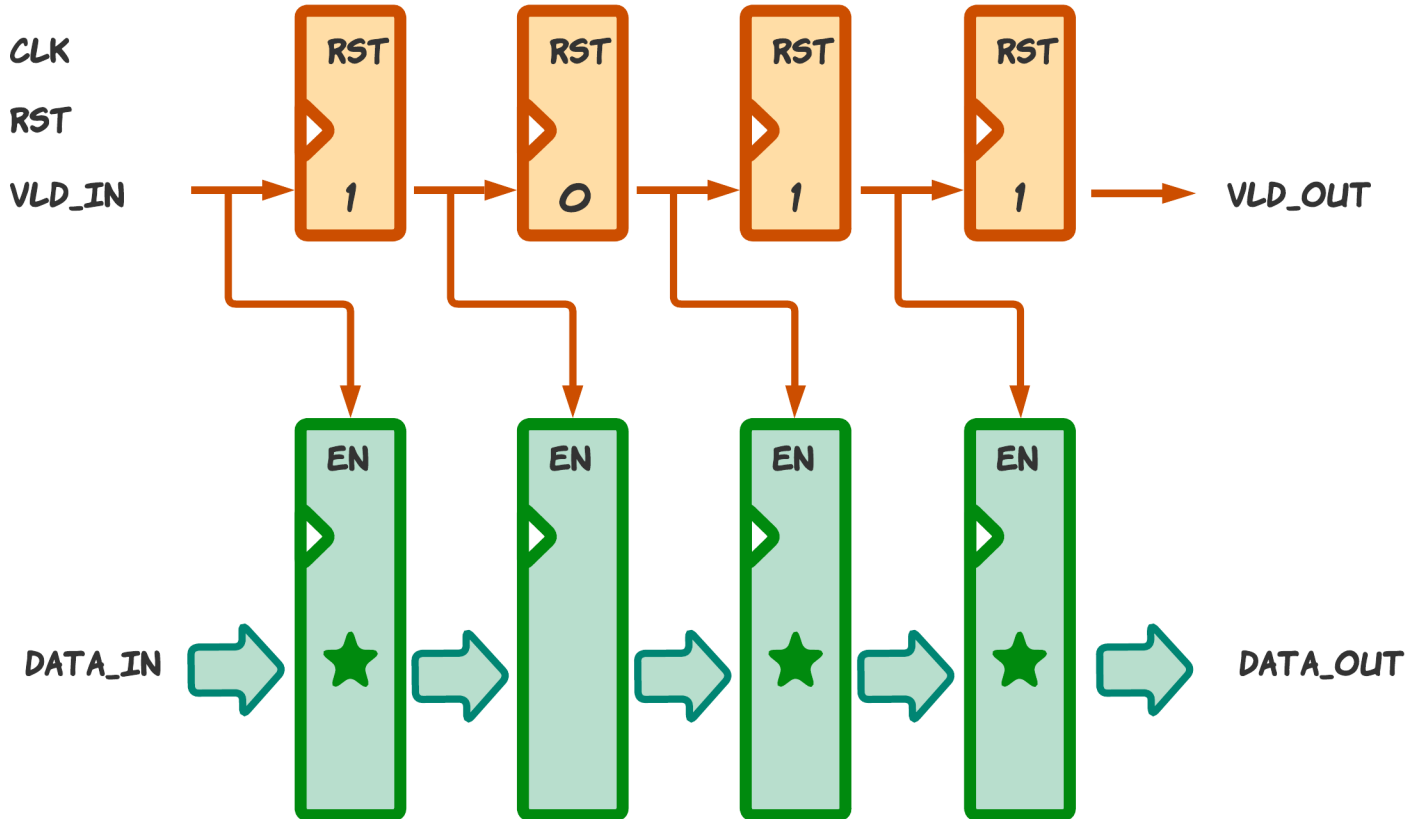


IMPLEMENTING A FORMULA WITH FSM THAT CONTROLS TWO ISQRT MODULES



SHIFT REGISTER

WITH VALID BIT FOR WIDE DATA



PIPELINED SQRT (A + SQRT (B + SQRT (C)))H
WITH 3 ISQRT PIPELINED MODULES
AND SHIFT REGISTERS

