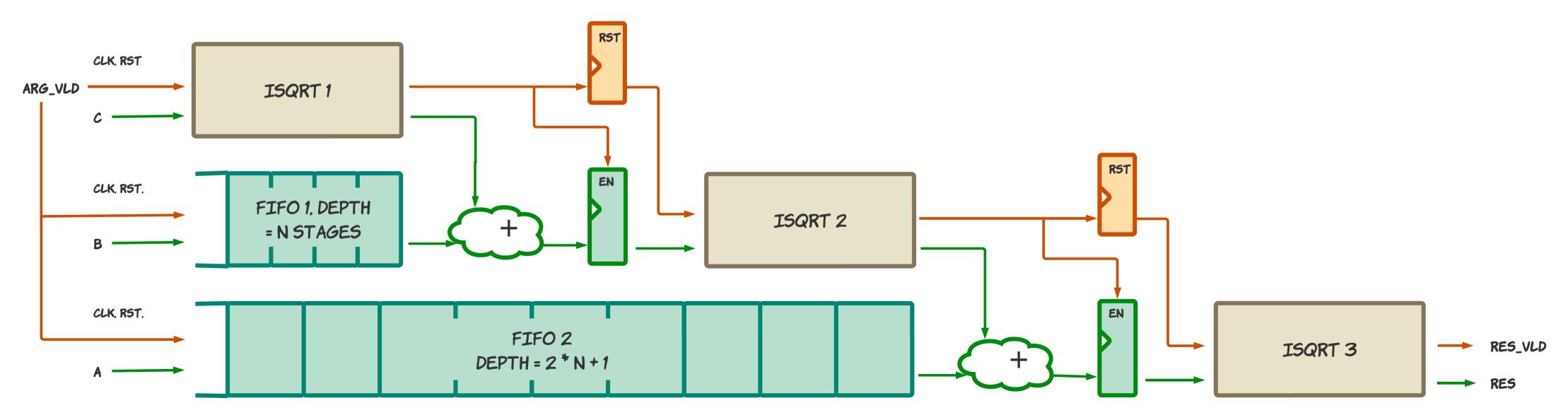
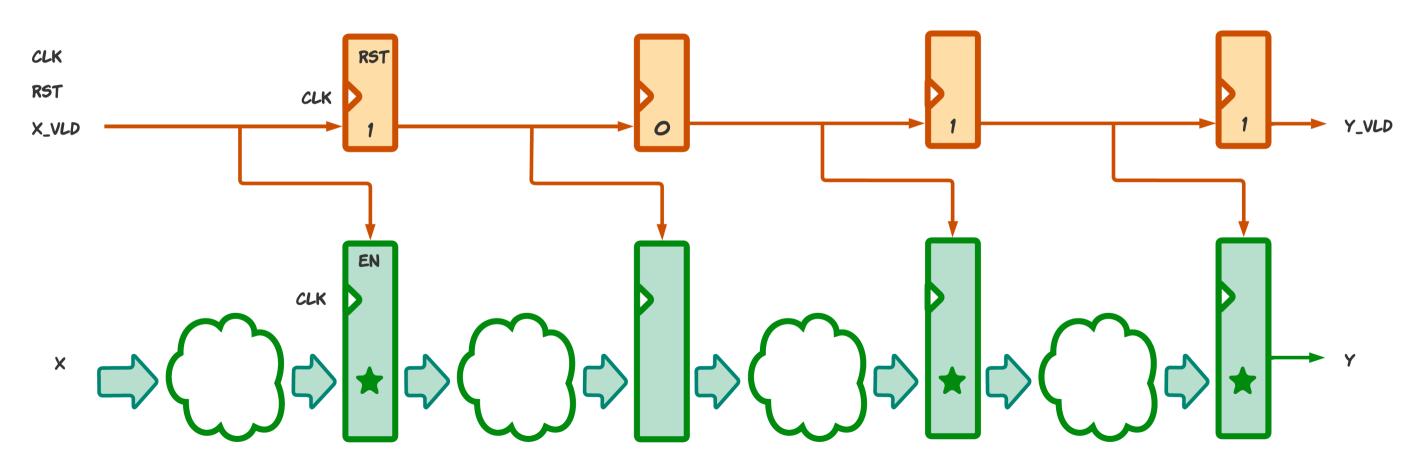
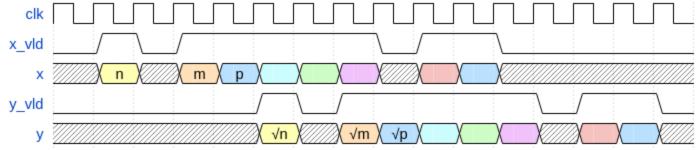
# PIPELINED SQRT (A + SQRT (B + SQRT (C)))H WITH 3 ISQRT PIPELINED MODULES AND TWO FIFOS

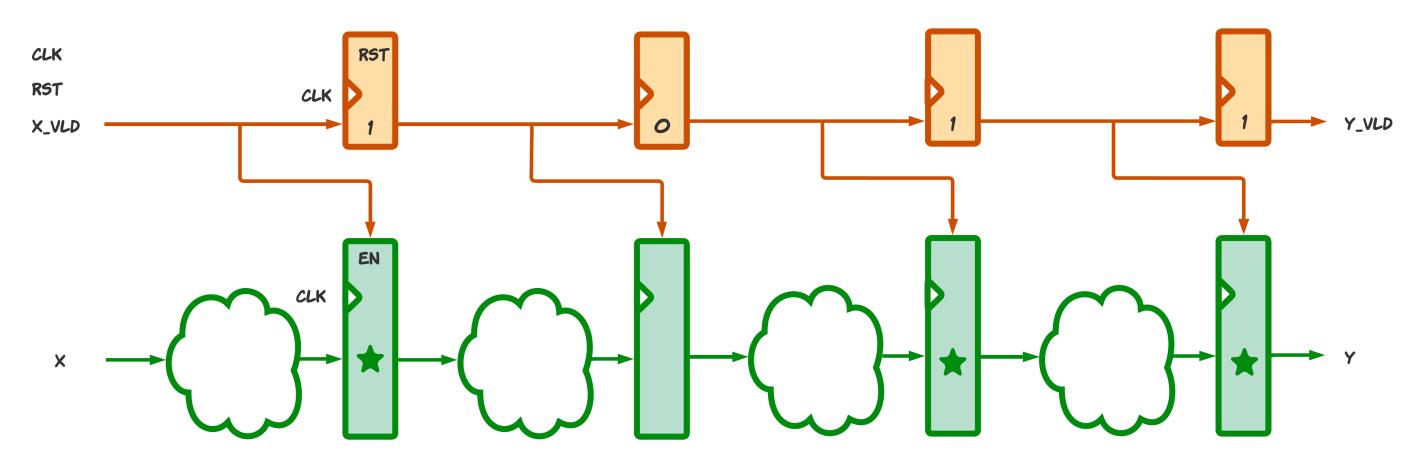


### ISQRT PIPELINE

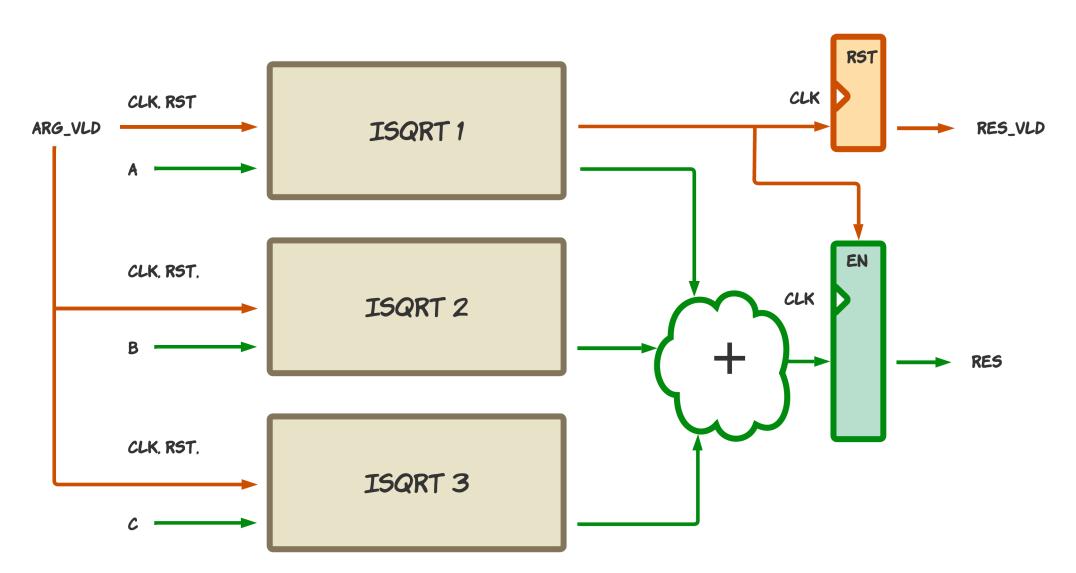


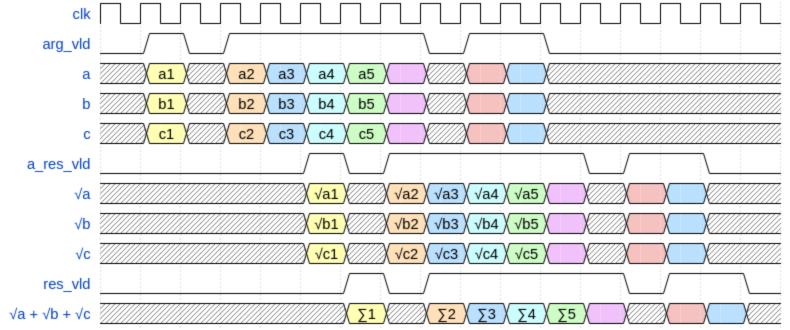


### ISQRT PIPELINE

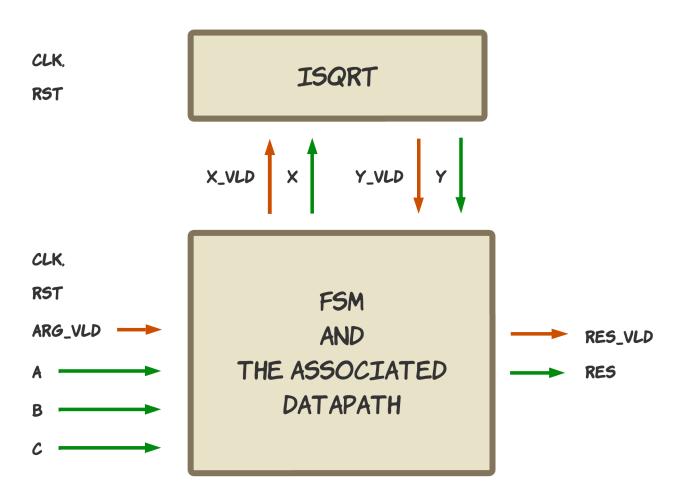


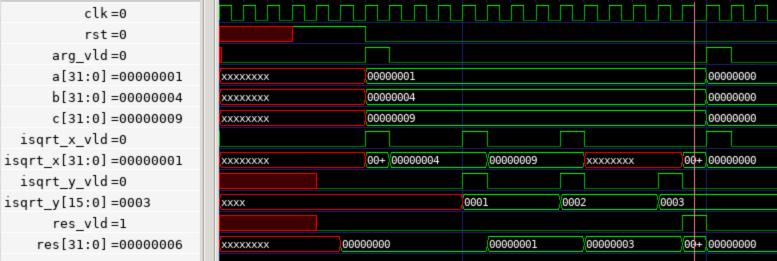
### PIPELINED SQRT (A) + SQRT (B) + SQRT (C) WITH 3 ISQRT PIPELINED MODULES



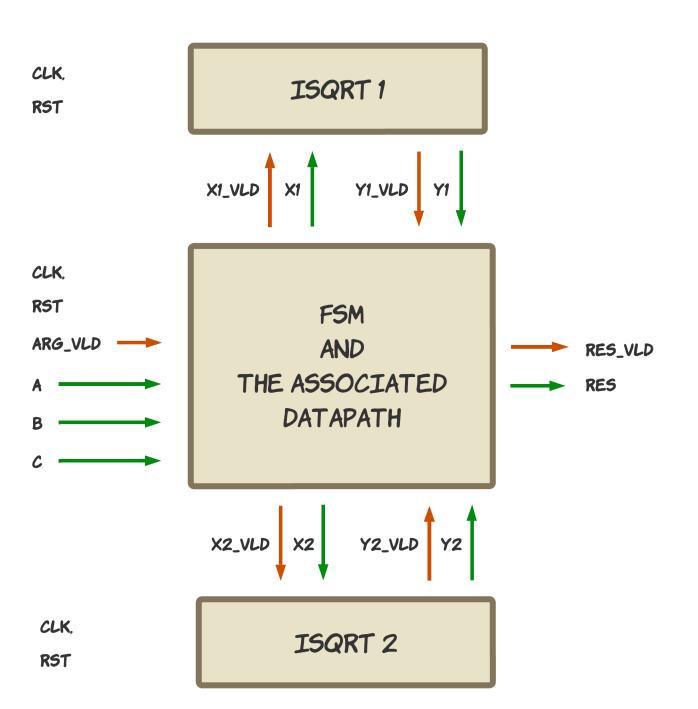


### IMPLEMENTING A FORMULA WITH FSM THAT CONTROLS ONE ISQRT MODULE





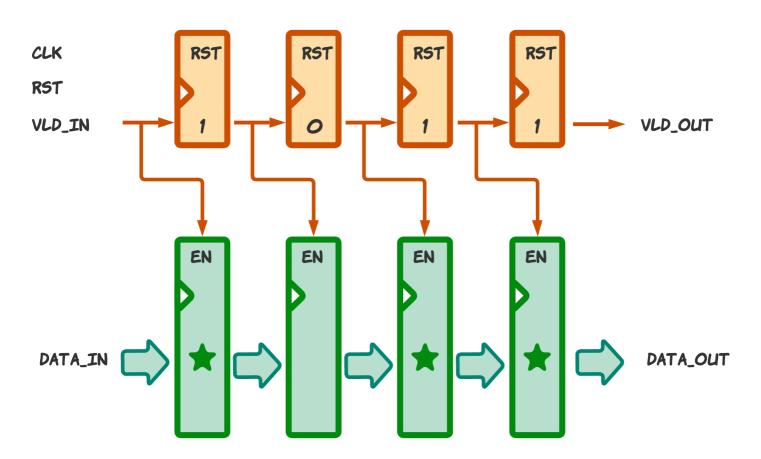
## IMPLEMENTING A FORMULA WITH FSM THAT CONTROLS TWO ISQRT MODULES



### PIPELINED SQRT (A + SQRT (B + SQRT (C)))H WITH 3 ISORT PIPELINED MODULES INCORRECT CLK, RST ISQRT 2 RES\_VLD ISQRT 3 ISQRT 1 ARG\_VLD -RES

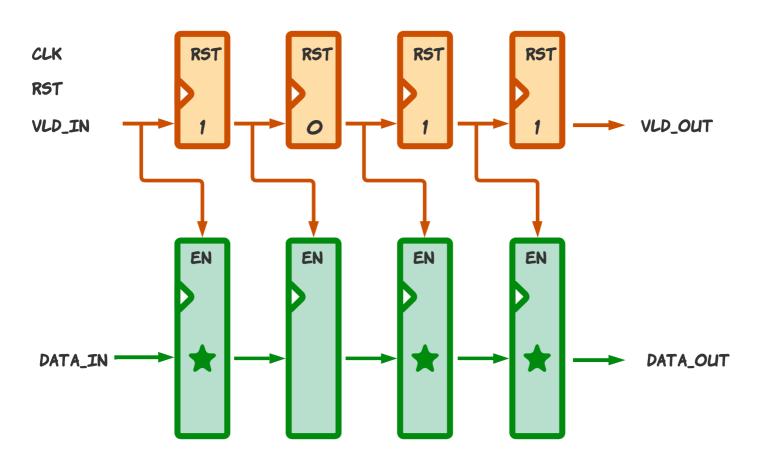
### SHIFT REGISTER

WITH VALID BIT FOR WIDE DATA



### SHIFT REGISTER

WITH VALID BIT FOR WIDE DATA



# PIPELINED SQRT (A + SQRT (B + SQRT (C)))H WITH 3 ISQRT PIPELINED MODULES AND SHIFT REGISTERS

