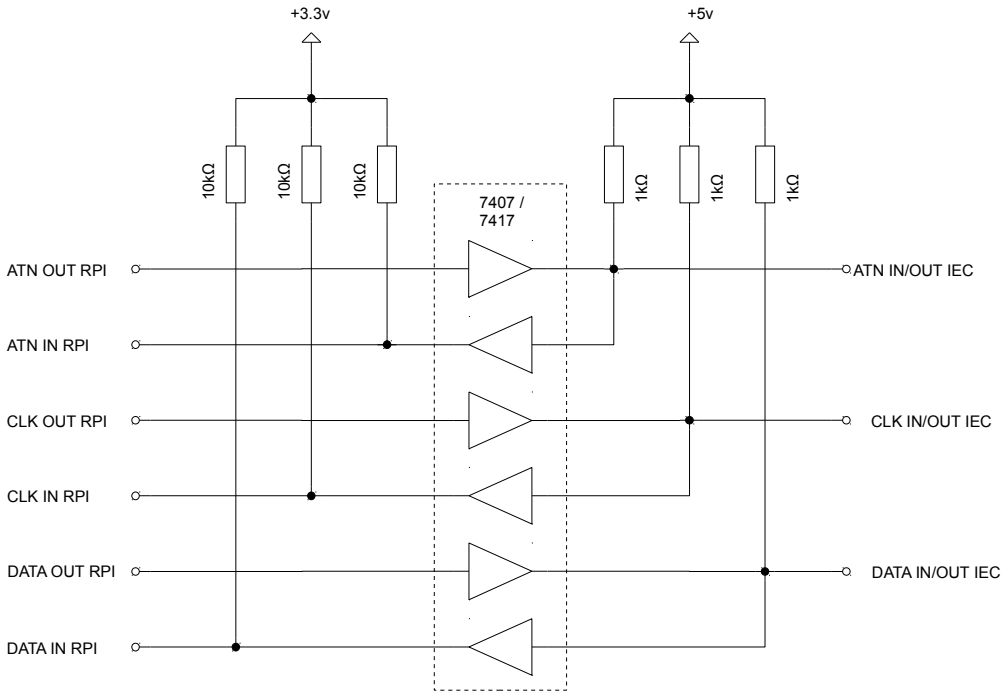
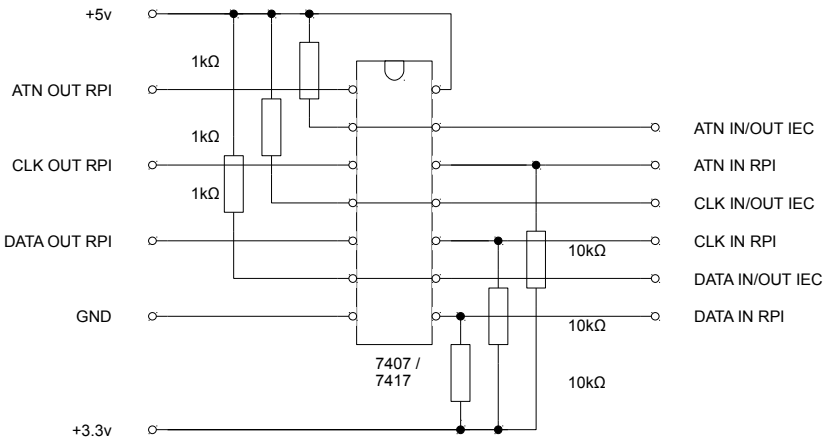
Circuit diagram of the Raspberry Pi GPIO <–> Commodore IEC adapter

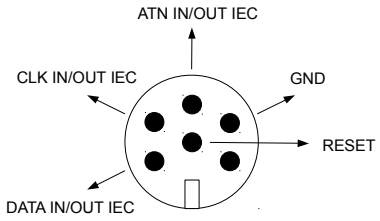


Component layout of the adapter and connector pinouts



RASPBERRY PI P1 HEADER

+3.3v	←	1 ■	□ 2	→	+5v
		3 □	□ 4		
		5 □	□ 6		
DEBUG1 (GPIO 4)	←	7 □	□ 8		
GND	←	9 □	□ 10		
ATN IN RPI (GPIO 17)	←	11 □	□ 12	→	CLK IN RPI (GPIO 18)
DATA IN RPI (GPIO 27)	←	13 □	□ 14		
ATN OUT RPI (GPIO 22)	←	15 □	□ 16	→	CLK OUT RPI (GPIO 23)
		17 □	□ 18	→	DATA OUT RPI (GPIO 24)
		19 □	□ 20		
		21 □	□ 22	→	DEBUG 2 (GPIO 25)
		23 □	□ 24		
		25 □	□ 26		



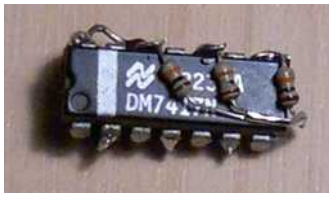
DIN 6 PIN FEMALE
BACKSIDE (SOLDER SIDE)

Assembling the adapter

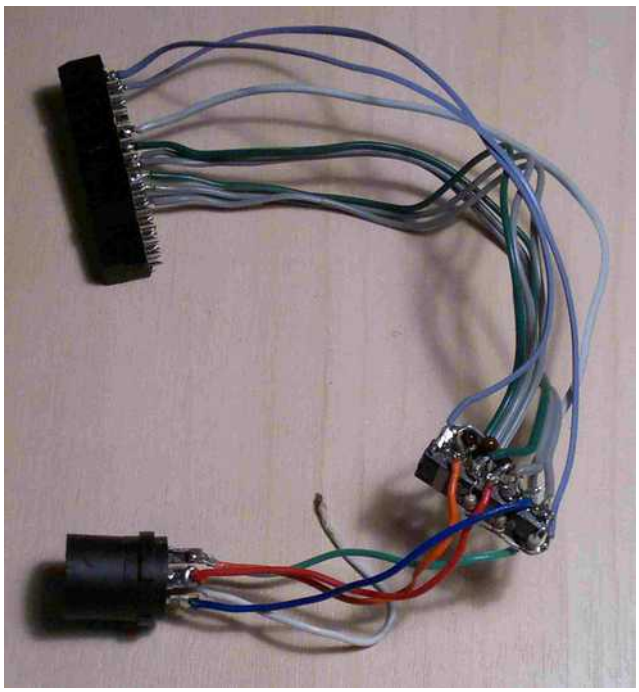
Start with the 7407 or 7417 open collector buffer.

Connect pins 2-13, 4-11 and 6-9 with short jumper wires.

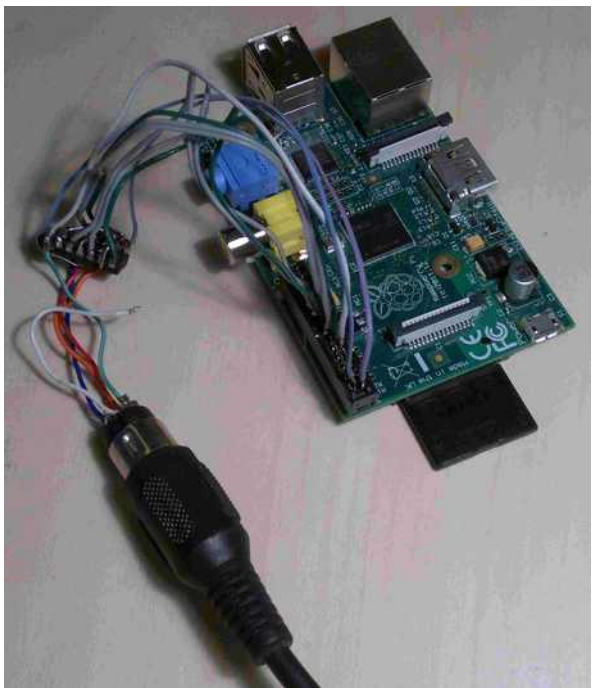
Then add the 1k resistors...



...and the 10k resistors...



...and finally all the wires to the connectors.



Perhaps not pretty but it works.