NAME

devIcv196.c − ADAS ICV196 Device Support for EPICS R3.14.

SYNOPSIS

Variables

int **devIcv196Verbose** = 0

Detailed Description

ICV196 Device Support accepts up to 2 boards in a VME crate, starting from address **0x200000** with an increment of 0x100. Each ICV196 uses two interrupt vectors, starting from vector 0xC4 for the board 0.

It supports the following record types: BI, BO, MBBI, MBBO, MBBIDIRECT, MBBODIRECT, LONGIN, LONGOUT. The device type **DTYP** is **ICV196** for all record types.

Signals 0 to 15 must always be configured as input because they may be programmed to generate interrupts on a zero to one input transition. The interrupt is enabled by giving an event number in the parameter string of the BI record **INP** field (eg: #C0 S1 @event 12). The interrupt service routine posts the event to allow records processing.

Signals 16 to 95 may be configured as input or output by groups of 8 signals. The configuration is automatically done and checked by the record/device init functions.

The **NOBT** of records MBBI, MBBO, MBBIDIRECT and MBBODIRECT is limited to 16 bits by the record support.

Records LONGIN and LONGOUT allow to read or write 32 bit patterns. Three patterns are available through signal number S0 to S2, starting at signals 0, 32 and 64. The first pattern (S0) must always be configured as input.

Variable Documentation

int devIcv196Verbose = 0

This IOC shell variable allows to print debug messages. Valid range is:

- 0 no message is printed

- 1 messages at initialization are printed

- 2 initialization and I/O messages are printed

Author

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