

Figure 1: Presentation layer of subsystem

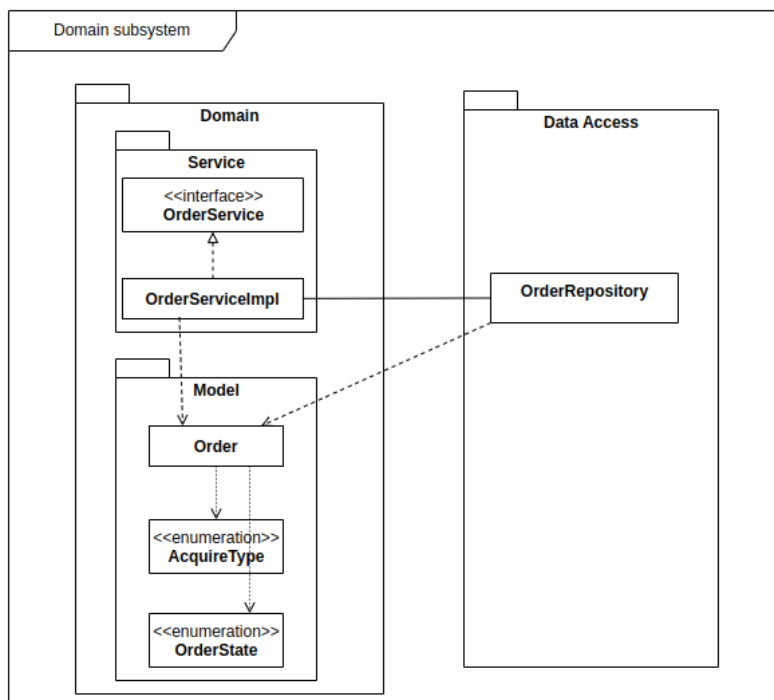


Figure 2: Domain subsystem

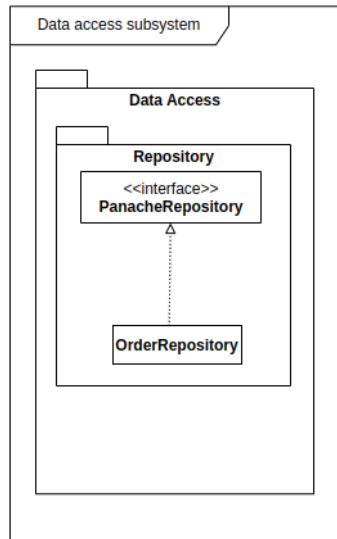


Figure 3: Data access subsystem

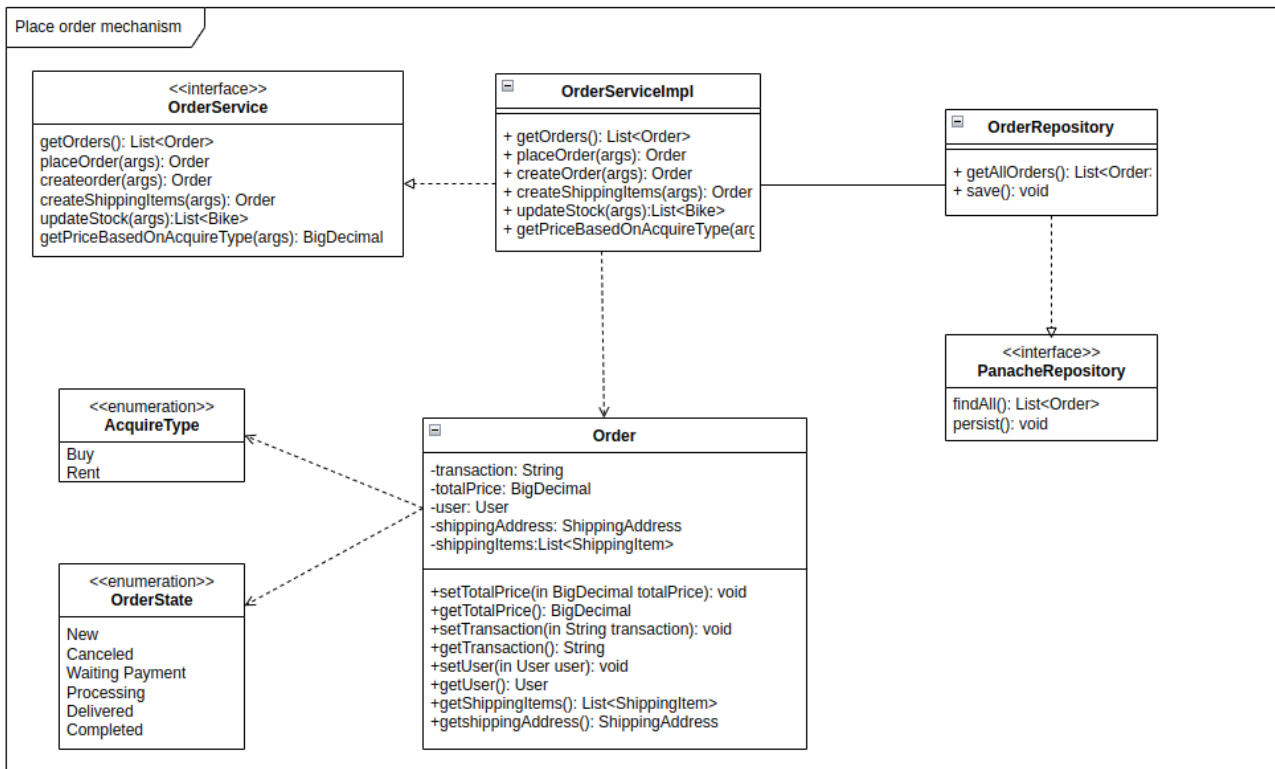


Figure 4: Mechanism Architecture

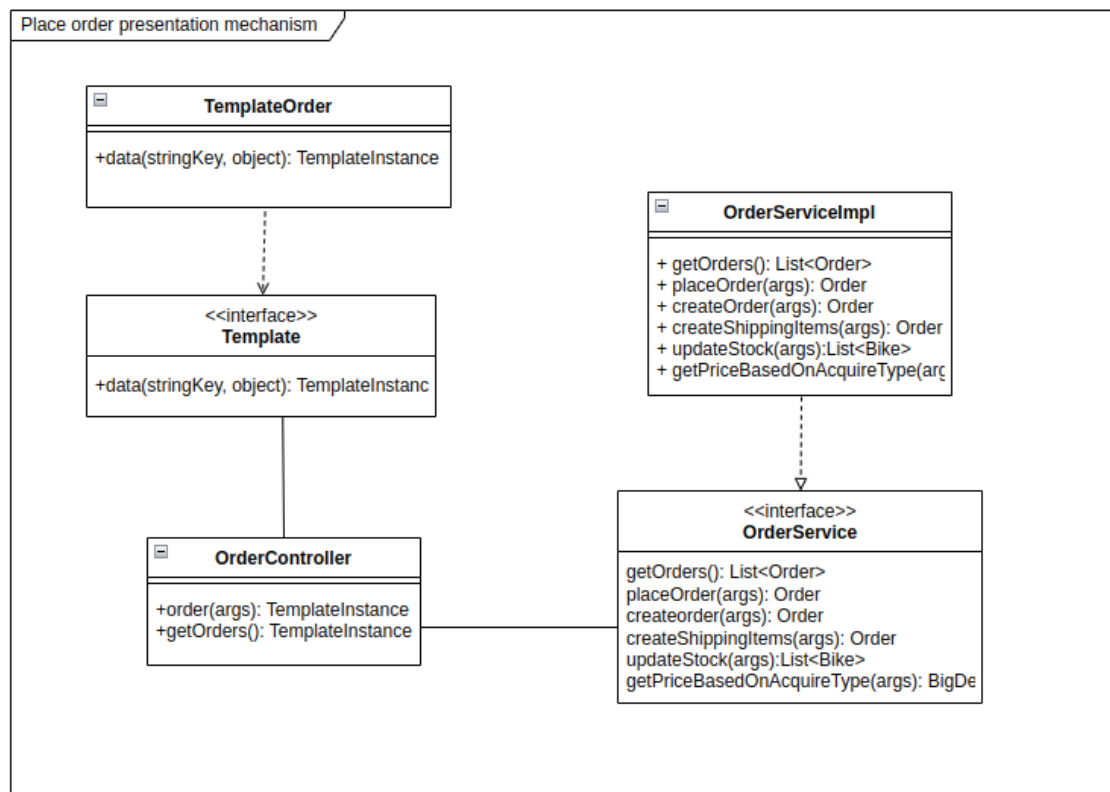


Figure 5: Place Order presentation mechanism

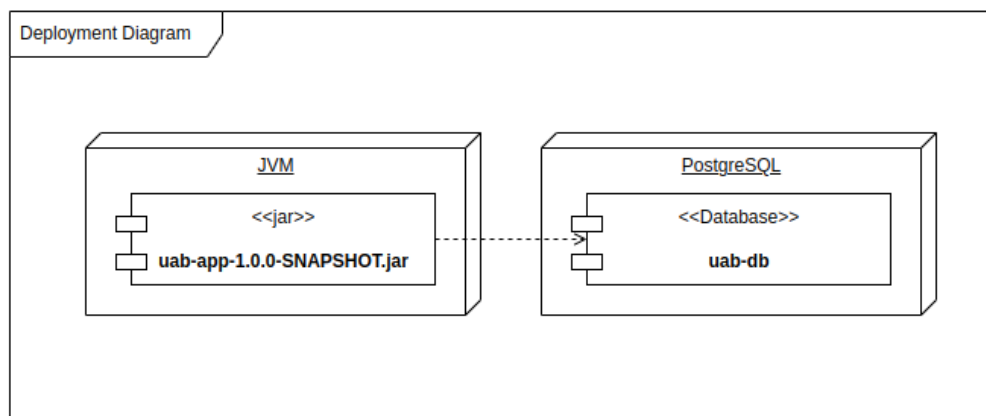


Figure 6: Deployment diagram

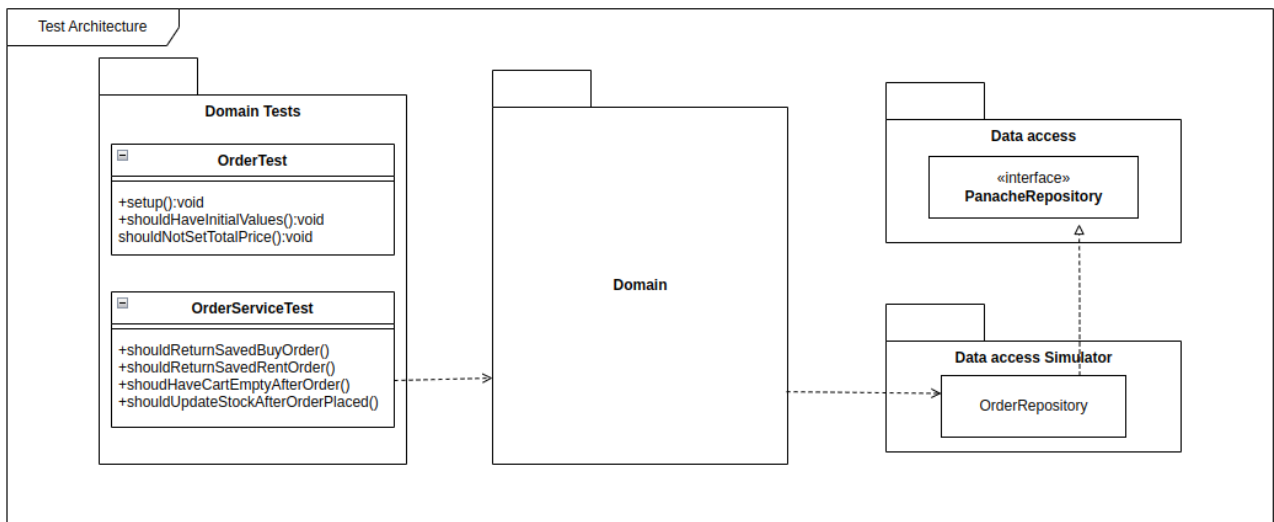


Figure 7: Test architecture diagram

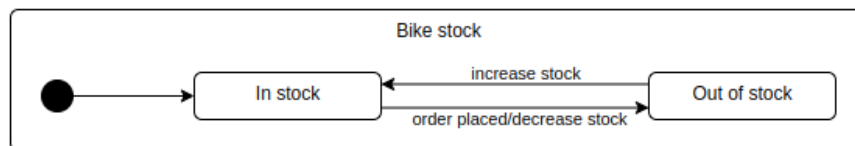


Figure 8: State model for Bike class

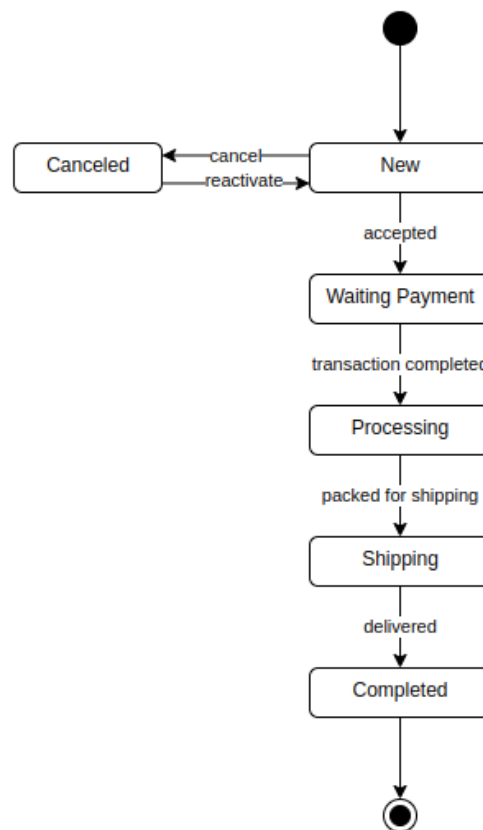


Figure 9: State model for Order Class