433 MHz Impedance Matched Balun + LPF Integrated Front-End SMD Passive Component for SiLabs Si4455, Si4460, Si4461, Si4463, and Si4464 Chipsets

P/N 0433BM41A0019

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General Specifications				
Part Number	0433BM41A0019	A		
Frequency Range(MHz)	424-444			
Balanced Impedance	Si4455, Si4460, Si4461, Si4463, Si4464			
Unbalanced impedance	50Ω (single ended)	Ass. Ass. Com		
Average Insertion Loss when	0.0dB T. m @ 25C			
connected to Si44XX chipset	0.9dB Typ@25C 1.5dB max. (-45 to +85C)	Phase Diff. (deg.)	180° ± 10	
(Active OP Tx/Rx)	1.5dB max. (-45 to 4656)	VSWR @ BW	2.0 max.	
Insertion Loss when	1.6dB Typ in Rx, 1.4dB Typ in Tx	Amplitude Difference (dB)	2.0 max.	
component measured by	@25C 2.2dB max. in Rx, 1.9dB	Reel Quanity	4,000	
itself(passive insertion loss)	passive insertion loss) max. in Tx (-45 to +85C)		-40 to +85°C	
	35 min. @ 2xfo MHz	Recommended Storage	+5 ~ +35 °C, Humidity 45~75%RH, 18 months.	
Attenuation	35 min. @ 3xfo MHz	Conditions for Unused		
dB (min.)	35 min. @ 4xfo MHz	product on T&R		
	35 min. @ 5xfo MHz		500mW max. (CW)	

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Part Number Explanation					
	Packing Style	Bulk	Suffix = S	eg. 0433BM41A0019S	
P/N Suffix		T&R	Suffix = E	eg. 0433BM41A0019E	
	Termination style	Ni/Sn	Suffix = None	eg. 0433BM41A0019(E or S)	

Mechanical Dimensions					
	In			mm	
L	0.126	±	0.008	3.20 ± 0.20	
W	0.098	±	0.008	2.50 ± 0.20	
Т	0.059	±	0.006	1.50 ± 0.15	
a1	0.022	±	0.006	0.55 ± 0.15	
a2	0.028	±	0.008	0.70 ± 0.20	
b	0.004 min.		min.	0.1 min.	
С	0.012	+	0.008	0.30 + 0.20	
g	0.018	+	0.006	0.45 + 0.15	
р	0.039	+	0.008	1.00 + 0.20	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$					

Terminal Configuration							
No.	Function	No.	Function				
1	GND	5	GND				
2	Ant	6	TX				
3	GND	7	RXN				
4	GND	8	RXP				
4 3 25 16 7 8							



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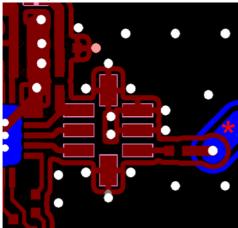
P/N 0433BM41A0019

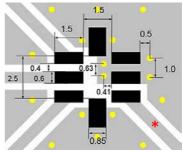
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Mounting Considerations

Line/Via width should be designed to match 500hm characteristic impedance, depending on PCB material and thickness.

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Solder Resist

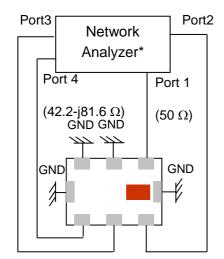
Land

 \bigcirc Through-hole (ϕ 0.3)

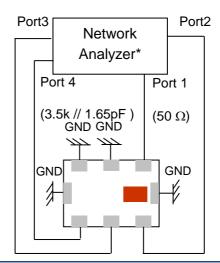
Do you need the layout/gerber files of the above? Go to: https://www.johansontechnology.com/silabs or send us a message at: https://www.johansontechnology.com/ask-a-question

Measuring Diagram

Tx Mode



Rx Mode



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Measuring Diagram (cont.)

Tx Mode:

Port1: Antenna Port

Port1 Terminate impedance: 50ohm Ports 2 and 3: Rx Balanced Port Port 2 and 3 Terminate impedance: 1/2 x (the loading impedance ZIC,RX

off of Si4455) Port4: Tx Port

Port4 Terminate impedance: Complex conjugate to ZIC,TX on of

Si4455

IL = S41 RL = S11 / S44

RXMode:

Port 1: Antenna Port

Port1 Terminate impedance: 50ohm Ports 2 and 3: Rx Balanced Port Port 2 and 3 Terminate impedance: Complex conjugate to 1/2 x (Balance impedance of ZIC,RX on of Si4455)

Port4: Tx Port

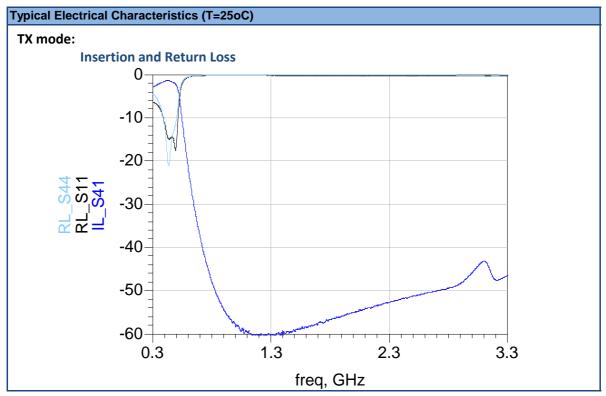
Port4 Terminate impedance: The loading

impedance ZIC,TX off of Si4455

IL=Sds21

RL=Sss11 / Sdd22

 $Amp_balance = dB(S(3,1)/S(2,1))$



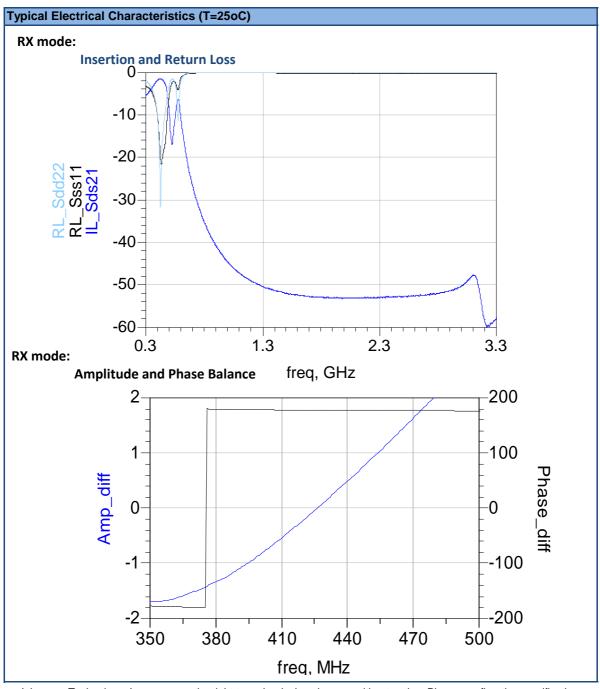
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Application Notes, Layout Files, and more

www.johansontechnology.com/silabs

RoHS Compliance

www.johansontechnology.com/technical-notes/rohs-compliance.html

Soldering Information

www.johansontechnology.com/ipcsoldering-profile

Antenna layout and tuning techniques

www.johansontechnology.com/tuning

Antenna layout review, tuning, and characterization services

www.johansontechnology.com/ipcantennaservices

MSL Info

www.johansontechnology.com/technical-notes/msl-rating

Recommneded Storage Condition and Max Shelf Life

www.johansontechnology.com/recommended-storage-conditions

Packaging information

www.johansontechnology.com/tape-reel-packaging

