



Cycle Initial en Technologies de l'Information de Saint-Étienne

Second Order Passive Filters

Lucas Lescure - Charlie Durand



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1. Abstract

The goal of this report is to introduce ourselves to second order passive filters in an effort to understand and manipulate the effects of damping on these kinds of filters. We will also be seeing first order active filters and understand what makes them different from other first order filters previously studied.

3 2. 2nd Order Passive Filters

2. 2^{nd} Order Passive Filters

2.1. Study of low pass filter

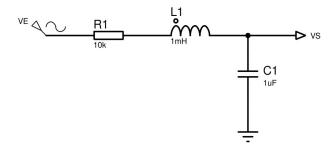


Figure 2.1. Low pass RLC-C filter

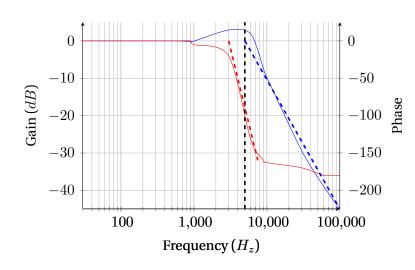
Using the formulas found in the preparation of the experiment we calculate the critical damping resistance: $R_C = 2 \left(\frac{L}{C}\right)^{1/2}$ such as $R_C = 632\Omega$

However since we aren't experimenting in ideal conditions we must account for the resistance of the coil (22Ω) and the signal generator (50Ω). As such the resistance needed to assure an overall resistance of 630Ω is 570Ω . We obtain this value using two resistors in series, one being 100Ω and the other 470Ω .

Using the same procedure we calculate the characteristic frequency of the circuit: $f_0 = \frac{1}{2\pi\sqrt{LC}}$ such as $f_0 = 5.0\,kH_z$

2.1.a) Study in sinusoidal regime

We measure, on the first hand, the following values where $R = 100\Omega$:



Using the formulas from the preparation we easily find the damping coefficient: $m=\frac{1}{2}R\left(\frac{C}{L}\right)^{1/2}$ such as m=0.16

On top of this we can clearly observe the cut-off frequency around 5 kH_z with a slope of $-40\ dB/dec$.

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On a second hand, we measure the following values where $R=R_C$:

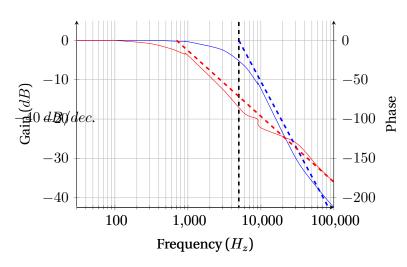


Figure 2.2. Bode Diagram for $R = R_C$

Using the formulas from the preparation we easily find the damping coefficient: $m=\frac{1}{2}R_C\left(\frac{C}{L}\right)^{1/2}$ such as m=0.9

On top of this we can clearly observe the cutoff frequency around 5 kH_z with a slope of

On the last hand, we measure the following values where $R=10~k\Omega$:

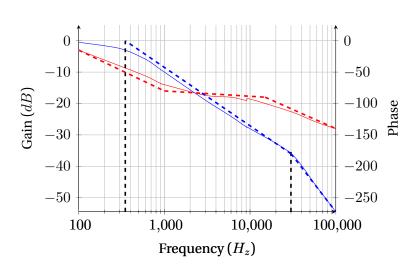


Figure 2.3. Bode Diagram for $R=10~k\Omega$

Using the formulas from the preparation we easily find the damping coefficient: $m=\frac{1}{2}R\left(\frac{C}{L}\right)^{1/2}$ such as m=15.8

This time we notice two distinct cut-off frequencies, the first being at around $350\,H_z$ and the second at $30\,kH_z$.

The slope between both cut-off frequencies is about $-20\,dB/dec$ and $-40\,dB/dec$ after the second cut-off frequency.

We can therefore conclude that by modifying the damping coefficient, we can alternate between a underdamped, critically damped, or overdamped signal response. This is done by tweaking values of the resistor.

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2.1.b) Study in impulsive regime

We'll now proceed to study the same circuit in an impulsive regime, to do so we'll send a square signal $2\,V_{pp}$ with frequency $100H_z$ such as to neglect the negative gain which will allow us to view the response signal without having to worry about any voltage loss.

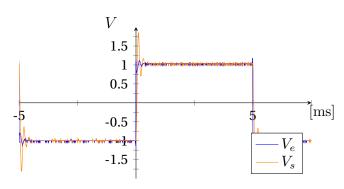


Figure 2.4. Chronograms of V_s and V_e

Using the oscilloscope we measure the rise time to be at $41.8~\mu s$.

On top of this we can clearly see that the signal response oscillates heavily upon a radical change of voltage before setting back down to its respective value.

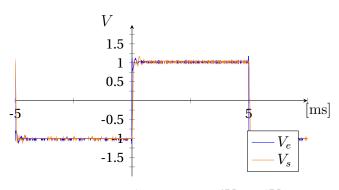


Figure 2.5. Chronograms of V_s and V_e

Using the oscilloscope we measure the rise time to be at $106.5 \ \mu s$.

This time no oscillation is seen and the signal response follows quickly the input signal

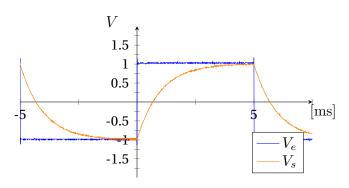


Figure 2.6. Chronograms of V_s and V_e

Using the oscilloscope we measure the rise time to be at $2.04 \ ms$.

No oscillation is seen, however the time needed for the response to follow the input signal is drastically slowed, resulting in a response that can barely catch up to its respective value.

2. 2nd Order Passive Filters

2.2. Study of high pass filter

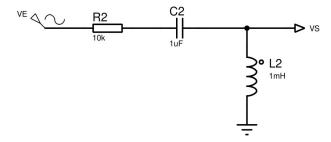


Figure 2.7. High pass RLC-L filter

2.2.a) Study in sinusoidal regime

We measure the following values where $R = 100\Omega$:

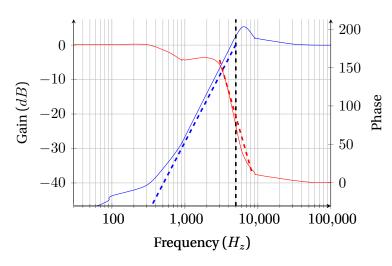


Figure 2.8. Bode Diagram for $R=100\Omega$

Using the formulas from the preparation we easily find the damping coefficient: $m=\frac{1}{2}R\left(\frac{C}{L}\right)^{1/2}$ such as m=0.16

Compared to the other Bode diagrams from the loss pass filter we still have the cut-off frequency around $5\ kh_z$ with a slope of $40\ dB/dec$.

We can also notice that the phase diagram has been offset by 180 degrees, and that due to the nature of the high pass frequency filter the gain curve has been mirrored.

 $3. 1^{st}$ Order Active Filters

3. 1^{st} Order Active Filters

3.1. Study of high pass filter

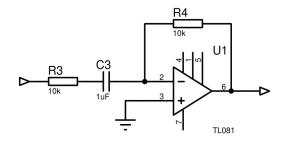
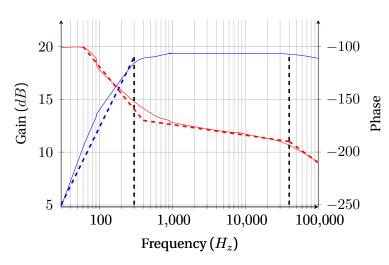


Figure 3.1. High pass operational amplifier filter

3.1.a) Study in sinusoidal regime

Our measurements will be done with a $15\,V$ entry signal such as to accurately trace the following Bode diagrams:



We can see the an expected cut-off circuit at around $300\ H_z$, however we can also notice that the gain and phase start dropping at higher frequencies.

This is due to the limitation of our operational amplifier, which, at high frequencies, can no longer output the intended output voltage in time.

As a result of this our filter is only applicable over a limited bandwidth.

Figure 3.2. Bode Diagram

3.1.b) Study in impulsive regime

We'll now proceed to study the same circuit in an impulsive regime, to do so we'll send a square signal $2 V_{pp}$ with frequency $100 H_z$ such as to make sure that we have a clear and amplified signal response.

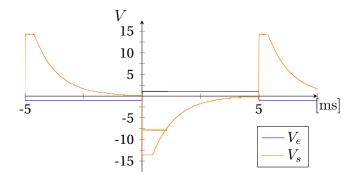


Figure 3.3. Chronograms of V_s and V_e

3. 1st Order Active Filters

3.2. Study of low pass filter(mistakes were made)

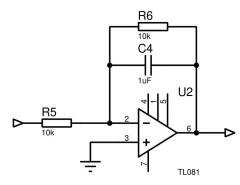
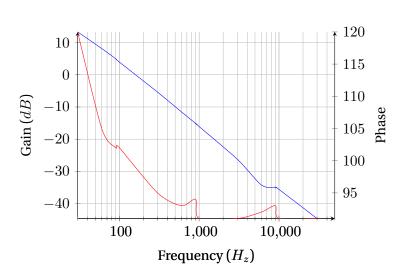


Figure 3.4. Low pass operational amplifier filter

3.2.a) Study in sinusoidal regime

Our measurements will be done with a $15\ V$ entry signal such as to accurately trace the following Bode diagrams:



Due to the curves of the phase and gain we can quickly determine that the intended circuit was improperly cabled and is responsible for the absurdity of the graph.

Figure 3.5. Bode Diagram

3.2.b) Study in impulsive regime

Due to the same cabling mistake the chronograms make no sense whatsoever

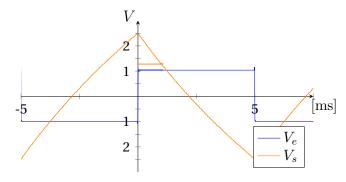


Figure 3.6. Chronograms of V_s and V_e