

Write data in SRAM

Memories are extensively discussed in the course computer systems. In order to write successfully to the SRAM memory is an VHDL example with constraints file is given. (Also the generated sof file is included).

The behavior of this design is:

At address i the number $2 \times i$ is stored ($i = 0 \dots 4$).

De details of the SRAM protocol are in the document: [samsung_sram.pdf](#)

(blackboard: course materials → practicum → practicum/lab sessions: SRAM (final assignment))