How to read both SRAMs on the livedesign board (ALTERA- Cyclone)

This directory contains the VHDL source file, constraint file and the generated SOF file to read both SRAMs on the board.

You can also use the SOF file to check if YOUR design stores the data in the correct address.

After programming the Altera board with this design you can read the SRAM memory addresses 0 until 255.

The procedure (after you programmed the board with the *read_both_srams.sof*) is:

1. Reset the design by pressing the button TEST/RESET
2. The left two 7-segment displays show the address (in HEX) the other four 7-segment displays show the content of that address (in HEX; 16 bits) of SRAM0. If you press button SW4 the content of SRAM1 is shown.
3. If you press button SW6 then you go the next address (modulo 256).
4. If you press button SW5 then you go to the previous address