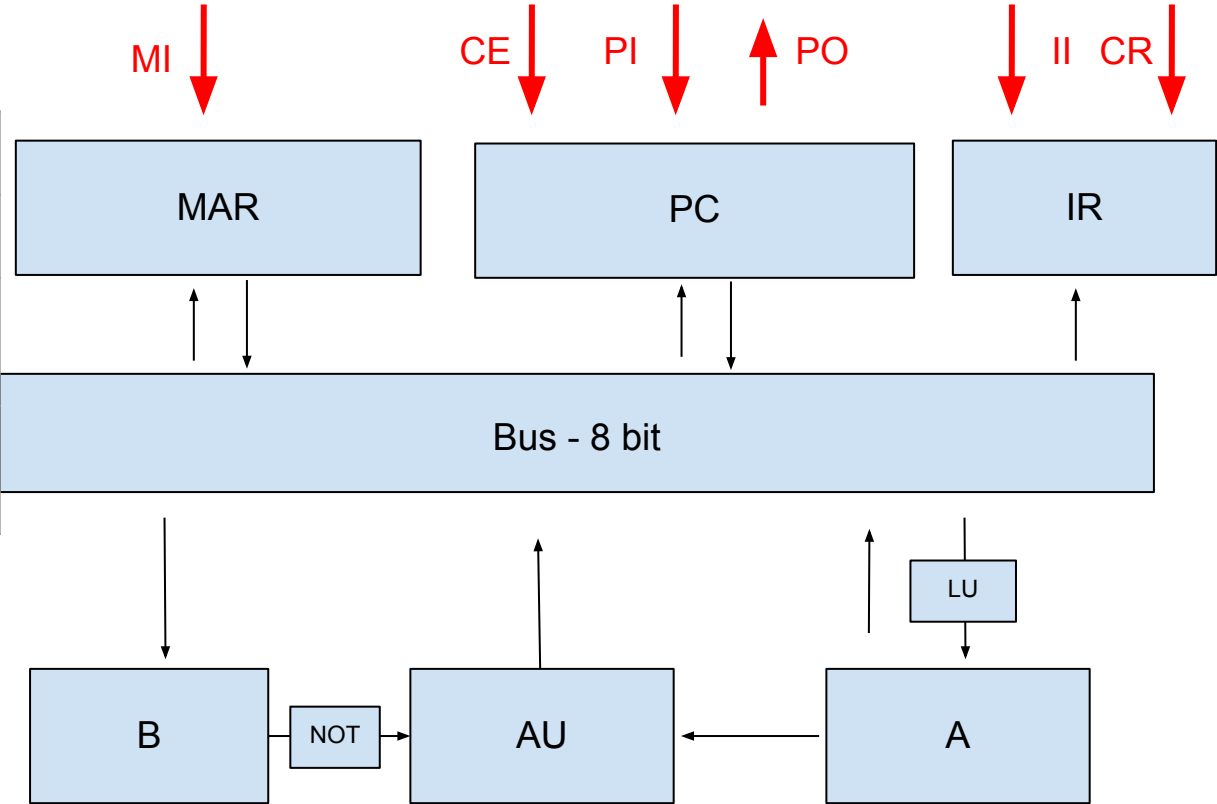


0 II	0 BO
1 MIH	1 EOS
2 MIL	2 POL
3 PIL	3 POH
4 PIH	4 AO
5 AI	5 EO
6 BI	6 RD
7 WR	7 CR

0 ADD
1 CE
2 ADD + INC
3 Reset A



CTRL Byte: XXX XXX XX
DST SRC ALU
Instruction: X X X XXXXXX
ld& ld| ld! Opcode

Logic chips are tied high by default. Can be used to generate 0xFF. Would 0x00 be better? Might be able to use ALU to manipulate.

Logic ops: LDA I SUB A is INC if ! not set
Orange needs !
LDB M(A) where MIH: I MIL: A?

Bits	0 _{HI}	1	2	3
0 _{LO}	NOP	LDA I	JP	ADD A
1	CALL I	LDA B	JPZ	ADDC A
2	RET	LDB I	JNZ	INC B
3	PSH B	LDB A	JPC	ZRO A
4	POP A	LDA M	JNC	SUB A
5	LDZ MA B	LDM A	JPN	CMP
6	LDZ B MA	LDB M	JNN	
7	LDA M+A	LDM B	JPAB	