

1 2 3 4 5 6 7 8

Jetson Orin Baseboard

github.com/antmicro/jetson-orin-baseboard

A

A

B

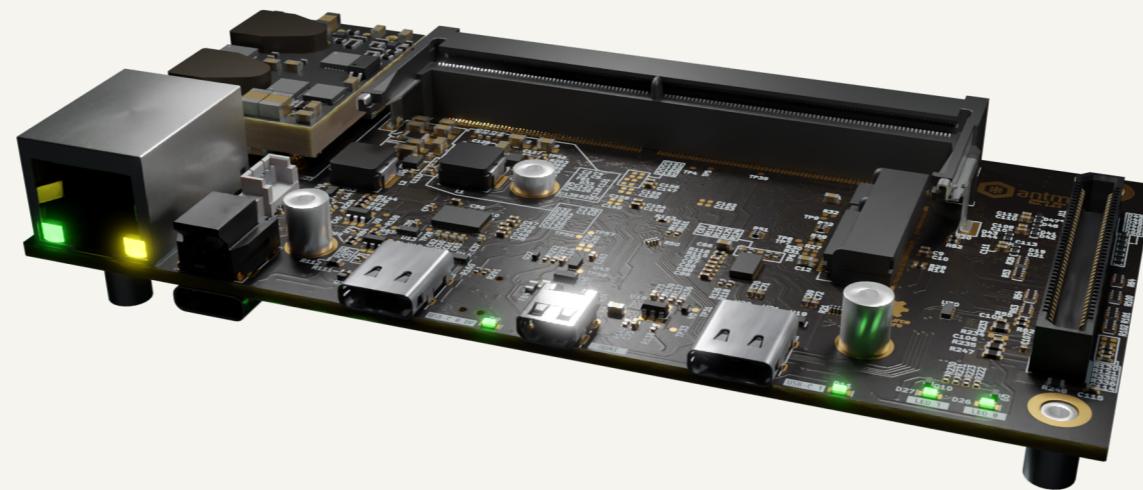
B

C

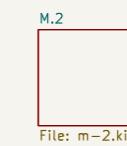
C

D

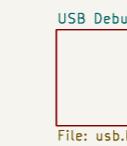
D



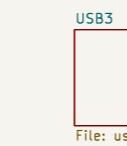
File: som.kicad_sch



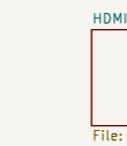
File: m-2.kicad_sch



File: usb.kicad_sch



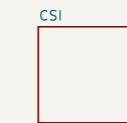
File: usb3.kicad_sch



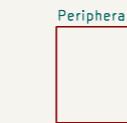
File: hdmi.kicad_sch



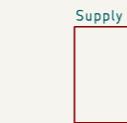
File: ethernet.kicad_sch



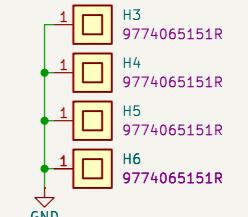
File: csi.kicad_sch



File: peripherals.kicad_sch



File: supply.kicad_sch



www.antmicro.com
Antmicro Ltd

Sheet: / File: jetson-orin-baseboard.kicad_sch

Title: Jetson Orin Baseboard

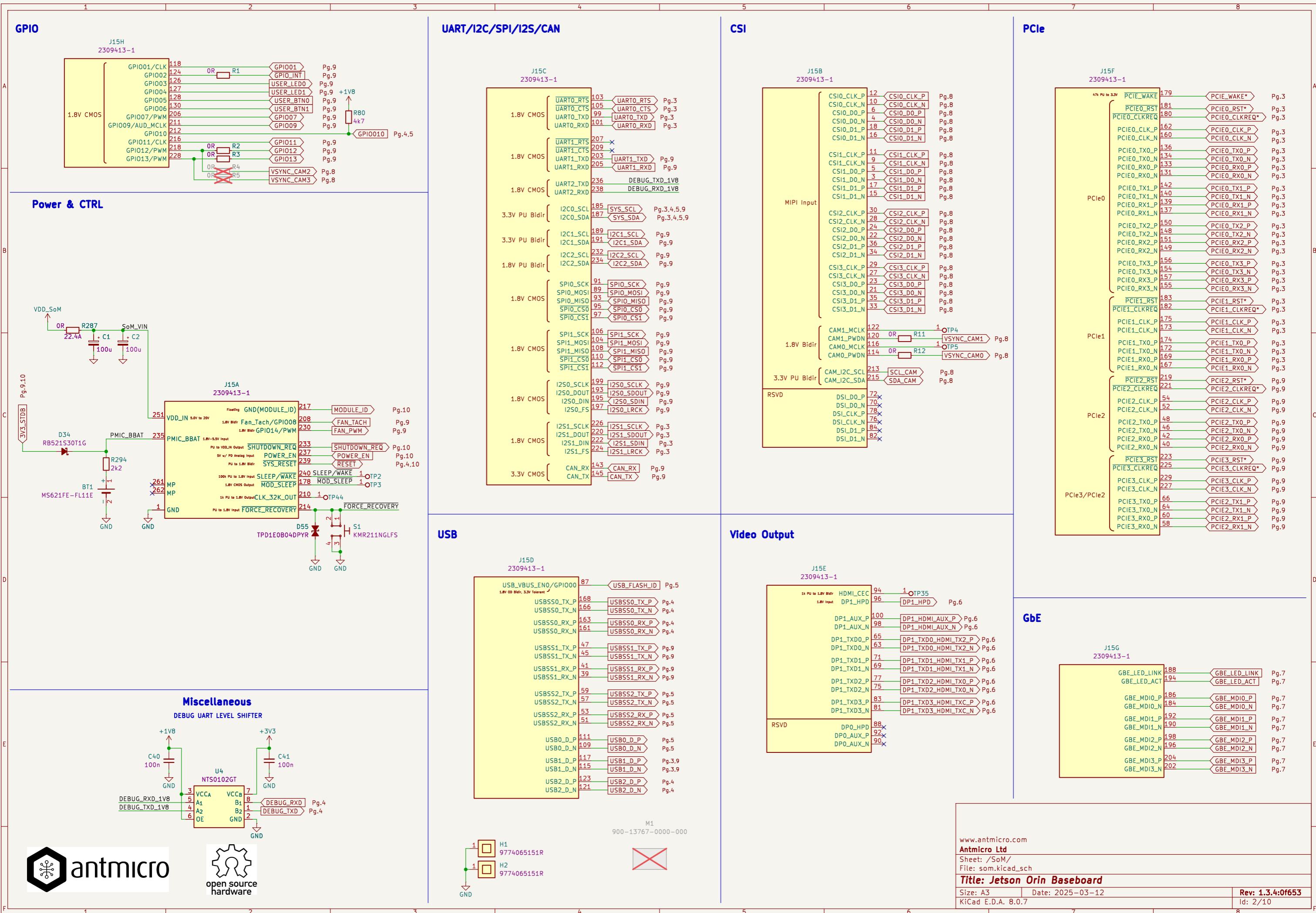
Size: A3 Date: 2025-03-12

KiCad E.D.A. 8.0.7

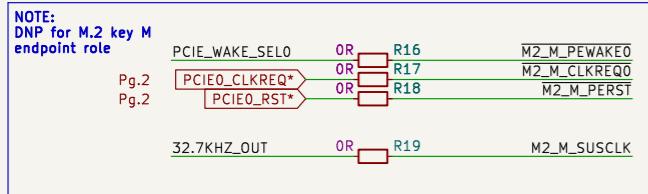
Rev: 1.3.4:0f653

Id: 1/10

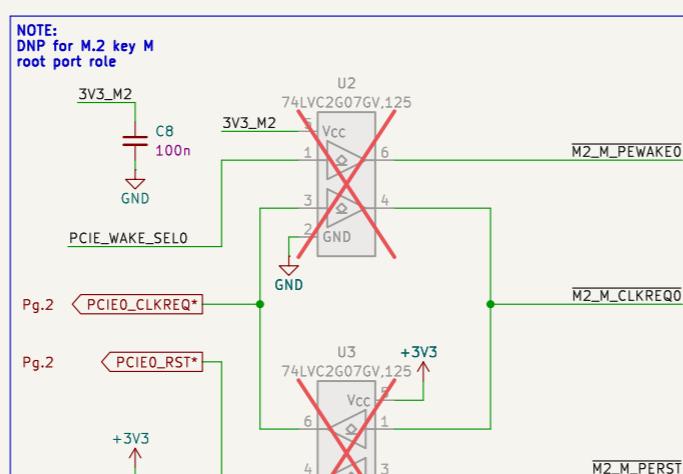
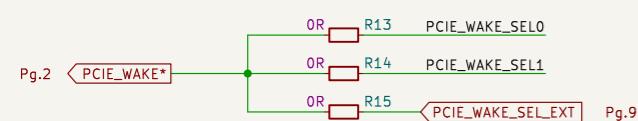
1 2 3 4 5 6 7 8 F



PCIe0 Root Port / Endpoint mode selector

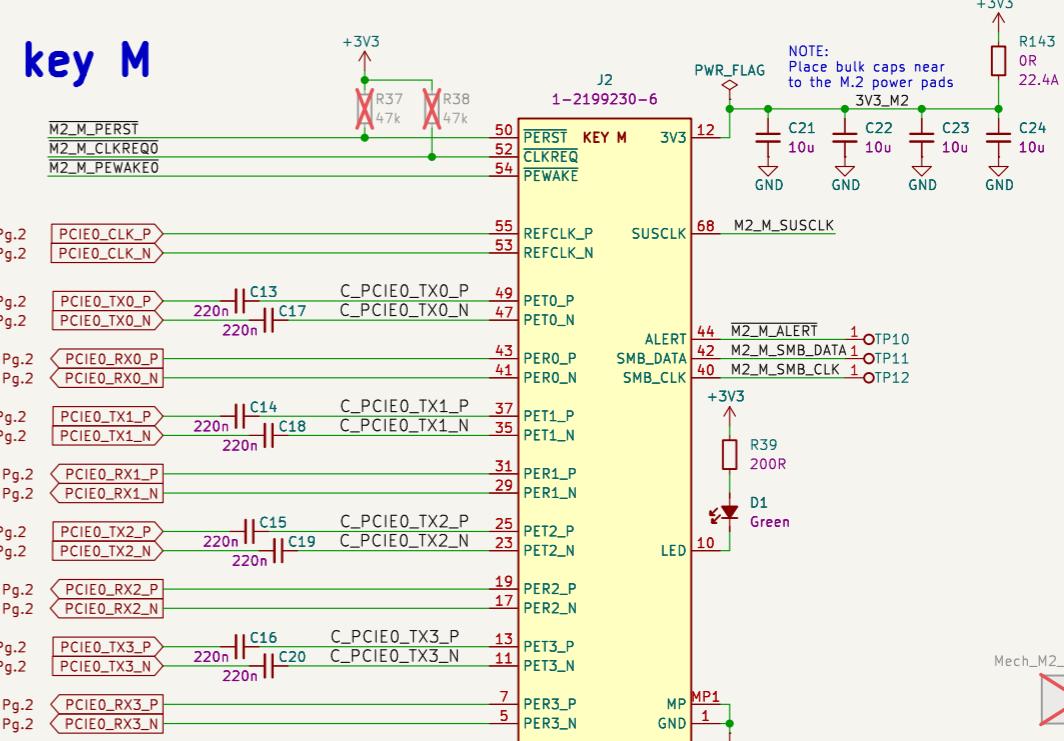


M.2 PCIE_WAKE selector



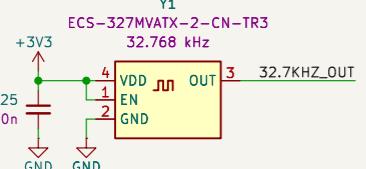
NOTE about PCIe 0 role switching:
For details, refer to the PCIe section of
'Jetson Orin NX Series Product Design Guide'
<https://developer.nvidia.com/jetson-orin-nx-series-design-guide>

PCIe0 1x4 key M

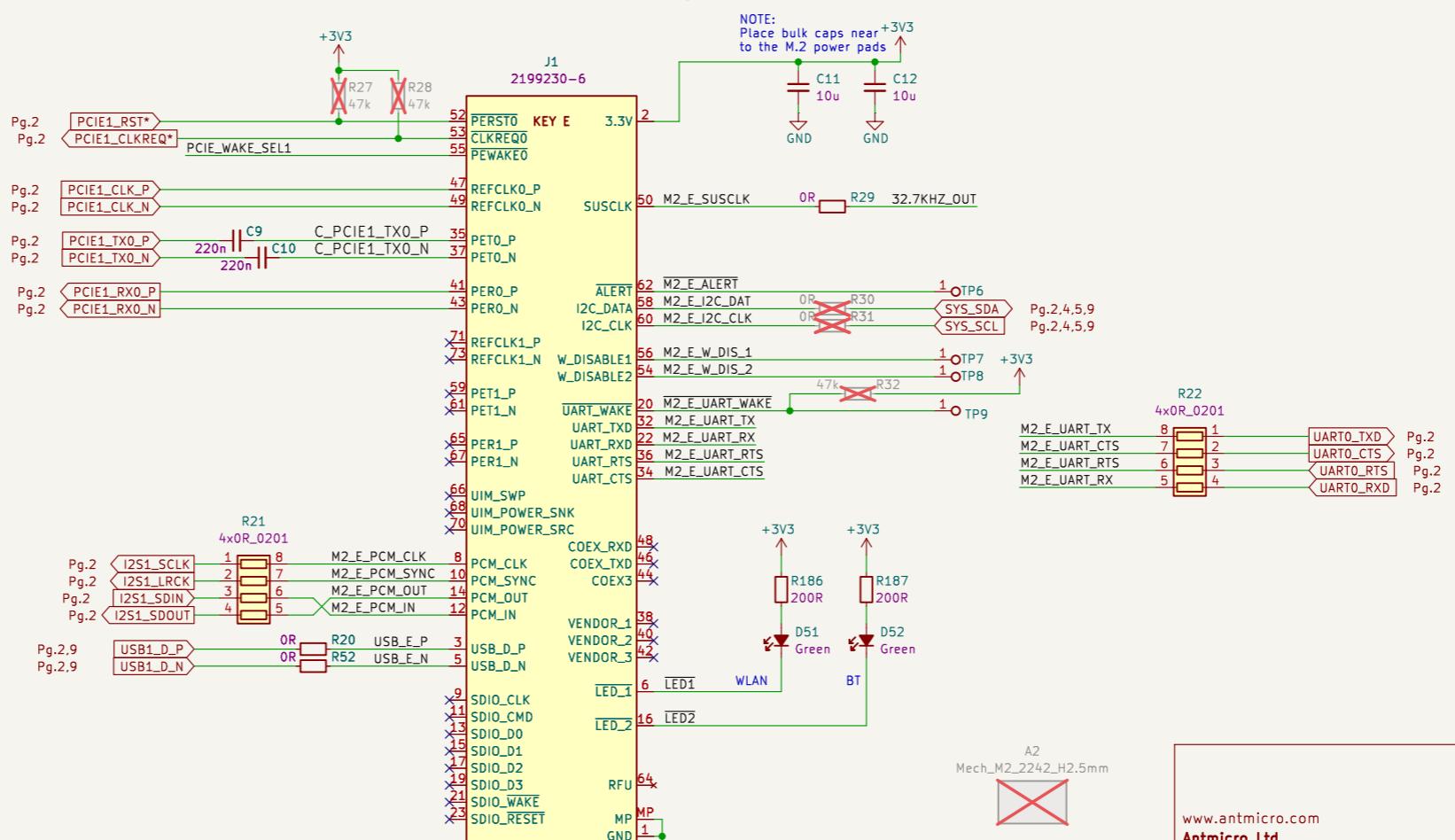


SP8 Spacer_Drill3.7mm_H2.5mm_9774025151R

SUSCLK SOURCE



PCIe1 1x1 key E



www.antmicro.com
Antmicro Ltd

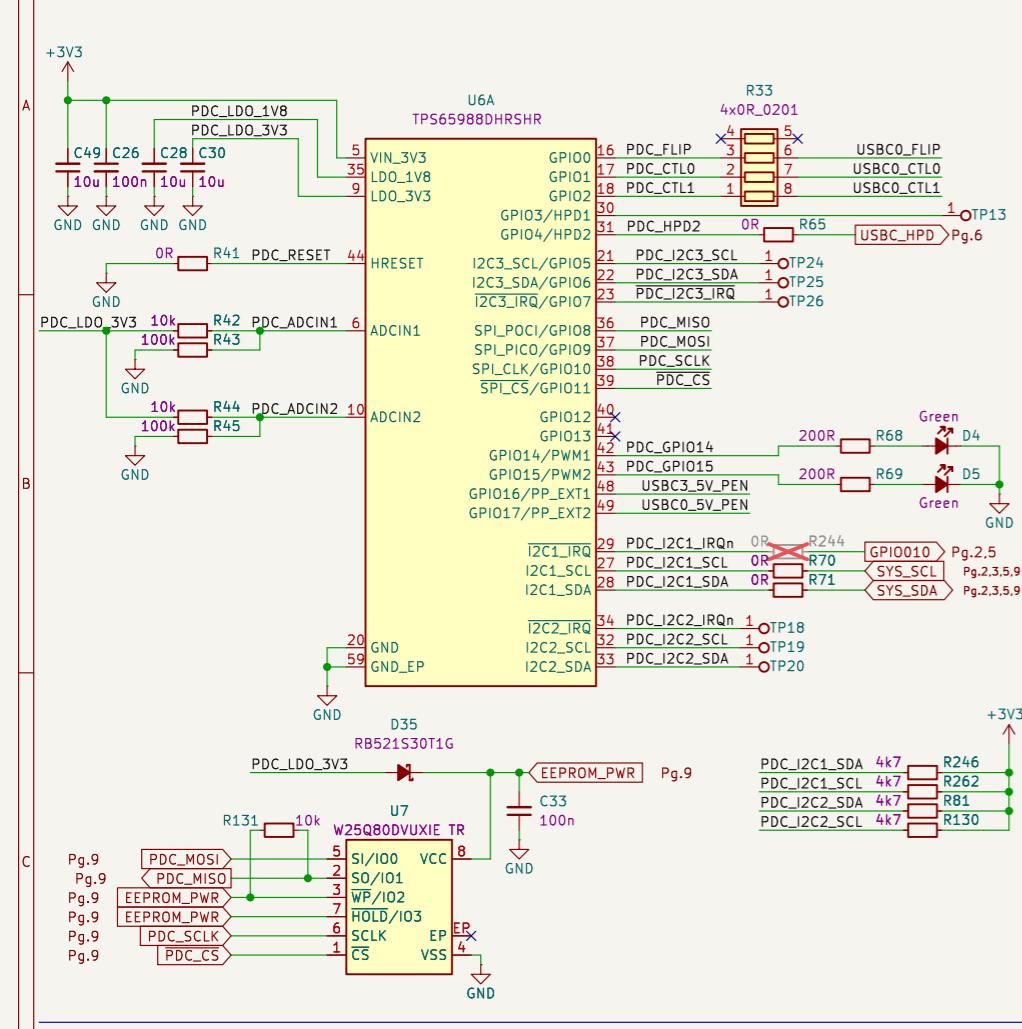
Sheet: /M.2/
File: m-2.kicad_sch

Title: Jetson Orin Baseboard

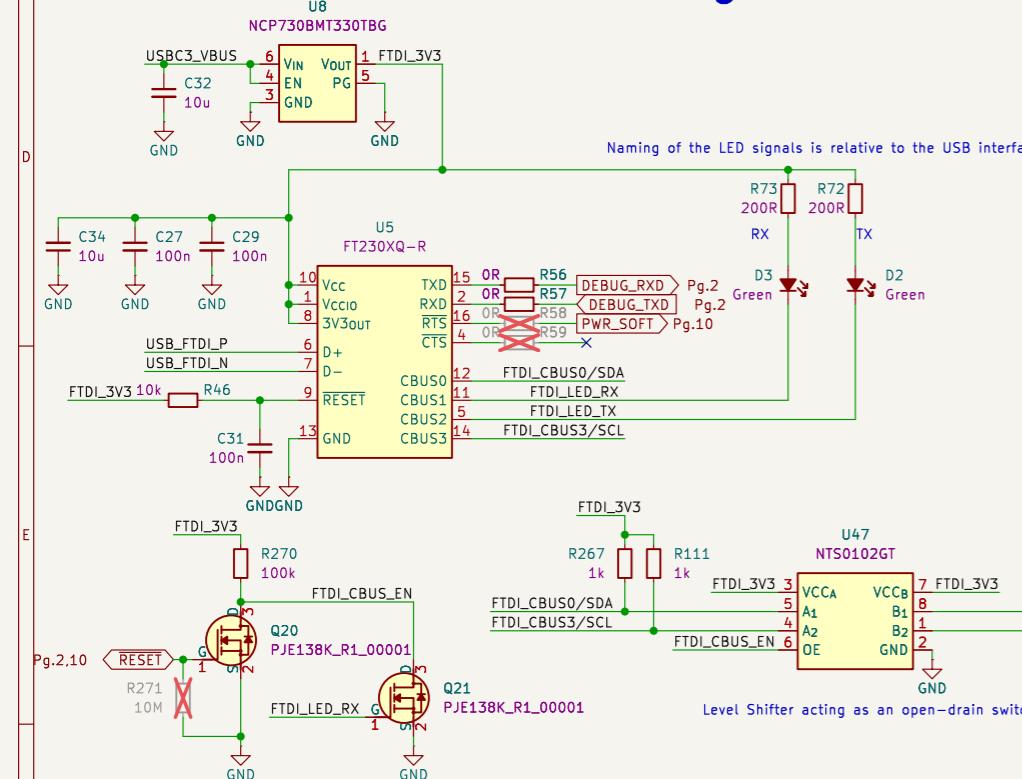
Size: A3 Date: 2025-03-12
KiCad E.D.A. 8.0.7

Rev: 1.3.4:0f653
Id: 3/10

USB PD Controller

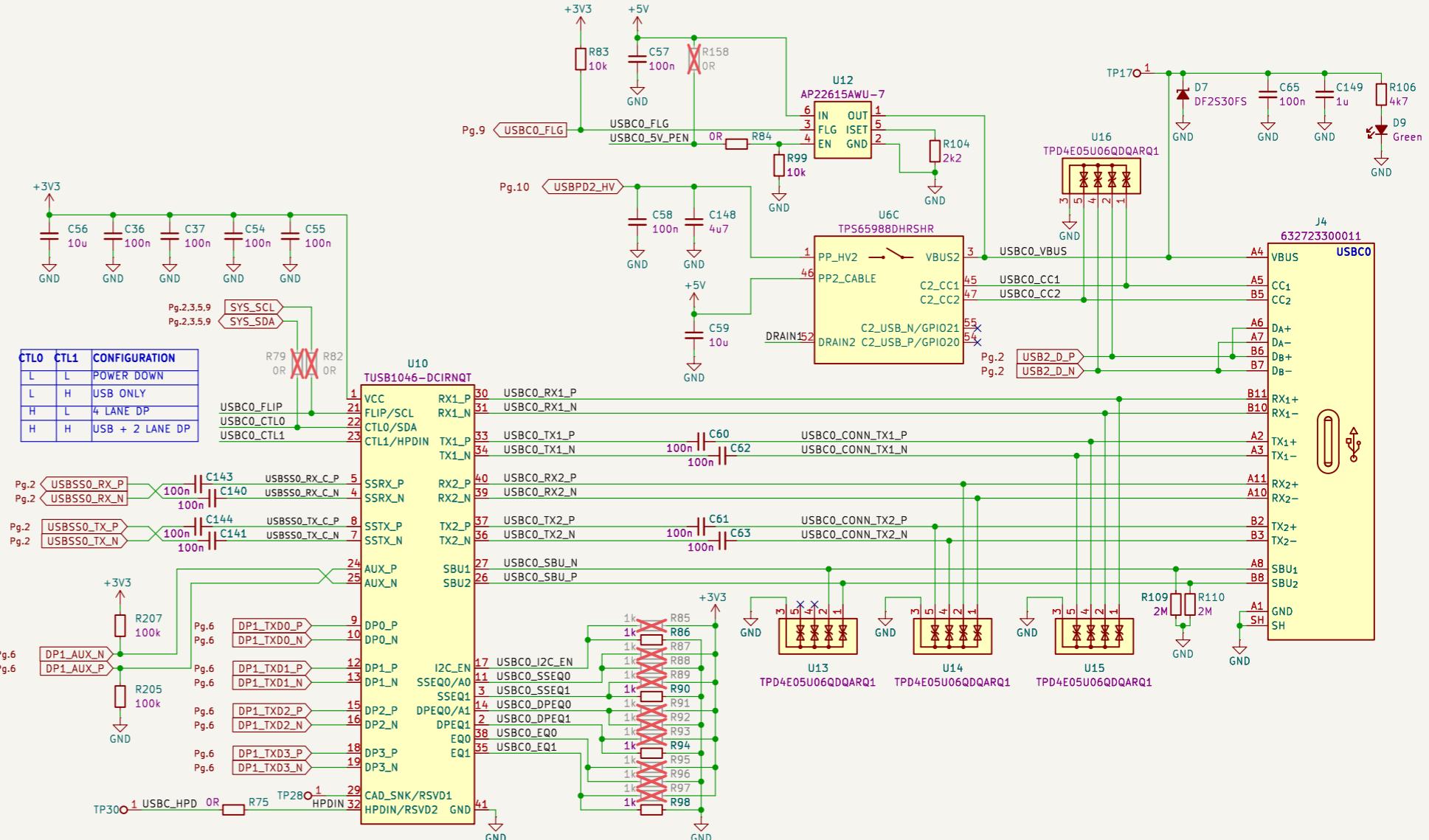


USB-UART Bridge

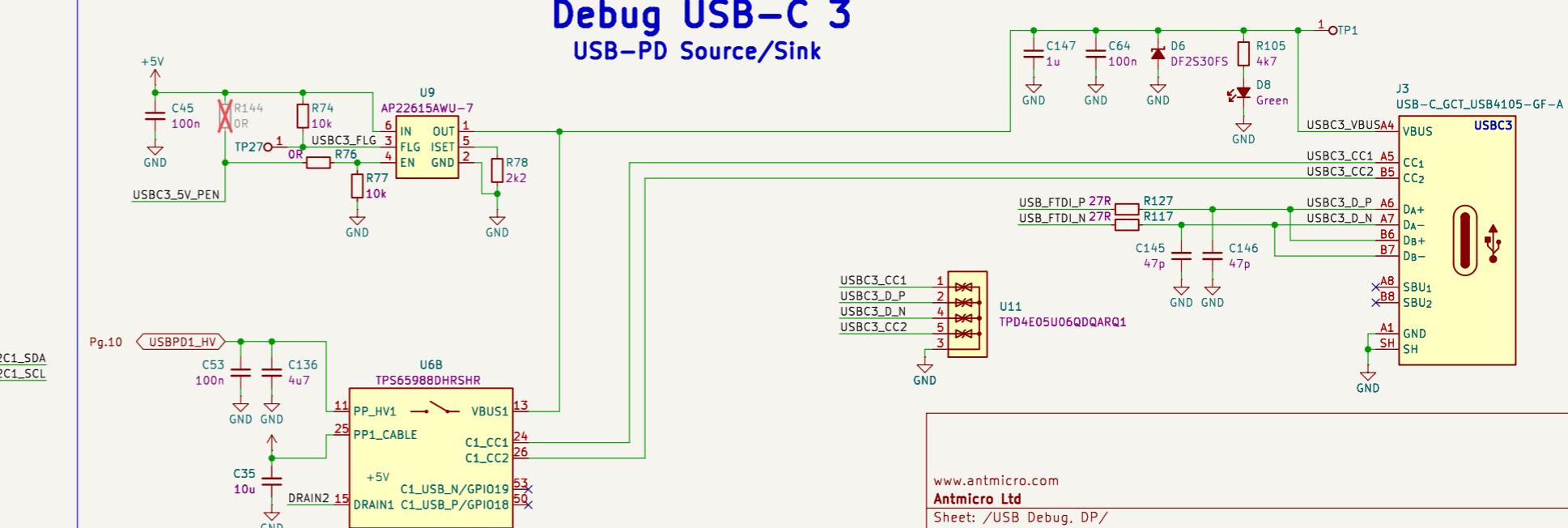


USB-C 0 Display Port alt mode

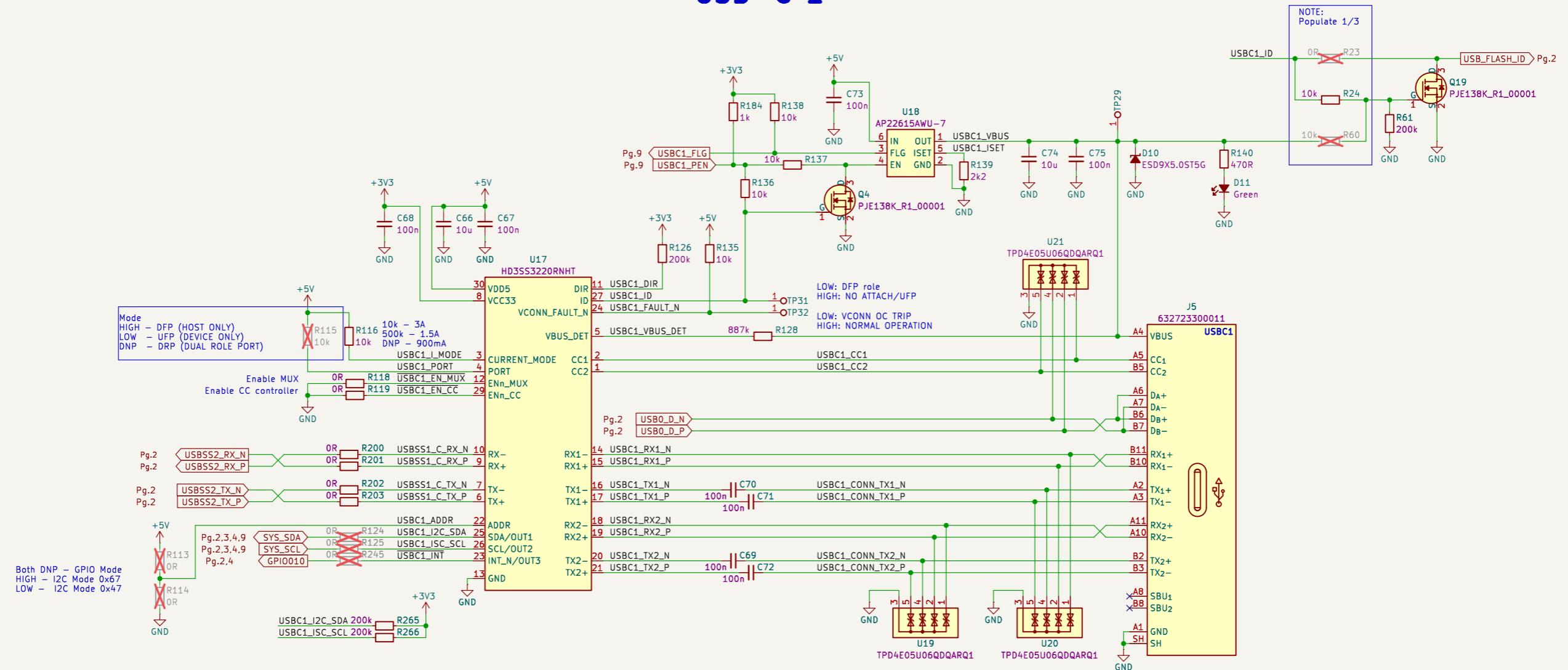
USB-PD Source/Sink



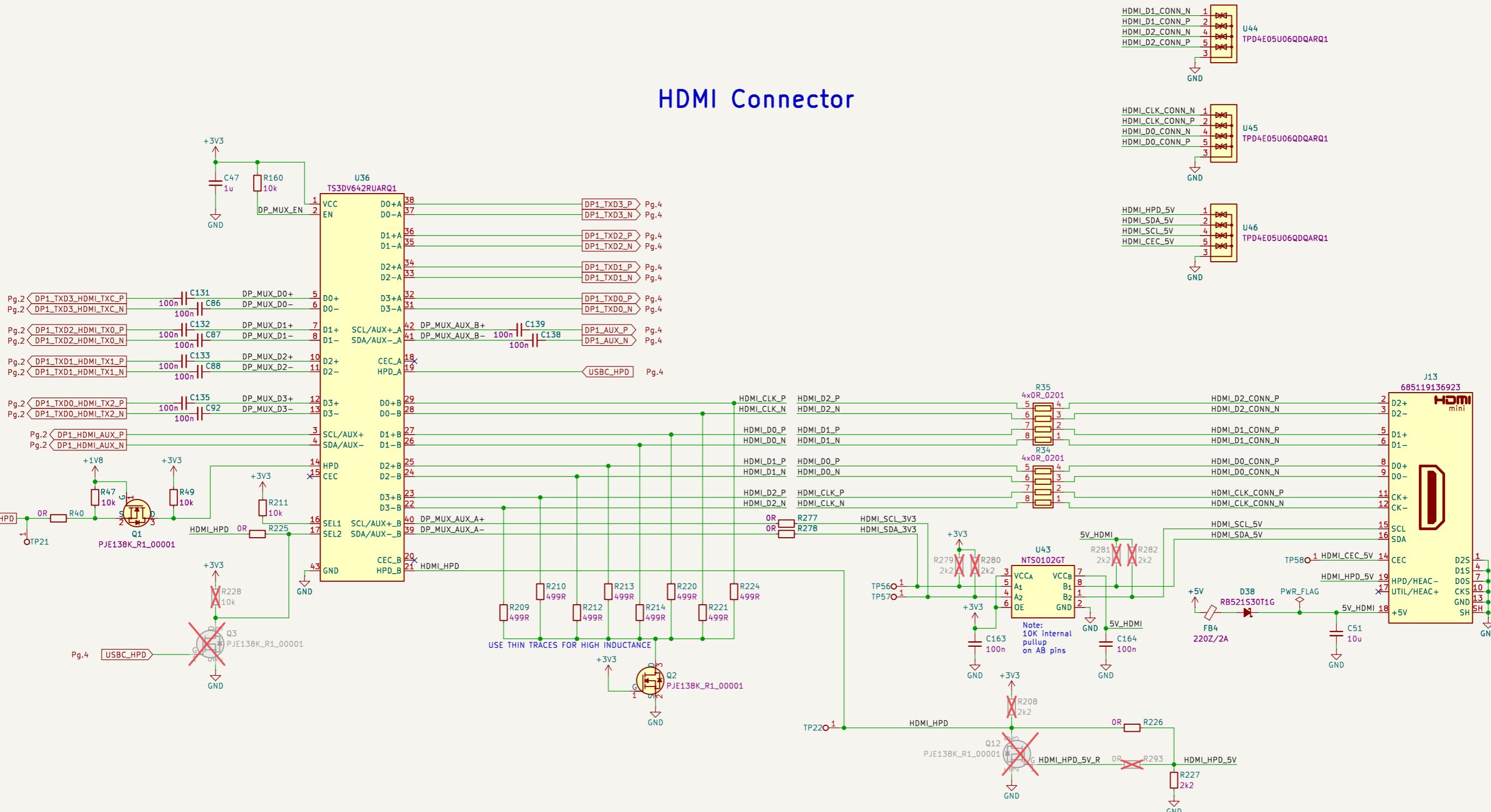
Debug USB-C 3
USB-PD Source/Sink



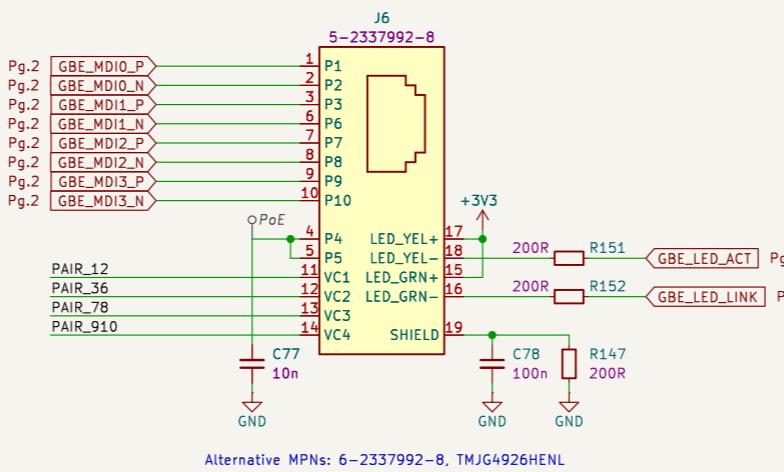
USB-C 1



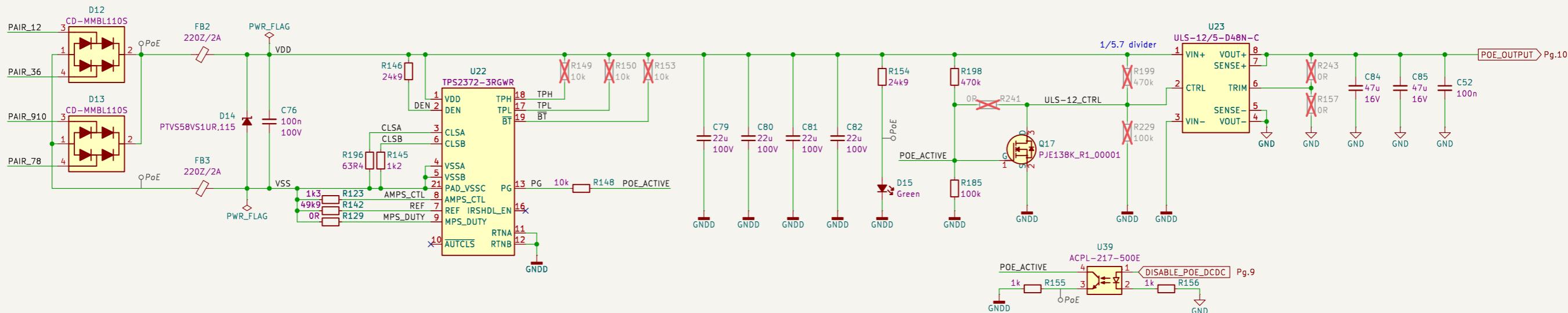
HDMI Connector

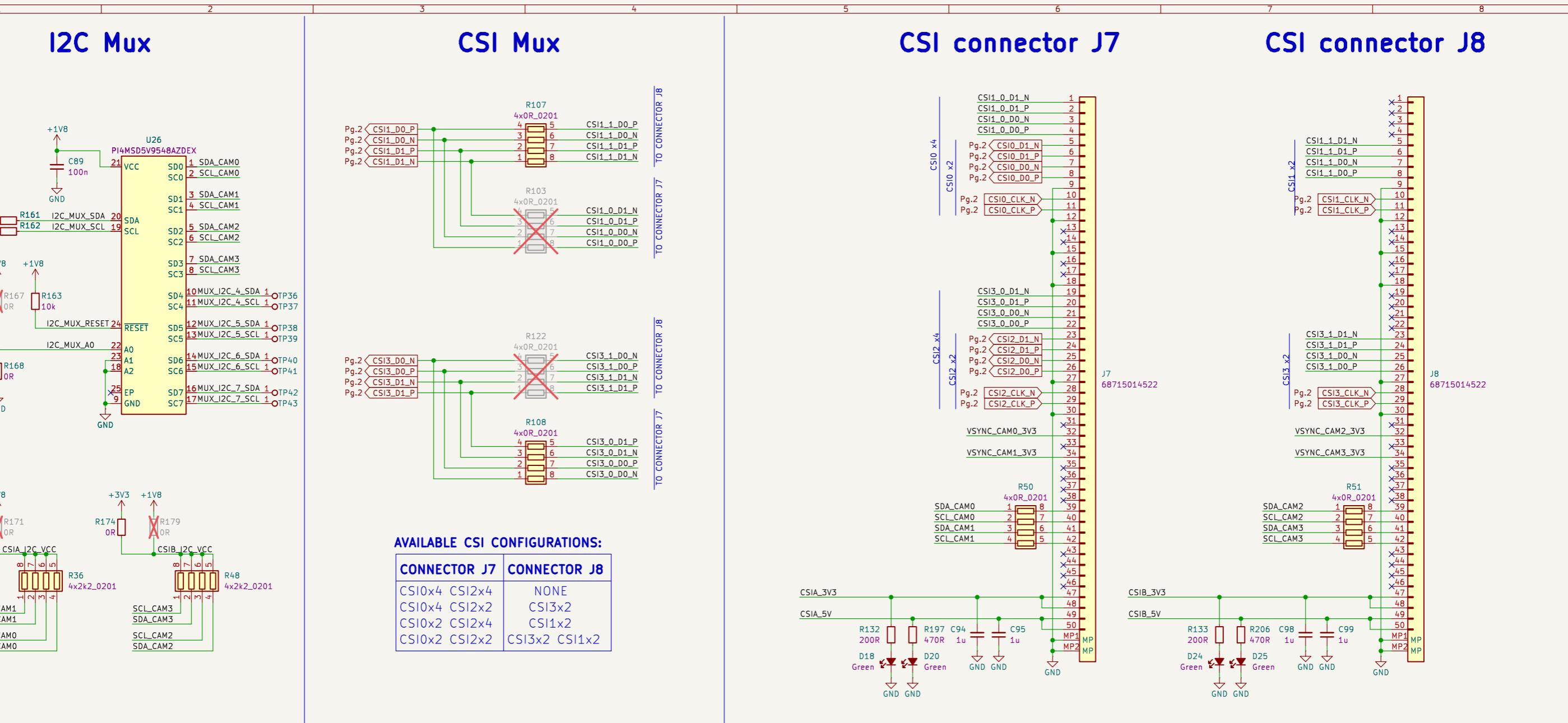


Ethernet Connector

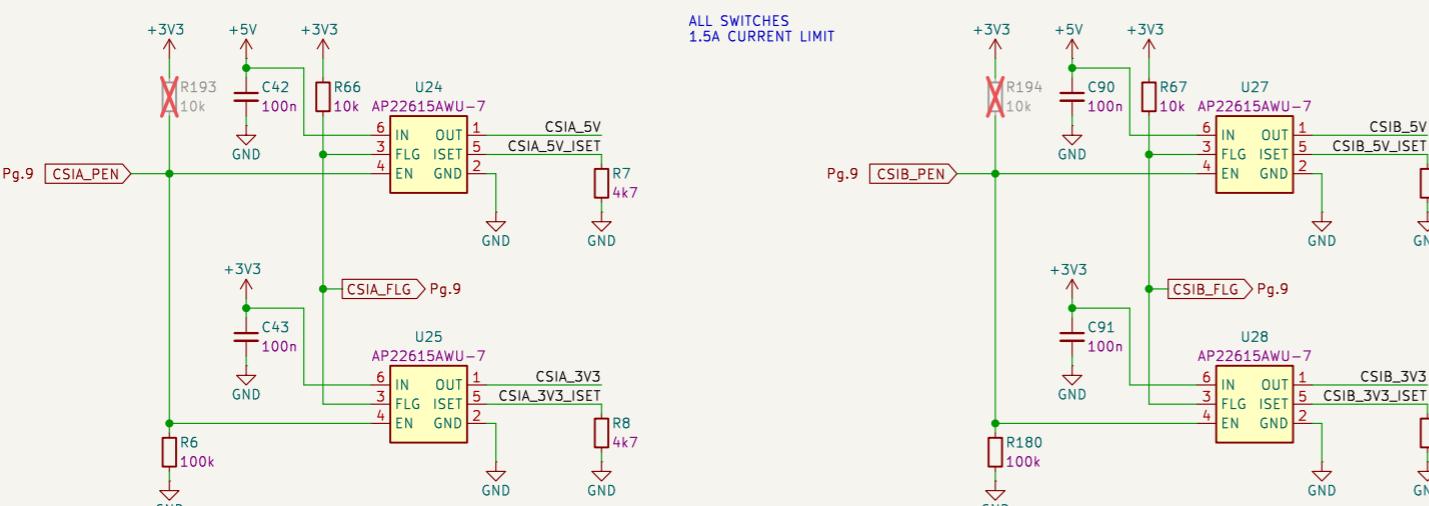


Power over Ethernet





Power switches



Level shifters

