

## Part 2 Report and Checkoff

### Report

Included below is a list of the necessary sections that are included in the report. Failure to at least include these sections will deduct from your grade.

1. Overview
2. Implemented Instructions
3. Components Added
4. Processor Speed
5. Testing
6. Group Experiences

The above required sections should include subsections where needed. None of these sections should be shorter than a full paragraph. Explain in detail each section/subsection. The testing section should be your largest section with waveform images showing **relevant information**. These images should not be larger than 1/4 of a page and you should not have more than 2 images per page. If you include an image and don't discuss it you will lose points. Your processor speed can be determined by your clock period and a formula given in the Sequential Logic notes on Piazza. **The only section that can have any first person writing is the group experiences.**

### Check off

For the checkoff you must demonstrate the following snippet of code using Modelsim. All instructions must work in order correctly to achieve points for the checkoff. The checkoff must be complete by the time the last TA leaves on Friday November 6th. **No late checkoffs will be given for any reason**

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Add	r2 , r0 , r0
Add	r3 , r0 , r2
Sub	r4 , r3 , r2
<b>Xor</b>	r5 , r4 , r0
<b>And</b>	r6 , r3 , r2
<b>Or</b>	r7 , r4 , r3

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