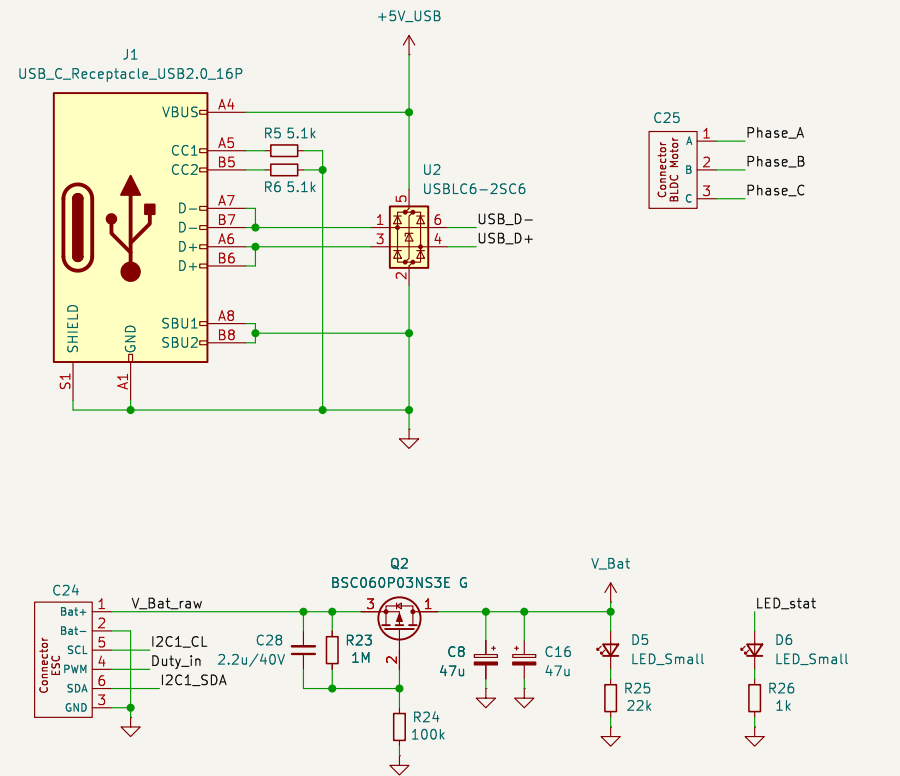
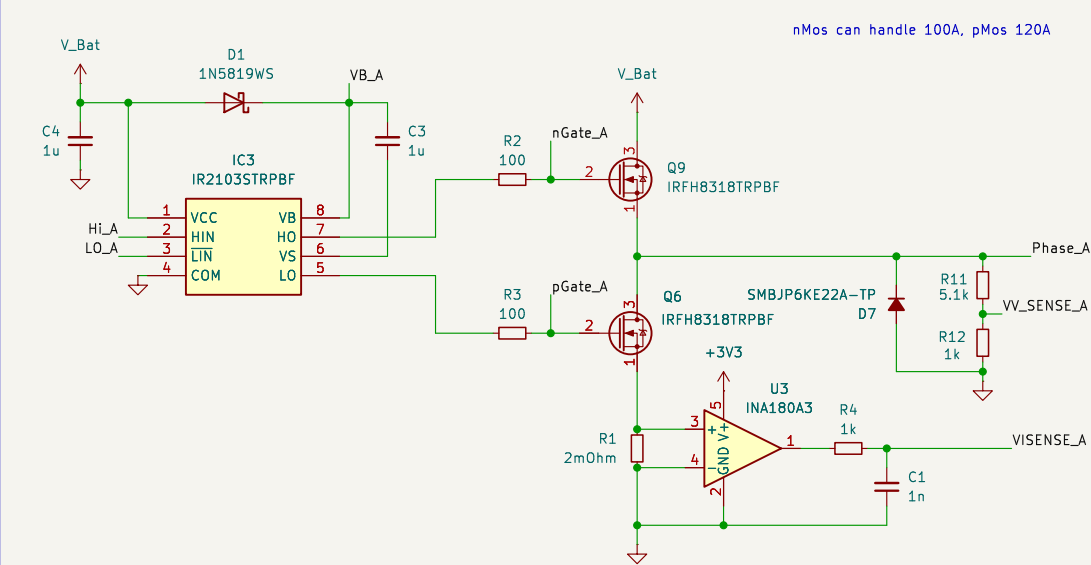


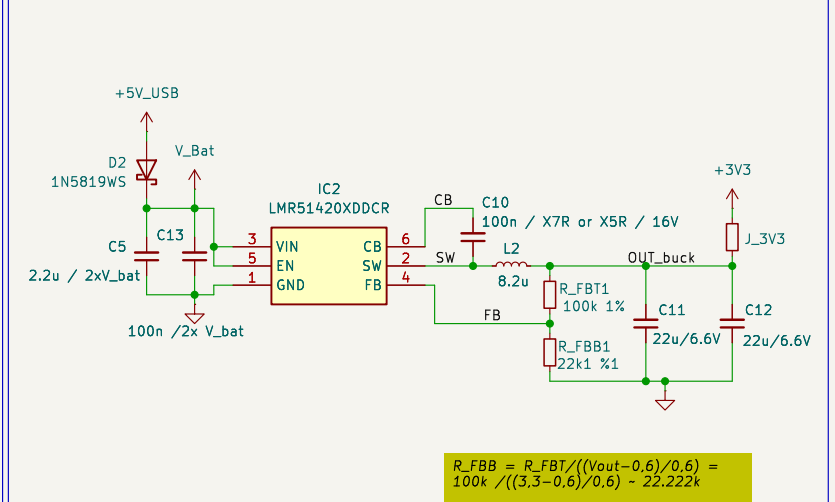
## I/O



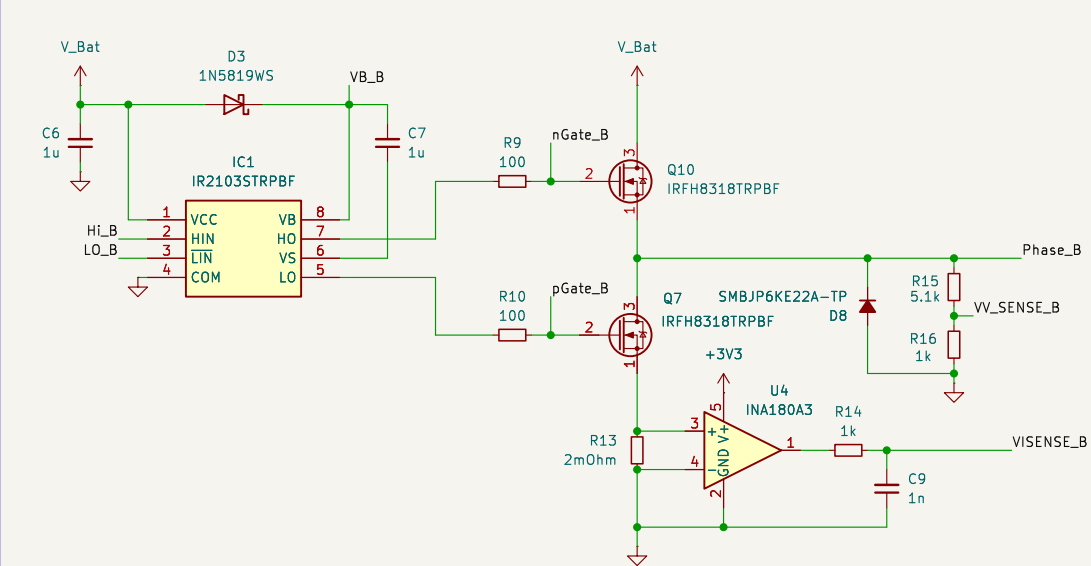
## PHASE A



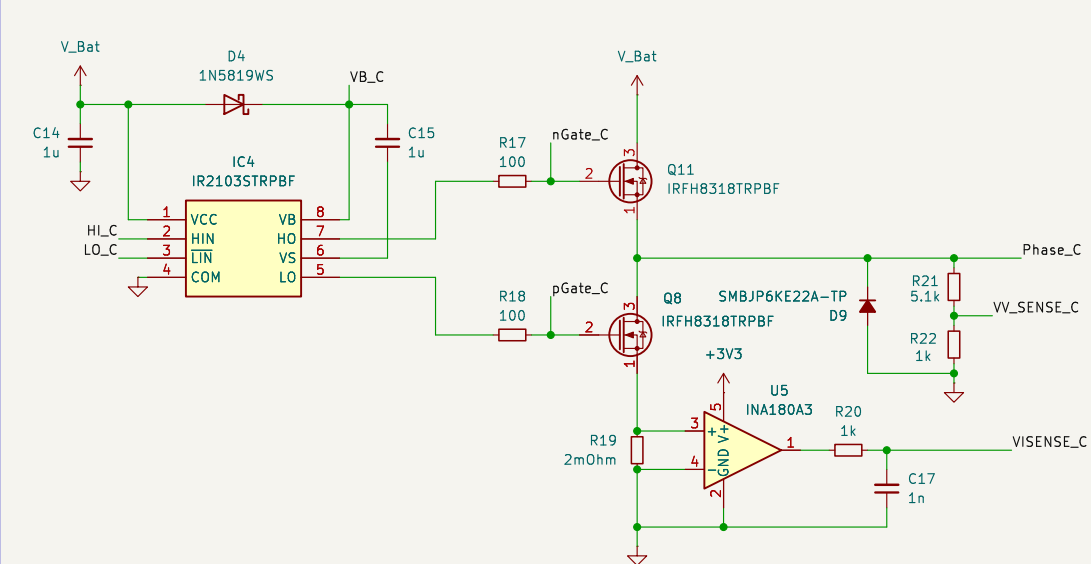
## BUCK CONVERTER



## PHASE B



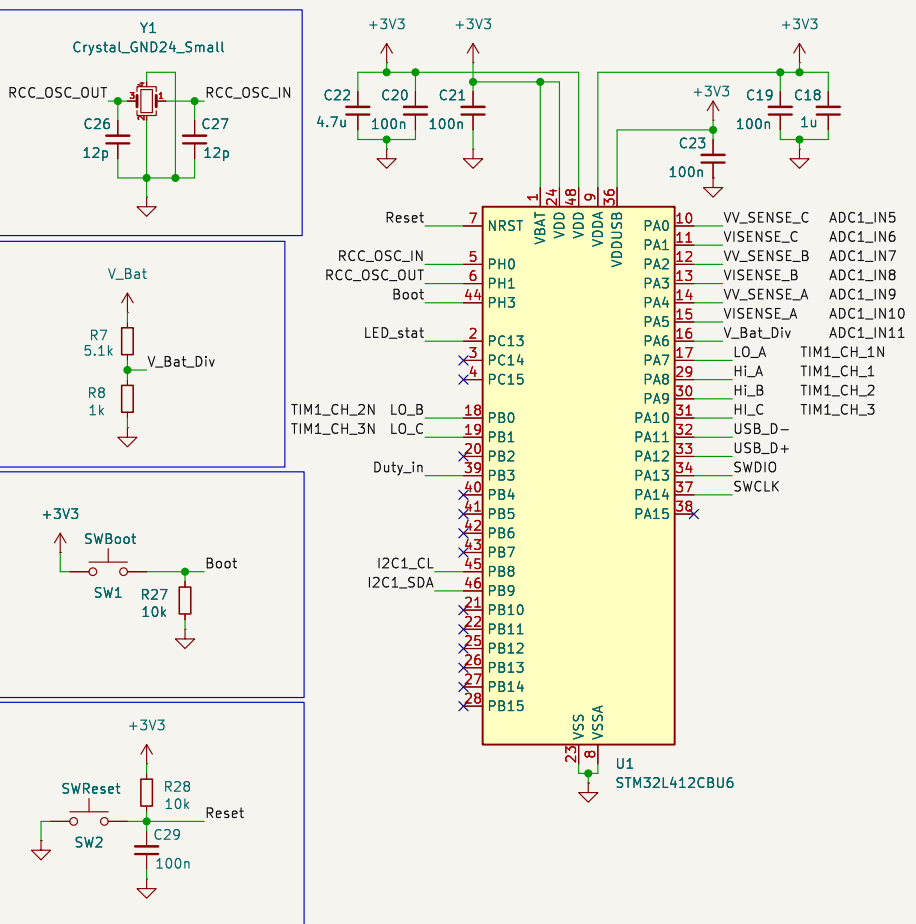
## PHASE C



**Calculations:**  
 Battery max 4.2V per Cell should drive till 54 batteries aka 16.8V  
 ESC Max pulsed current: 200A  
 Max continious current: 100A

**Design Errors**  
 -Nmos Source/Drain Switched  
 -Halfbridge Driver for 2 Nmos  
 -no Boot/Reset Buttons

## UC



H1 MountingHole  
 H2 MountingHole