# Computer Arithmetic Signed and Unsigned

# **Topics**

**Overflow** 

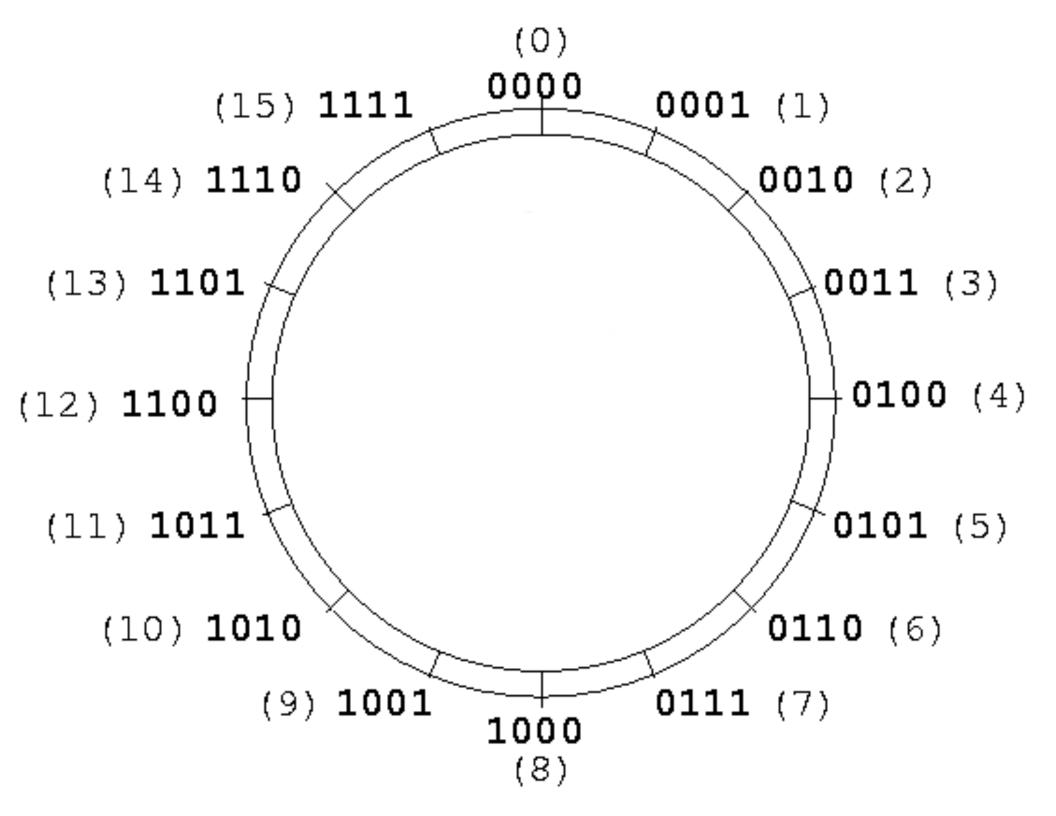
Addition and carry, subtraction and borrow

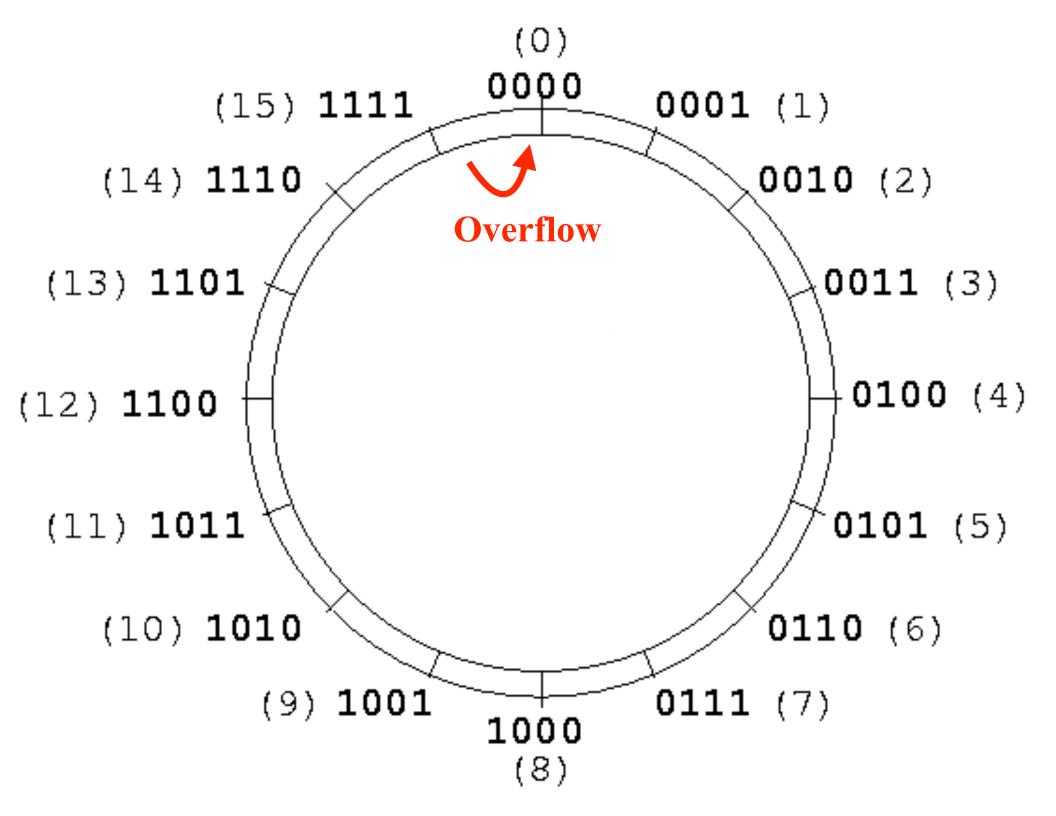
Processor flags: Z, N, C, V

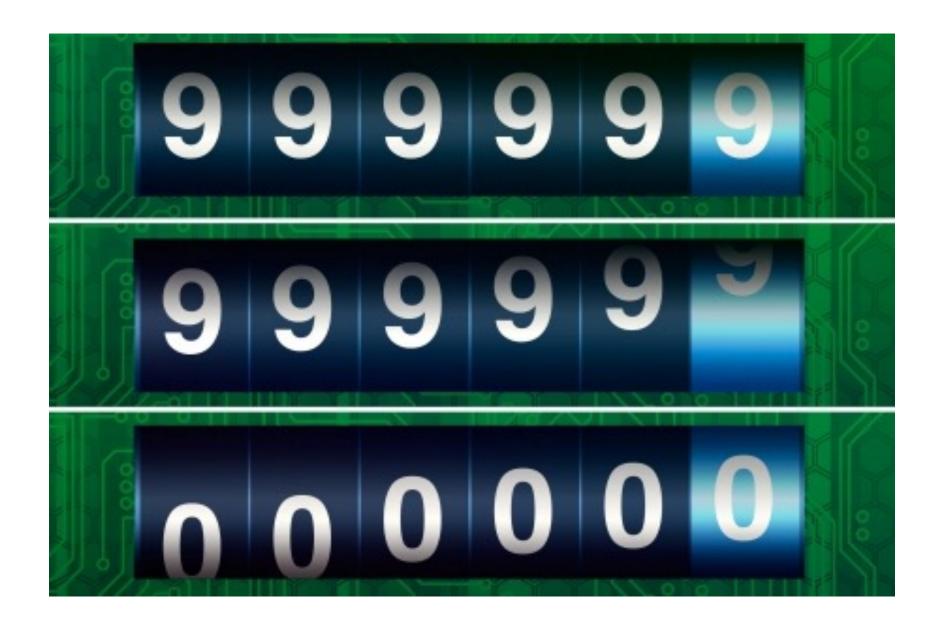
2's complement representation of negative numbers

Comparisons

Implicit type conversions (C craziness)







# What Happens at Overflow?

247

9

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## Addition (Hexadecimal)

**F7** 

**09** 

\_\_\_

```
Addition (Hexadecimal)
```

т F7

**09** 

\_ \_ \_

## Addition (Hexadecimal)

11

**F7** 

**09** 

\_ \_ \_

## Addition (Hexadecimal)

11

**F7** 

**09** 

\_ \_ \_

11110111 00001001

-----

```
1
11110111
00001001
----
```

```
11
11110111
00001001
----
```

```
111
11110111
00001001
-----
```

```
1111
11110111
00001001
```

```
11111
11110111
00001001
```

```
111111
11110111
00001001
```

```
1111111
11110111
00001001
```

```
11111111
11110111
00001001
```

0000000

```
Result: 00000000 (only room for 8-bits)
Carry (C): 1 (extra bit)
```

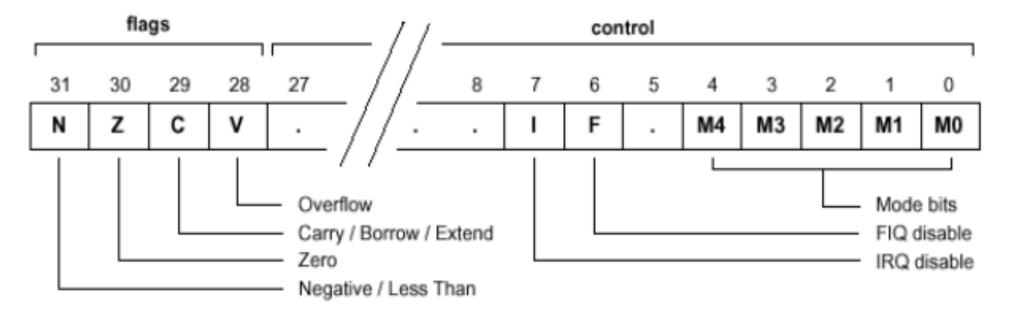
To hold the result of adding two n-bit numbers requires n+1 bits

add32/

#### add32

```
00000000 + 000000000 = 000000000 : Z=1, C=0
00000000 + 00000001 = 00000001 : Z=0, C=0
00000001 + 00000001 = 00000002 : Z=0, C=0
00000002 + 00000001 = 00000003 : Z=0, C=0
fffffffe + 00000001 = ffffffff : Z=0, C=0
ffffffff + 00000001 = 000000000 : Z=1, C=1
ffffffff + fffffffff = fffffffe : Z=0, C=1
```

#### **CPSR**



Arithmetic instructions set Z, C (N, V) Logic instructions just set Z (N)

(We will cover N soon, and V later)

```
// Multiple precision addition
// http://godbolt.org/g/HMYrme
int64 t add64(int64 t a, int64 t b)
 return a + b;
add64:
  adds r0, r0, r2
  adcs r1, r1, r3
  bx lr
```

# Negative (signed) Numbers

Up to now, all binary numbers have been positive (unsigned)

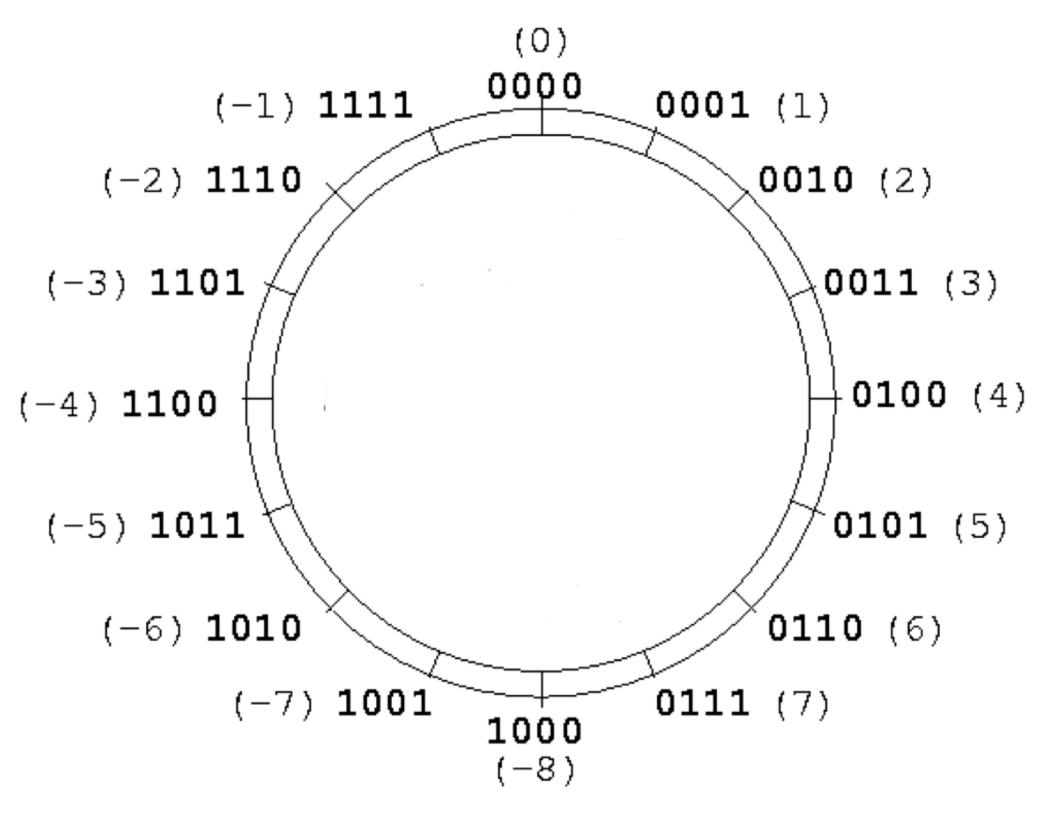
How would you define negative (signed) numbers?

A clever way of defining -1 is to say that -1 is the number that when added to 1, results in 0 (mod 16)

0xf + 0x1 = 0x10 % 16 = 0x0

0xf can be interpreted as -1

Note that modulo addition of signed numbers is exactly the same as addition of unsigned numbers!!



```
For 4-bit numbers,
0x0 = 0
0xf = -1
0xe = -2
0x8 = -8 (could be interpreted as 8)
0x7 = 7
0x1 = 1
```

0x0 = 0

if we choose to *interpret* 0x8 as -8, then the most-significant bit of the number indicates that it is negative (n)



How do we negate an 8-bit number?

Subtract the number from 100000000!

11111111

100000000

00000001

\_ \_ \_ \_ \_ \_ \_ \_ \_

11111111

1111111 (0xff) is -1

The 1's on the top are borrows

#### Another way

```
Rewrite 100000000 as 11111111 + 1
-x = (111111111+1)-x = (111111111-x)+1 = \sim x+1
  11111111
 -00000001
  11111110 = ~00000001 (~ means flip bits)
  11111110
 +00000001
  11111111
```

### Subtraction is converted to

### Why?

- Only one arithmetic op: add
- Need to think like this to understand when a carry is generated

#### **sub32/**

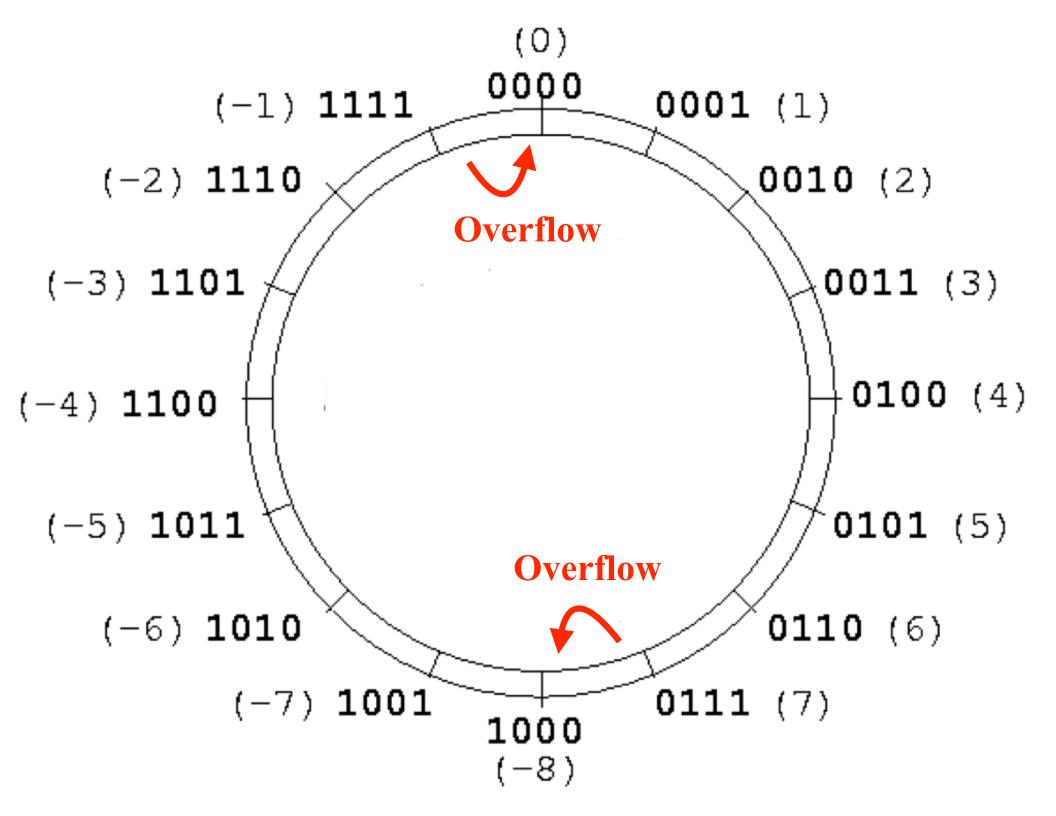
```
// What happens if the timer overflows?
unsigned timer get time(void) {
    return GET32(SYSTIMERCLO);
void delay us(unsigned us) {
    unsigned rb = timer get time();
    while (1) {
        unsigned ra = timer_get_time();
        // subtraction results in a positive
        // value even if the timer overflows
        if( (ra-rb) >= us {
            break;
```

# Addition / subtraction of signed and unsigned numbers is the same!

# Comparison

```
unsigned comparison: ucmp32/
>
0000001-0000000=0000001+fffffff+1=00000001
                                              C=1
ffffffff-0000001=fffffffff+ffffffe+1=fffffffe
                                              C=1
ffffffff-0000000=fffffffff+fffffff+1=ffffffff
                                              C=1
=
0000000-0000000=00000000+fffffff+1=00000000
                                              C=1
0000001-0000001=00000001+ffffffe+1=00000000
                                              C=1
fffffff-ffffffffffffff+0000000+1=00000000
                                              C=1
<
                                              C=0
0000000-0000001=0000000+ffffffe+1=fffffff
0000000-fffffff=00000000+0000000+1=00000001
                                              C=0
0000001-fffffff=00000001+00000000+1=00000002
                                              C=0
```

uge =

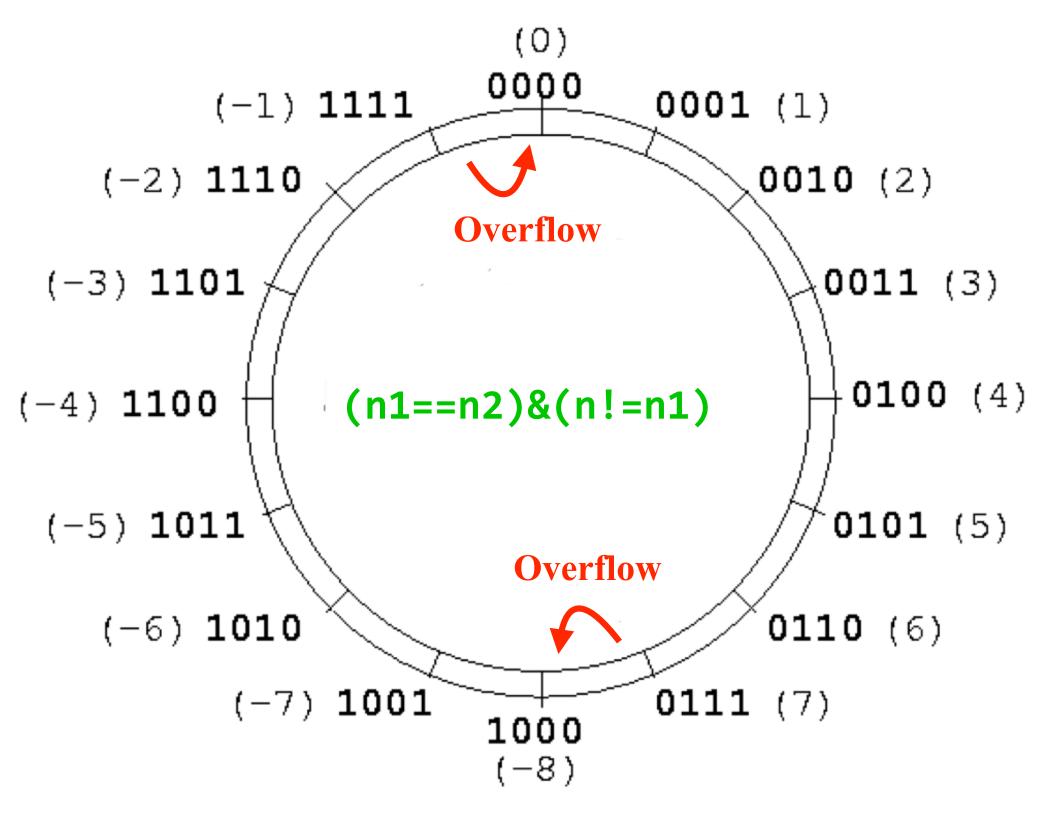


• In two's complement, when you exceed the maximum value of int (2,147,483,647), you "wrap around" to negative numbers:



Here is the link after Google upgraded to 64-bit integers:





```
signed comparison cmp32/
7fffffff-00000000=7ffffffff+fffffff+1=7fffffff N=0, V=0
0000000-80000000=00000000+7fffffff+1=80000000 N=1, V=1
7fffffff-8000000=7ffffffff+7fffffff+1=fffffffff N=1, V=1
0000000-0000000=00000000+fffffff+1=00000000 N=0, V=0
8000000-8000000=80000000+7fffffff+1=00000000 N=0, V=0
7ffffff-7ffffffffffff+8000000+1=00000000 N=0, V=0
0000000-7ffffff=00000000+80000000+1=80000001 N=1, V=0
8000000-0000000=80000000+ffffffff+1=80000000 N=1, V=0
8000000-7ffffff=80000000+80000000+1=00000001 N=0, V=1
V = (n1==n2)&(n!=n1)
```

```
signed comparison cmp32/
7fffffff-00000000=7ffffffff+ffffffff+1=7fffffff N=0, V=0
0000000-80000000=00000000+7fffffff+1=80000000 N=1, V=1
7fffffff-8000000=7ffffffff+7fffffff+1=fffffffff N=1, V=1
0000000-0000000=00000000+fffffff+1=00000000 N=0, V=0
8000000-8000000=80000000+7fffffff+1=00000000 N=0, V=0
7ffffff-7ffffffffffff+8000000+1=00000000 N=0, V=0
0000000-7ffffff=00000000+80000000+1=80000001 N=1, V=0
8000000-0000000=80000000+fffffff+1=80000000 N=1, V=0
8000000-7ffffff=80000000+80000000+1=00000001 N=0, V=1
bge: signed greater than or equal (n == v)
blt: signed less than (n != v)
int ge() { return !v ? !n : n }
```

Code	Suffix	Flags	Meaning		
0000	EQ	Z set	equal		
0001	NE	Z clear	not equal		
0010	CS	C set	unsigned higher or same		
0011	CC	C clear	unsigned lower		
0100	МІ	N set	negative		
0101	PL	N clear	positive or zero		
0110	VS	V set	overflow		
0111	VC	V clear	no overflow		
1000	НІ	C set and Z clear	unsigned higher		
1001	LS	C clear or Z set	unsigned lower or same		
1010	GE	N equals V	greater or equal		
1011	LT	N not equal to V	less than		
1100	GT	Z clear AND (N equals V)	greater than		
1101	LE	Z set OR (N not equal to V)	less than or equal		
1110	AL	(ignored)	always		

# Methods used to compare signed and unsigned numbers are NOT the same!

### **Type Conversion**

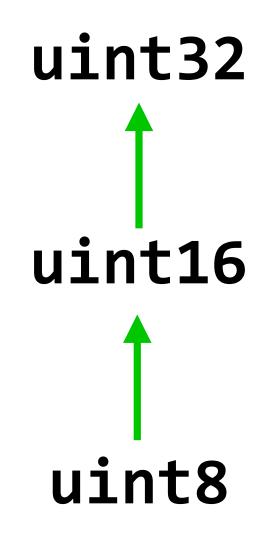
## Jedi Job Interview Questions

```
uint16_t x = 0xffff;
uint32_t y = x;

// what is the value of y?
```

# Type Hierarchy uint32 uint16 uint8

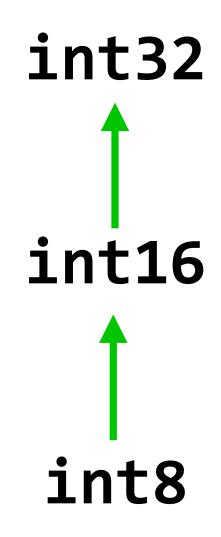
Types are *sets* of allowed values
Arrow indicate *subsets*: uint8 ⊂ uint16



Safe (values preserved)

```
int16_t x = -1;
int32_t y = x;

// what is the value of y?
```



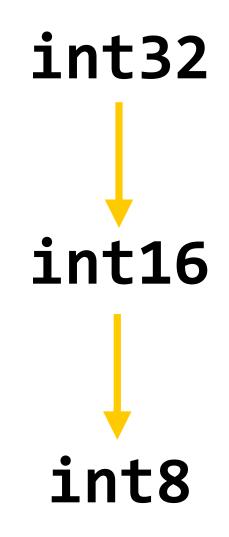
Safe (values preserved)

```
// Sign extend
int8 0xFE -> int32 0xFFFFFFE
int8 0x7E -> int32 0x000007E
```

```
// Assembly language
LSL r0,r0,#24
ASR r0,r0,#24
```

```
int32_t x = -1;
int16_t y = x;

// what is the value of y?
```



Defined (throw away msb)
Dangerous (doesn't preserve all values)

```
int32_t x = -1;
uint32_t y = x;

// what is the value of y?
```

uint32 — int32

uint16 — int16

uint8 — int8

Defined (copies bits\*)

uint32 — int32

uint16 — int16

uint8 — int8

Dangerous! (neg become large)

```
uint32_t x = 0xffffffff;
int32_t y = x;

// what is the value of y?
```

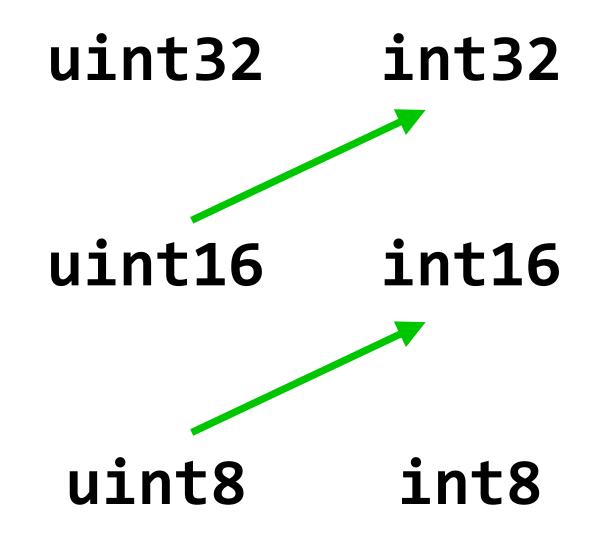
uint32 int32

uint16—int16

uint8 — int8

Technically Not Defined (copies bits)

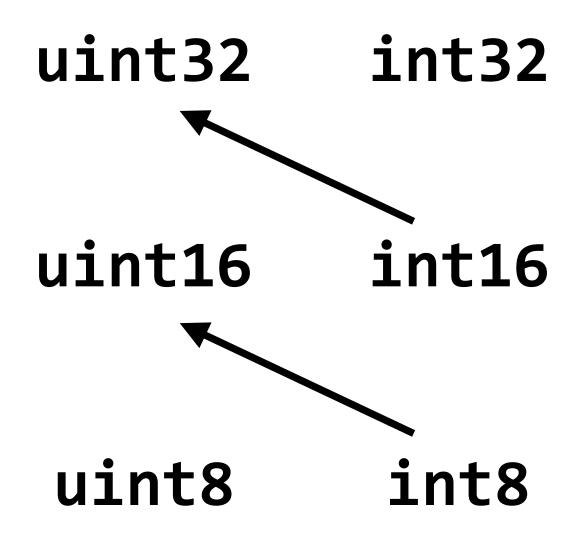
```
uint16_t x = 0xffff;
int32_t y = x;
// what is the value of y?
```

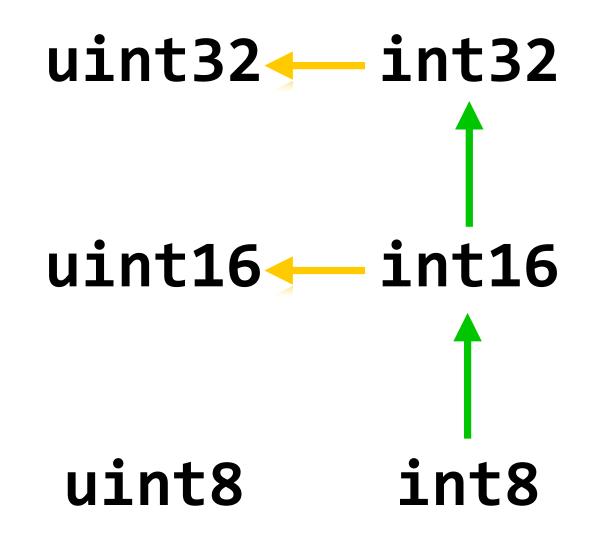


Safe (values preserved)

```
int16_t x = -1;
uint32_t y = x;

// what is the value of y?
```





Defined, Dangerous

The semantics of numeric casts are:

Casting from a larger integer to a smaller integer (e.g. u32 -> u8) will truncate

Casting from a smaller integer to a larger integer (e.g. u8 -> u32) will zero-extend if the source is unsigned sign-extend if the source is signed

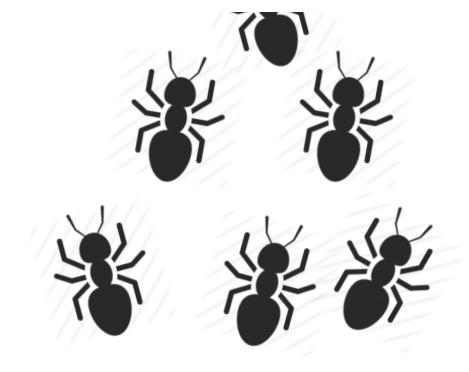
Casting between two integers of the same size (e.g. i32 -> u32) is a no-op

## **Binary Operations**

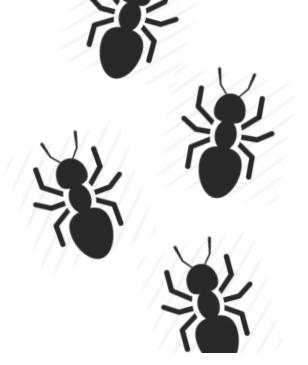
# Implicit Type Conversion (Coercion)

### Resulting type from a binary operator (ARM32)

	u8	u16	u32	u64	i8	i16	i32	i64
u8	i32	i32	u32	u64	i32	i32	i32	i64
u16	i32	i32	u32	u64	i32	i32	i32	i64
u32	u32	u32	u32	u64	u32	u32	u32	i64
u64								
i8	i32	i32	u32	u64	i32	i32	i32	i64
i16	i32	i32	u32	u64	i32	i32	i32	i64
i32	i32	i32	u32	u64	i32	i32	i32	i64
i64	i64	i64	i64	u64	i64	i64	i64	i64



# Bugs, Bugs, Bugs



```
#include <stdio.h>
int main(void)
    int a = -20;
    unsigned int b = 6;
    if( a < b )
        printf("-20 < 6 - all is well</pre>
\n");
    else
        printf("-20 >= 6 - omg \n");
```

Whenever you mix signed and unsigned numbers you get in trouble

Bjarne Stroustrup

### **C Promotion Rules**

- 6.3.1.3 Signed and unsigned integers conversions
- 1 When a value with integer type is converted to another integer type, if the value can be represented by the new type, it is unchanged.
- 2 Otherwise, if the new type is unsigned, the value is converted by repeatedly adding or subtracting one more than the maximum value that can be represented in the new type until the value is in the range of the new type.
- 3 Otherwise, if the new type is signed and the value cannot be represented in it; either the result is implementation-defined or an implementation-defined signal is raised.

#### 6.3.1.8 Usual arithmetic conversions

1 If both operands have the same type, then no further conversion is needed.

2 Otherwise, if both operands have signed integer types or both have unsigned integer types, the operand with the type of lesser integer conversion rank is converted to the type of the operand with greater rank.

3 Otherwise, if the operand that has unsigned integer type has rank greater or equal to the rank of the type of the other operand, then the operand with signed integer type is converted to the type of the operand with unsigned integer type.

4 Otherwise, if the type of the operand with signed integer type can represent all of the values of the type of the operand with unsigned integer type, then the operand with unsigned integer type is converted to the type of the operand with signed integer type.

5 Otherwise, both operands are converted to the unsigned integer type corresponding to the type of the operand with signed integer type.