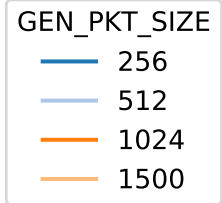


Changing packet size and #RX descriptors for L2 forwarding application

Sum of PCIe Read Misses

1e9

1.4
1.2
1.0
0.8
0.6
0.4
0.2
0.0



Number of RX Descriptors

128 256 512 1024 2048 4096

