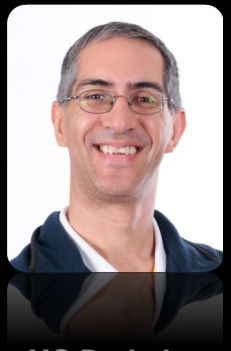


UC Berkeley
Teaching Professor
Dan Garcia

CS61C

Great Ideas in Computer Architecture

(a.k.a. Machine Structures)

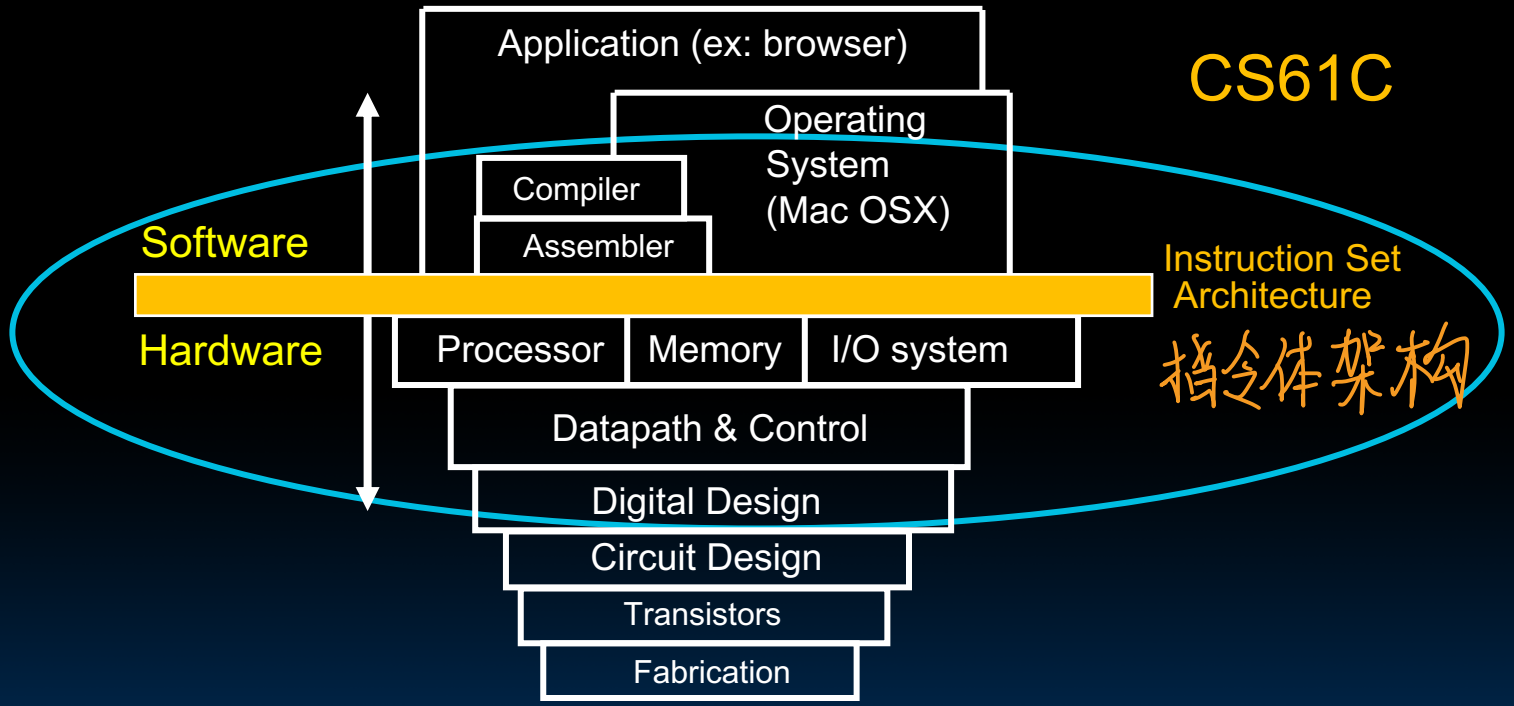
UC Berkeley
Teaching Professor
Lisa Yan

同步数字系统

Introduction to Synchronous Digital Systems (SDS): Switches, Transistors, Signals, & Waveforms

Switches

Machine Structures



New-School Machine Structures

Software

Parallel Requests
Assigned to computer
e.g., Search "Cats"

Parallel Threads
Assigned to core e.g., Lookup, Ads

Parallel Instructions
>1 instruction @ one time
e.g., 5 pipelined instructions

Parallel Data
>1 data item @ one time
e.g., Add of 4 pairs of words

Hardware descriptions
All gates work in parallel at same time

Harness Parallelism & Achieve High Performance

Warehouse Scale Computer

逻辑门

Great Idea #1: Abstraction (Levels of Representation/Interpretation)

High Level Language Program (e.g., C)

```
temp = v[k];
v[k] = v[k+1];
v[k+1] = temp;
```

Compiler

Assembly Language Program (e.g., RISC-V)

```
lw    x3, 0(x10)
lw    x4, 4(x10)
sw    x4, 0(x10)
sw    x3, 4(x10)
```

Assembler

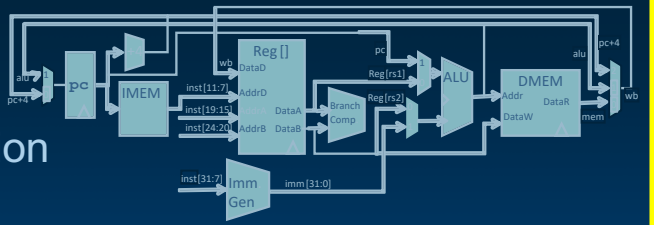
Machine Language Program (RISC-V)

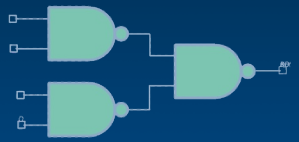
```
1000 1101 1110 0010 0000 0000 0000 0000
1000 1110 0001 0000 0000 0000 0000 0100
1010 1110 0001 0010 0000 0000 0000 0000
1010 1101 1110 0010 0000 0000 0000 0100
```

Hardware Architecture Description (e.g., block diagrams)

Architecture Implementation

Logic Circuit Description (Circuit Schematic Diagrams)





Synchronous Digital Systems

- Hardware of a processor, e.g., RISC-V, is a Synchronous Digital System 同步数字系统
- Synchronous:
  - All operations coordinated by a central clock
  - "Heartbeat" of the system!
- Digital: 离散值
  - All values represented by discrete values
  - Electrical signals are treated as 1s and 0s; grouped together to form words 0, 1表示

操作与中央时钟协调

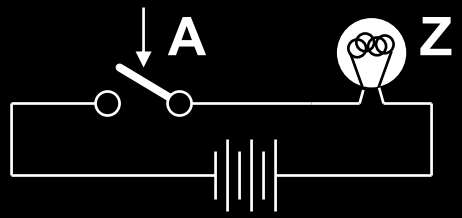
Logic Design

- Next several weeks: we'll study how a modern processor is built; starting with basic elements as building blocks
- Why study hardware design?
  - Understand capabilities and limitations of HW in general and processors in particular
  - What processors can do fast and what they can't do fast (avoid slow things if you want your code to run fast!)
  - Background for more in depth HW courses (150, 152)
  - There is just so much you can do with standard processors: you may need to design own custom HW for extra performance

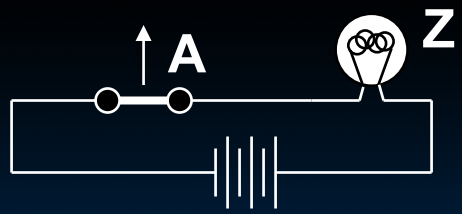
design hardware

Switches: Basic Element of Physical Circuit

- Implementing a simple circuit
  - Close switch when **A** is 1, open when **A** is 0



Close switch (if **A** is "1" or asserted) and turn on light bulb (**Z**)

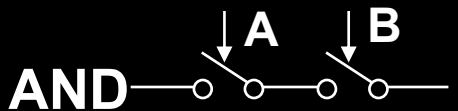


Open switch (if **A** is "0" or unasserted) and turn off light bulb (**Z**)

$$Z \equiv A$$

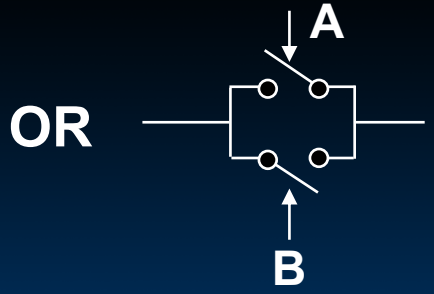
Switches (continued)

- Compose switches into more complex ones (Boolean functions):



A & B

Z ≡ A and B

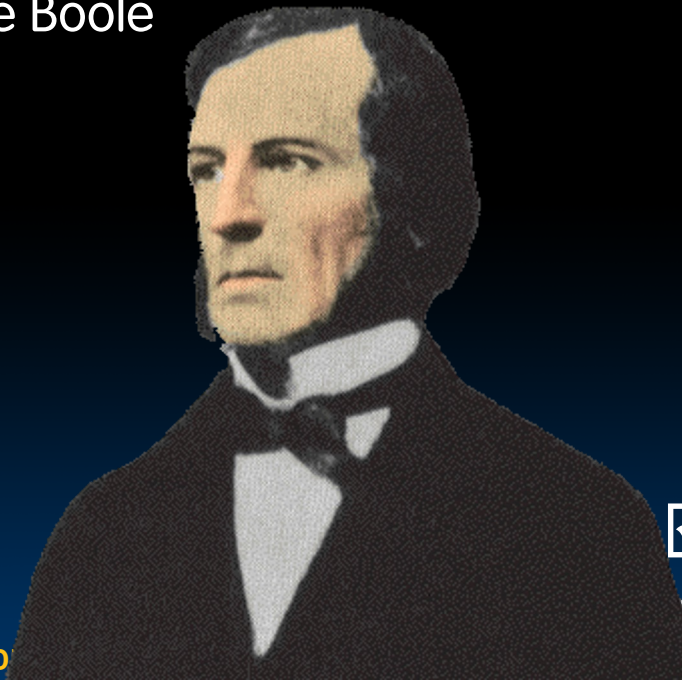


A || B

Z ≡ A or B

Historical Note

- Early computer designers built ad hoc circuits from switches
- Began to notice common patterns in their work: ANDs, ORs, …
- Master's thesis (by Claude Shannon) made link between transistors and 19th Century Mathematician George Boole
  - Called it "Boolean" in his honor
- Could apply math to give theory to hardware design, minimization, …



Transistors

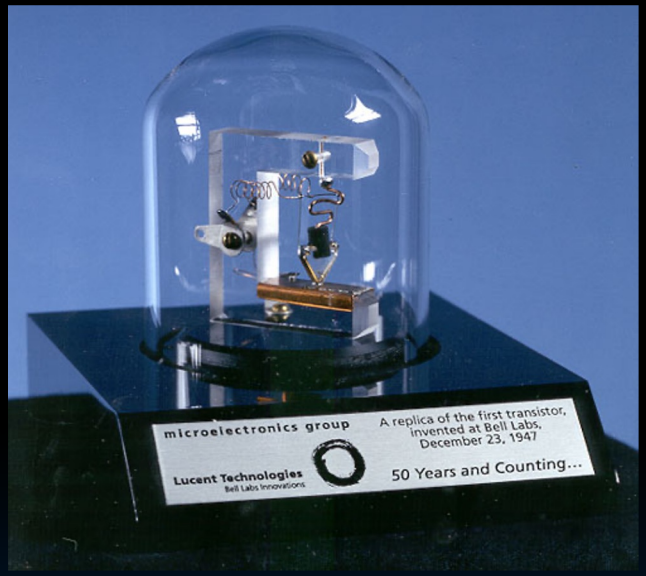
三极管

The Transistor ("born" 1947-12-23)

en.wikipedia.org/wiki/History_of_the_transistor
www.pbs.org/transistor
youtu.be/-td7YT-Pums
youtu.be/OwS9aTE2Go4

半导体 设备 放大 开关 信号

- Semiconductor device to amplify or switch signals
  - Key component in ALL modern electronics
- Who?
  - John Bardeen, William Shockley, Walter Brattain
- Before that?
  - Vacuum Tubes
- After that?
  - Integrated circuit, microprocessor



"The Transistor was probably THE most important invention of the 20th Century"
- Ira Flatow, Transistorized! (PBS Special)

Transistor Networks

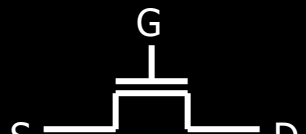
- Modern digital systems designed in CMOS
  - MOS: Metal-Oxide on Semiconductor 半导体上的金属氧化物
  - C for complementary: normally-open and normally-closed switches

- MOS transistors act as voltage-controlled switches
  电压控制开关

MOS Transistors

- Three terminals: Drain, Gate, Source
  - Switch action: Dan Garcia Says
    if voltage on gate terminal is (some amount) higher/lower than source terminal then conducting path established between drain and source terminals

To remember:
n ("normal")
p (has a circle, like the top part of P itself)



n-channel

open when voltage at G is low

closes when:

voltage(G) > voltage (S) + ε

p-channel

closed when voltage at G is low

opens when:

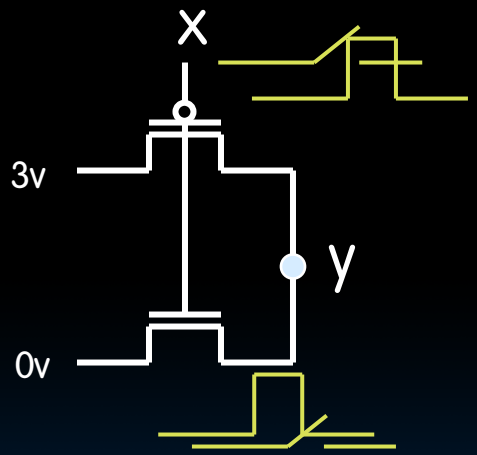
voltage(G) > voltage (S) + ε

MOS Networks

P型

"1" (voltage source)

"0" (ground)

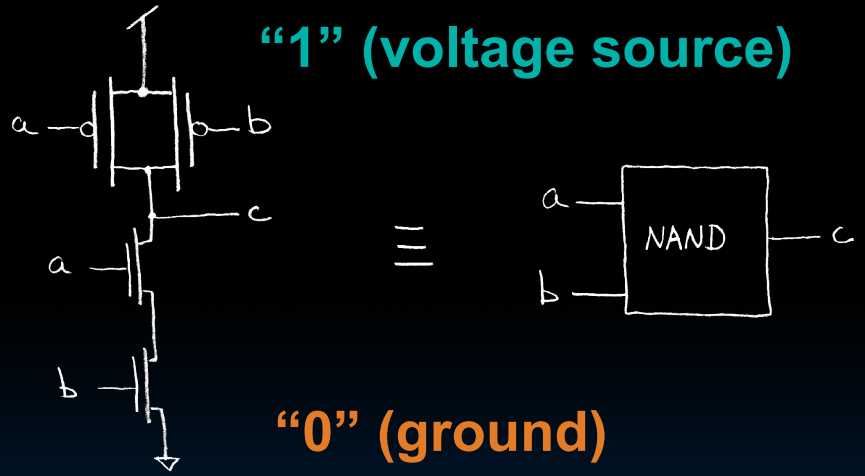


What is the relationship between x and y ?

x	y
0 volts	3 volts
3 volts	0 volts

Transistor Circuit Rep. vs. Block diagram

- Chips are composed of nothing but transistors and wires.
- Small groups of transistors form useful building blocks.



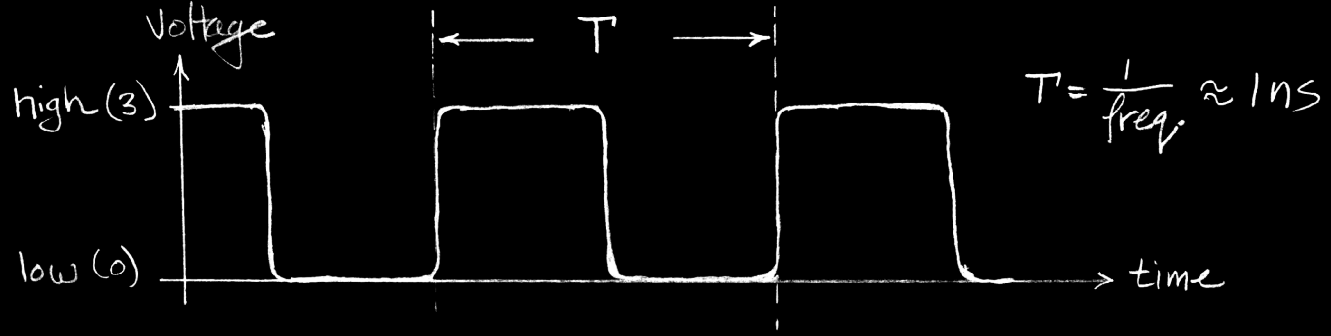
a	b	c
0	0	1
0	1	1
1	0	1
1	1	0

- Block are organized in a hierarchy to build higher-level blocks: ex: adders.
- You can build AND, OR, NOT out of NAND!

Signals and Waveforms

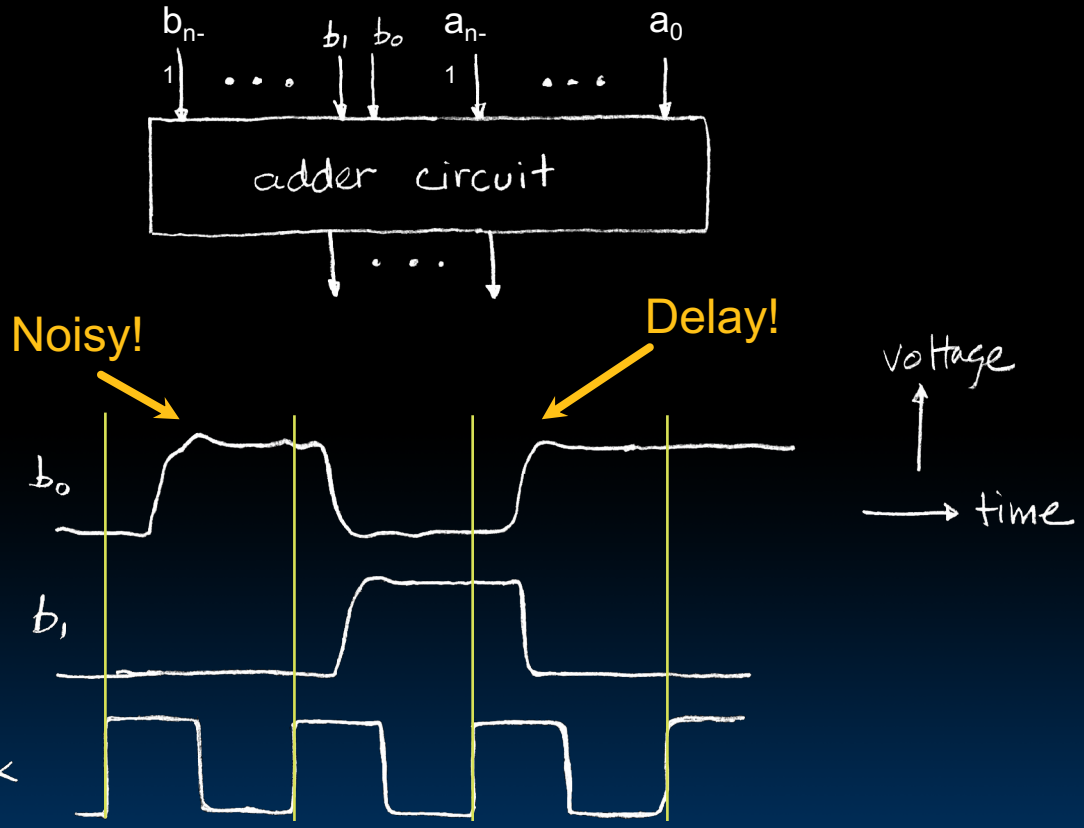
信号与波形

Signals and Waveforms: Clocks

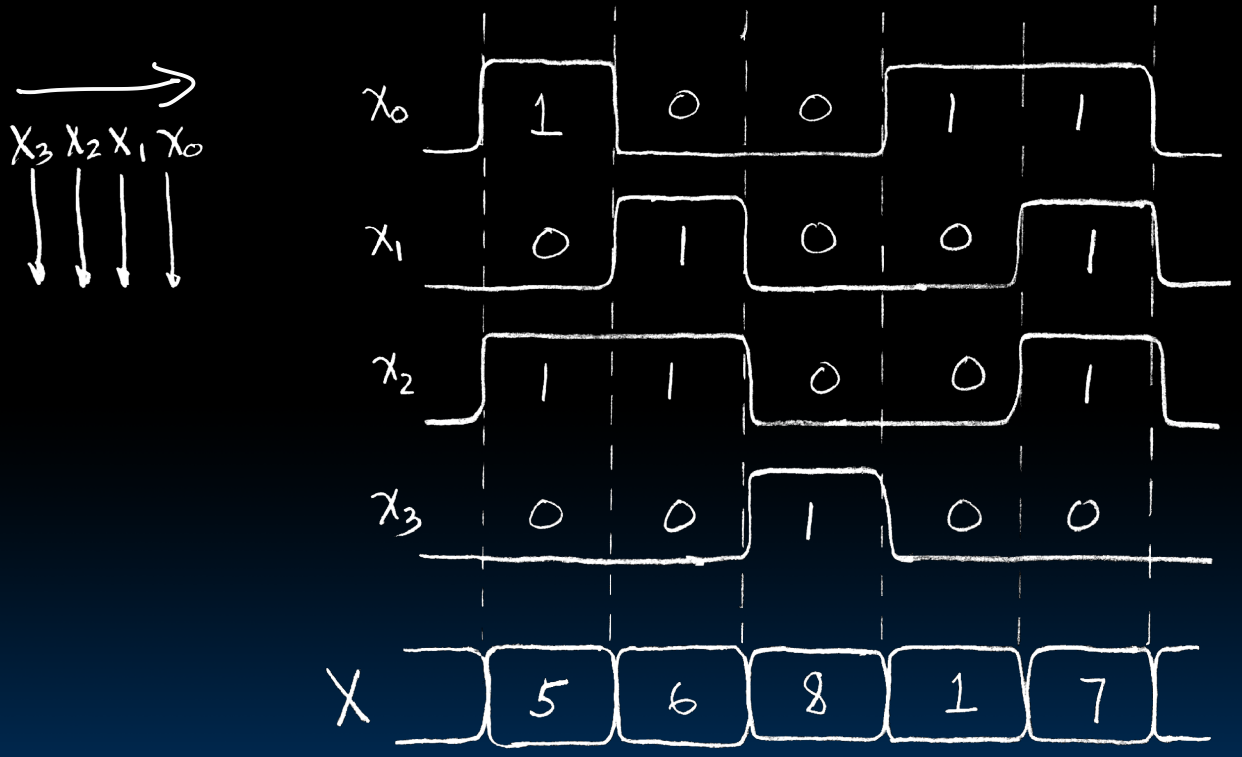


- Signals
  - When digital is only treated as 1 or 0
  - Is transmitted over wires continuously
  - Transmission is effectively instant
  - Implies that a wire contains 1 value at a time

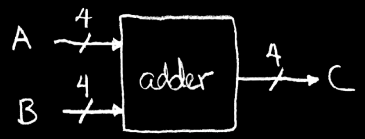
Signals and Waveforms



Signals and Waveforms: Grouping

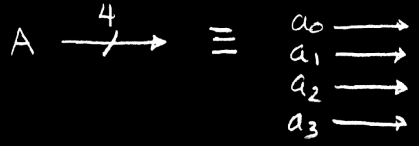


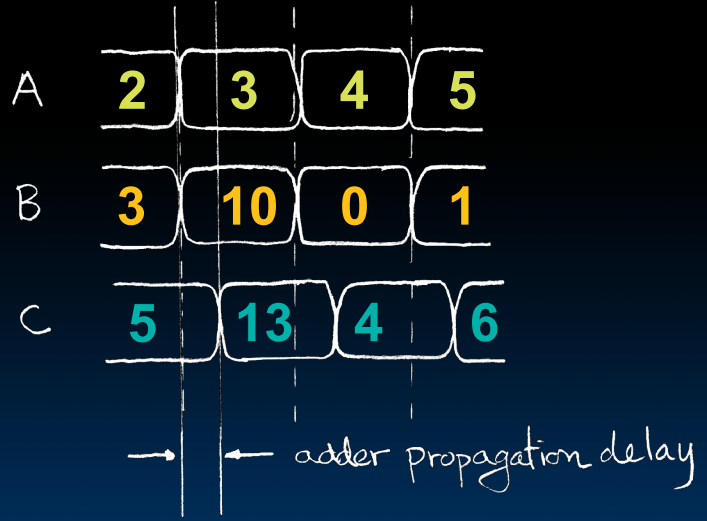
Signals and Waveforms: Circuit Delay



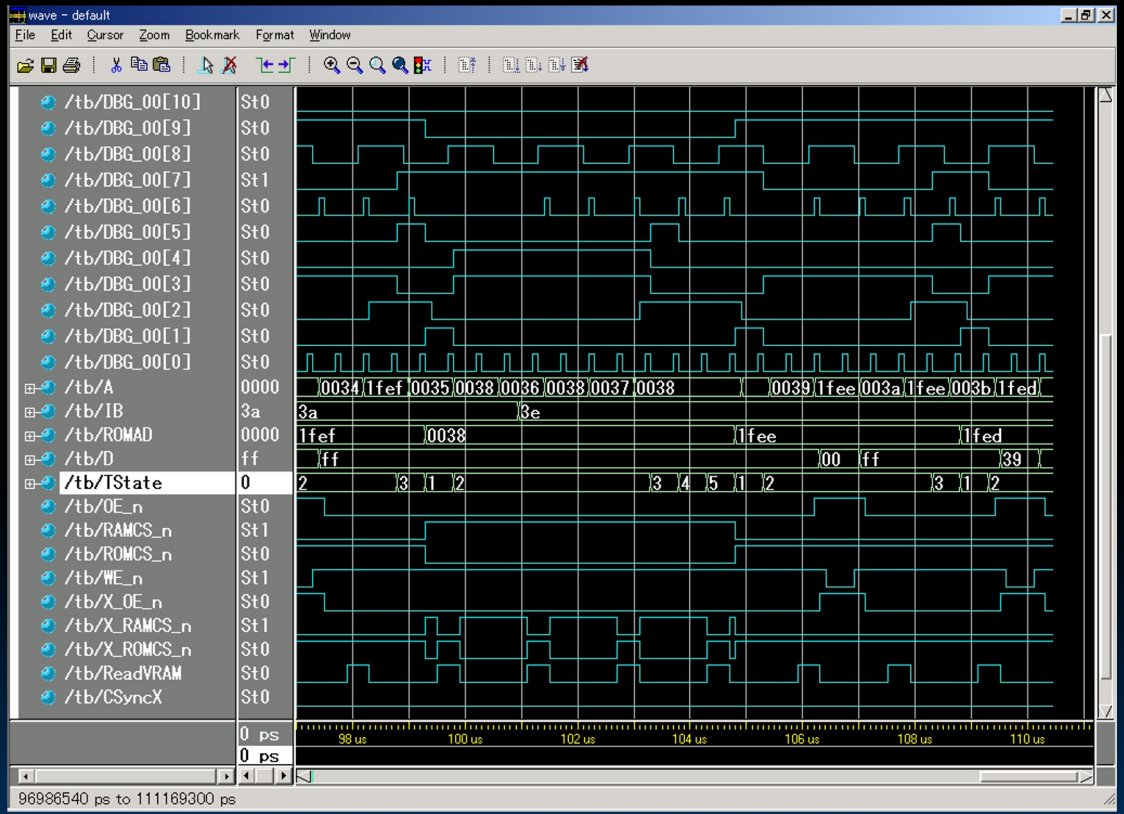
$A = [a_3, a_2, a_1, a_0]$

$B = [b_3, b_2, b_1, b_0]$





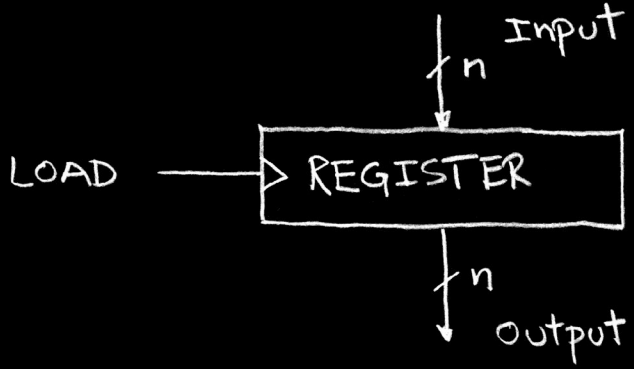
Sample Debugging Waveform



Type of Circuits

- Synchronous Digital Systems are made up of two basic types of circuits:
- Combinational Logic (CL) circuits 组合逻辑电路
  - Our previous adder circuit is an example.
  - Output is a function of the inputs only.
  - Similar to a pure function in mathematics, y = f(x). (No way to store information from one invocation to the next, no side effects)
- State Elements 状态元素
  - circuits that store information.

Circuits with STATE (e.g., register)



L14 SW can peek at HW (past ISA abstraction boundary) for optimizations | SW can depend on particular HW implementation of ISA | Timing diagrams serve as a critical debugging tool in the EE toolkit

FFF
FFT
FTF
FTT
TFF
TFT
TTF
TTT

And in conclusion…

- Clocks control pulse of our circuits
- Voltages are analog, quantized to 0/1
- Circuit delays are fact of life
- Two types of circuits:
  - Stateless Combinational Logic (&, |, ~)
  - State circuits (e.g., registers) 状态电路.