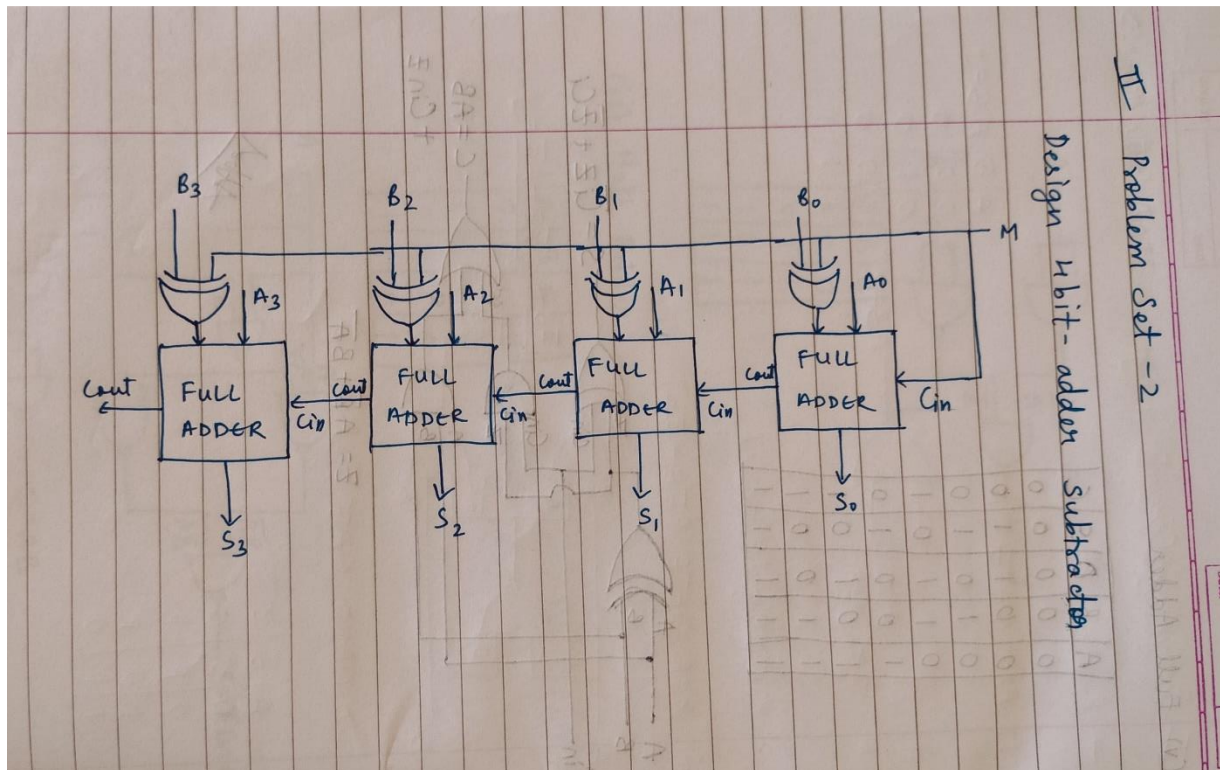


Lab 2 Report

4 Bit Adder-Subtractor



Simulations

