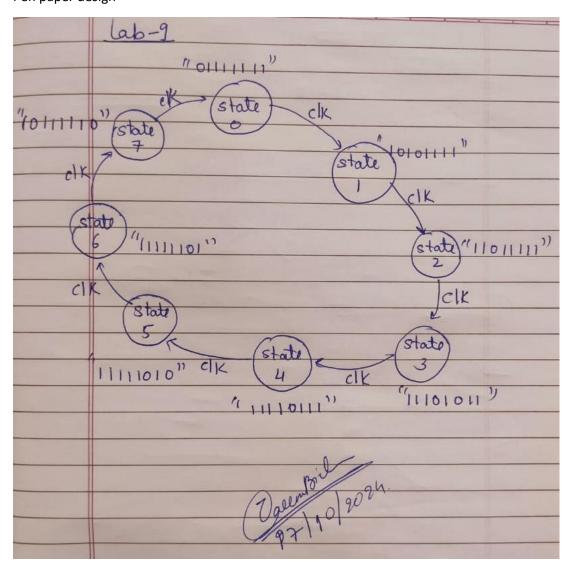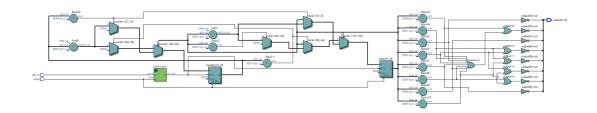Lab 10

Pen paper design

Rtl viewer

Xen10 board

Xen10

ALTERA
MAX 10
10M25SAE144C8G
E 8ABAZ1925A

USB2.0

POWER
ON
PWR_SEL
EXT_USB
FT232

Designed By:
Vidur Shah,
Mahesh Bhagnagre,
Maheshwar Mandar,
Amit Shevye

Supported By:
Wadhwani Electronics Lab, IIT Bombay
NPTEL, Intel

Dip
ON
1 2 3 4

RN1
RN2
RN3
LED8
VREF2
25K
8K
IC6
IC8
IC10

16x2 LCD
DLED
JP2
JP5
SCL
SDA
VCC
GND

VCC_ONE
VCC_ALL_PLED
+3u3_PLED
FT232_PLED
VCCIO_1A
VCCIO_1B
VCCIO_CLK

GND 123542

R3 470R
R5 0
R6 0
R4 470R
R19 470R

C54
C55
C30
C31
C42
C43
C44

EXT_PLED
R1 470R

ADC_XTAL
ADC_VREF
LCD_CONTRAST
REF_GND

JTAG_14, JP4.0-ADV7
 JP6
EXT
POT

93C46

5V VU V U
EXT PWR
ON