

Daniel Walter  
fordprkt@googlemail.com

Sheet: /Spartan3A/  
File: Spartan3A.sch

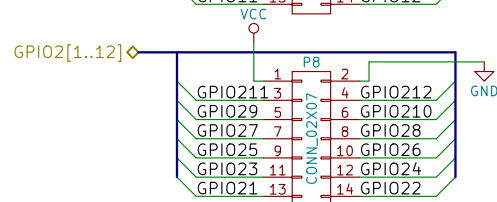
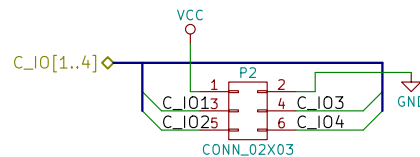
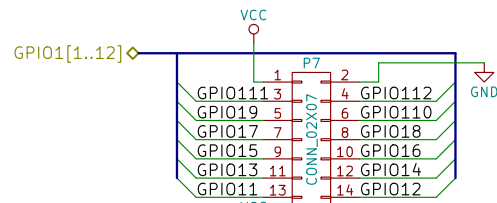
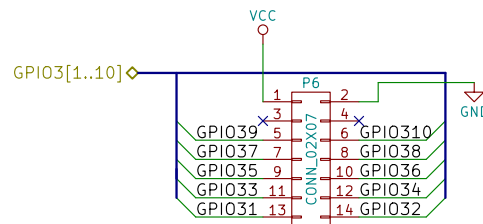
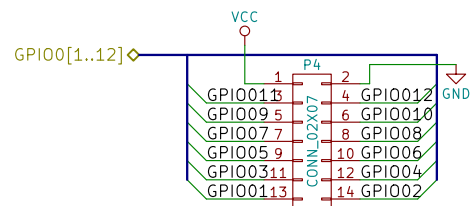
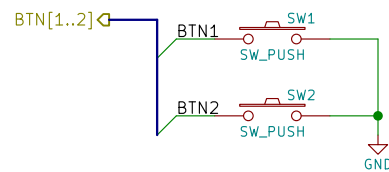
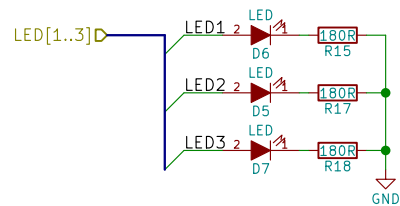
**Title: Xilinx Spartan 3A Eval Board**

Size: A3 Date: 2016-07-15

Rev: 0.1

KiCad E.D.A. kicad 4.0.3-stable

Id: 2/5



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File: Connections.sch

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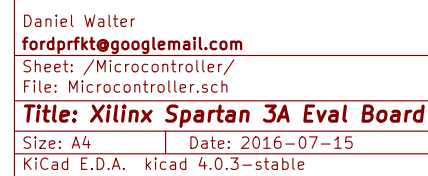
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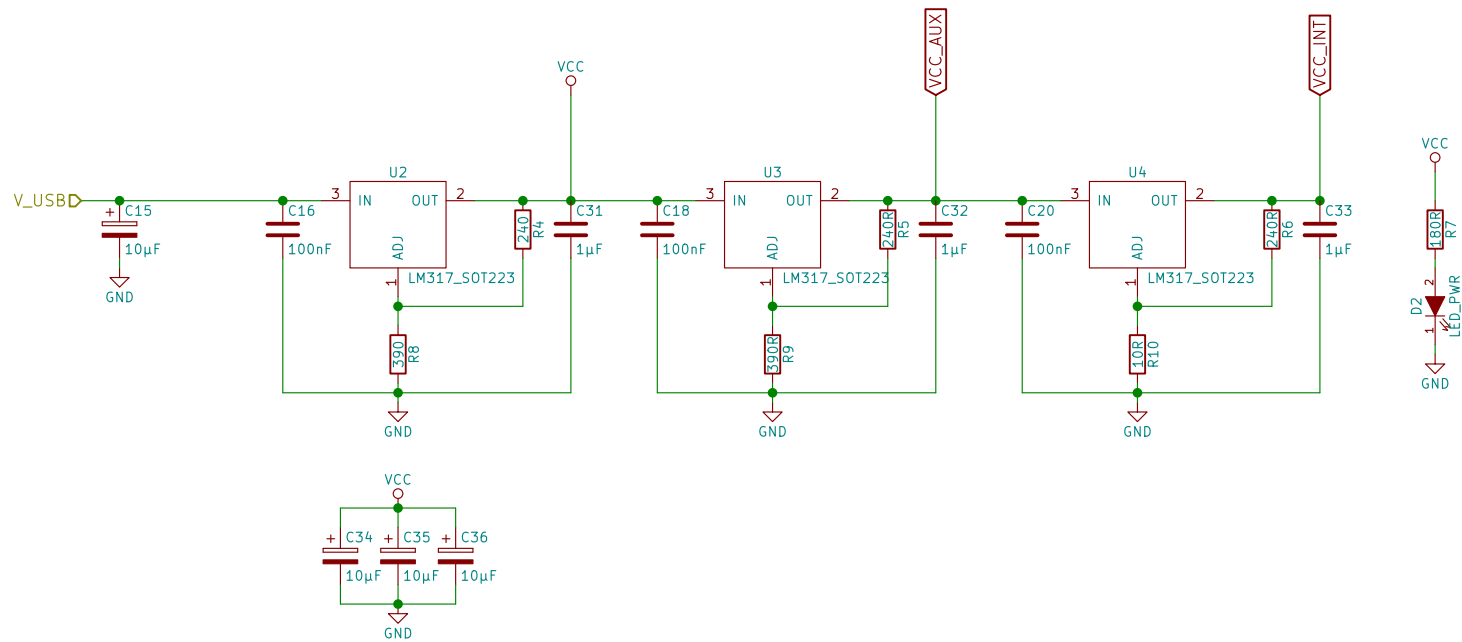
Date: 2016-07-15

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Rev: 0.1

Id: 3/5





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Sheet: /Supply/

File: Supply.sch

**Title: Xilinx Spartan 3A Eval Board**

Size: A4 Date: 2016-07-15

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Rev: 0.1

Id: 5/5