

LAB 4 – MOTOR CONTROLLER & ENVIRONMENTAL MONITOR

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INTRODUCTION

Congratulations! If you're reading this, it means you've reached the final laboratory exercise in CEG3136.

In Lab 4, you'll develop code to control the speed and direction of the **onboard DC motor** using the slide potentiometer and numeric touchpad. You'll also use the onboard **environmental sensor** to monitor temperature and humidity and display their values. You'll have the opportunity use the **real-time clock** in a bonus activity.

OBJECTIVES

After successfully completing Lab 4, you'll be able to:

1. Use an analog-to-digital converter (ADC) to monitor the level of an external voltage.
2. Generate a PWM output signal to control a DC motor, using a timer in compare mode.
3. Monitor the speed of a motor via a rotary encoder, using a timer in capture mode.
4. Build a closed-loop digital control system using a proportional-integral (PI) controller and tune the system to achieve stability and minimize steady state error.
5. Communicate with an external device using the SPI protocol.

Groups who choose to complete the bonus activity, will be able to:

6. Develop a new device driver for a MCU peripheral, using a similar driver as a model.
7. Configure a real-time clock (RTC) and use it to set and track date and time.

ACADEMIC INTEGRITY

1. Both group members must be involved from start to finish. A student who did not actively participate in the lab work cannot claim credit towards their course grade. Absence for medical or other valid reasons shall be communicated with the course instructor.
2. All lab deliverables must be your own original work. Copying between groups and the use of generative AI are prohibited. This means for example, that you're **not allowed to use Chat GPT** to write any code for you or to write any part of your lab report.

PROJECT SETUP

In the STM32CubeIDE, follow these steps to set up a new project for Lab 4, using your Lab 3 project as a starting point. Detailed instructions and screenshots for steps 1, 3, and 4 can be found in your Lab 0 manual.

1. Create a new STM32 project for the STM32L552ZET6Q MCU. Name your project CEG3136_Lab4 and create a New Folder in a safe location (e.g. H: or OneDrive). Select the ‘Empty’ project type.
2. Copy your `Src`, `Inc`, and `Drivers` folders from your Lab 3 project to your Lab 4 project. You can do this within the IDE by dragging and dropping while holding the Ctrl key. If you copy the folders outside of the IDE, you’ll need to Refresh your project to make them show up.
3. In your project Properties, find your compiler build settings, add the following preprocessor define:
`STM32L552xx`
4. Add the following include paths. Keep the default path `../Inc` in the list.
`../Drivers/CMSIS/Include`
`../Drivers/CMSIS/Device/ST/STM32L5xx/Include`

PROTIP: REOPENING A PROJECT

If you’re working on a lab PC, your workspace will be empty each time you sign in (even if you create it on your own drive). Follow these steps to import an existing project into a workspace:

1. Under the File menu, select Import.
2. Expand the General category and select Existing Projects into Workspace. Click Next.
3. Beside the box for Select Root Directory, click Browse.
4. Navigate to and select or enter the existing project folder, then click Select Folder.
5. Back in the Import dialog box, click Finish.

TROUBLESHOOTING TIPS

Unable to find program for Run or Debug:

1. Right click your project, select Clean Project, then Build Project.
2. Resolve any compile or link errors.

GDB server error or unknown MCU type:

1. Power off your board by pressing the red glowing button.
2. Unplug the USB cable from the host side (i.e. lab PC or laptop).
3. Wait 5 seconds.
4. Restore power and reattach the USB cable.

Undefined reference to main; folder and file icons look different:

1. Rename folders `Inc` and `Src` to `IncX` and `SrcX` respectively.
2. Create new folders `Inc` and `Src`.
3. Drag and drop the header/source files from the old folders to the corresponding new folders.
4. Delete the old folders.

Nonsensical compile errors; code looks totally fine:

Review the code from the top of the file to the first error, checking for an imbalance of curly brackets or round brackets that should be curly brackets.

Your program is not running:

- Suspend and Resume the program a few times without breakpoints:
 - `WaitForSysTick()` is normal. Your program does this every 1ms after executing its tasks.
 - Being stuck in the `InfiniteLoop` means an unhandled exception occurred. Comment out code to avoid enabling interrupts (or registering callbacks). If it still crashes with all interrupts disabled, comment out more code or use breakpoints to isolate the problem.
- Use the SFRs tab to check your peripheral hardware registers:
 - You can save specific registers or fields as favourites.
 - Watch them while you step through your code or click RD to read inputs while suspended.
 - Manually edit the values of fields you suspect are incorrect to see if it resolves the issue.

PERFORMANCE BOOST

In this section, we'll configure a *phase-locked loop* (PLL) to increase the system clock rate, greatly improving CPU performance. We'll also enable optimization in our compiler settings to generate smaller, more efficient assembly code.

SYSTEM CLOCK CONFIGURATION

For the labs so far, we've been using the default System Clock (SYSCLK) frequency of 4.0MHz. We will now increase the frequency to 48.0MHz, using the MCU on-chip PLL as a clock multiplier.

SYSCLK.H

Under the `Inc` folder, add a new header file named `sysclk.h`. Replace its entire contents by copying and pasting the text shown (upper right).

SYSCLK.C

Under the `Src` folder, add a new source file named `sysclk.c`. Replace its entire contents by copying and pasting the mini-text shown (lower right).

```
sysclk.h
#ifndef SYSCLK_H_
#define SYSCLK_H_

#include "stm32l5xx.h"

// System Clock frequency in Hz
#define SYSCLK_FREQ 48e6

void ConfigureSystemClock(void);

#endif /* SYSCLK_H_ */
```

```
sysclk.c
// System Clock (SYSCLK) configuration
#include <stdbool.h>
#include "sysclk.h"

static bool done = 0;

void ConfigureSystemClock (void) {
    if (done)
        return; // SYSCLK already configured

    // Multi-speed oscillator (MSI)
    RCC->CR |= RCC_CR_MSION | RCC_CR_MSIRDY | RCC_CR_MSIRGSEL;
    RCC->CR = (RCC->CR & ~RCC_CR_MSIRANGE_Msk) | 0x6 << RCC_CR_MSIRANGE_Pos;

    // Phase-locked loop (PLL)
    RCC->CR |= ~RCC_CR_PLLON; // Disable PLL
    RCC->PLLCFGR = 0b01 << RCC_PLLCFGR_PLLN_Pos; // 2MHz x 24 = 48MHz
    RCC->CR |= RCC_CR_PLLON; // Enable PLL
    RCC->PLLCFGR |= RCC_PLLCFGR_PLLREN; // Enable PLL clock output

    // Add flash read wait states to account for faster SYSCLK
    FLASH->ACR |= 0x2 << FLASH_ACR_LATENCY_Pos;

    // Select PLL as System Clock source
    RCC->CFGR |= 0x3 << RCC_CFGR_SW_Pos;

    done = true;
}
```

DRIVER UPDATES

Two drivers need to be updated to account for the System Clock increase from 4.0MHz to 48.0MHz.

SYSTICK.C

Open your existing `systick.c` file and make the following modifications:

IMPORTANT: You are not permitted to use machine assistance for this task.

1. Add include file `sysclk.h`.
2. Update the symbol definition SYSTICK and replace value 4000 with a new formula to calculate the SysTick period based on the System Clock frequency. Hint: Refer to `sysclk.h` and Exploration Question 2 of your Lab 1 Report to help you create the formula. Note that SYSTICK is the actual timer period, while the `SysTick_LOAD` register value is one less (`SYSTICK - 1`).
3. Locate the function `StartSysTick()`. Insert a call to `ConfigureSystemClock()` as the first statement.

I2C.C

Open your existing `i2c.c` file and locate the statement near the end of `I2C_Enable()` that configures the I²C Timing Register (`TIMINGR`). Update the code to look like the following:

```
bus iface->TIMINGR = 0x????????; // 100kHz from 48MHz SYSCLK
```

Follow the process below to generate the correct 32-bit hexadecimal value to write to the register.

IMPORTANT: You are not permitted to use machine assistance for this task.

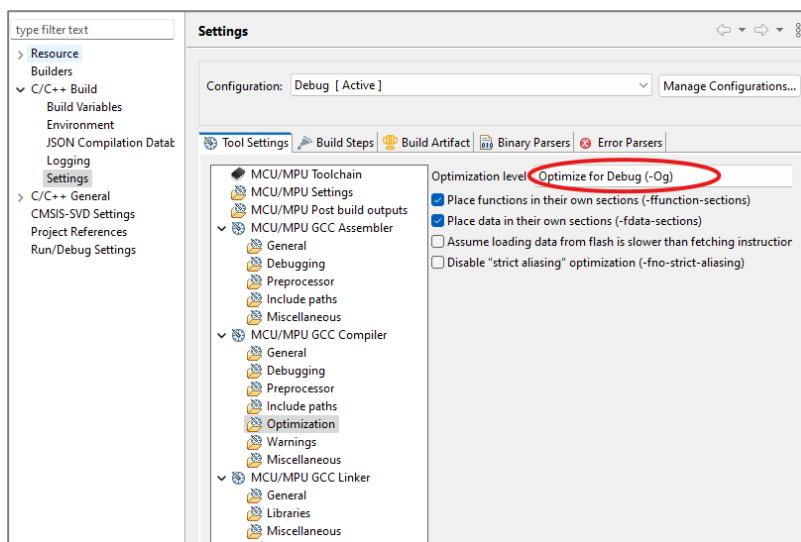
1. Read sections 43.4.10 and 43.9.5 of your [MCU Reference Manual](#).
2. Using that information, determine the value of each field in `I2C_TIMINGR`.
3. Write out the complete register value, grouped in nibbles (4 bits or 1 hexadecimal digit).
Note that the nibbles may not be listed in the same order as the reference table!

COMPILER OPTIMIZATION

Follow these steps to enable the C compiler to generate optimized source code:

1. In the Project Explorer, right click your project and open Properties.
2. Navigate to C/C++ Build, Settings, GCC Compiler, Optimization.
3. Set Optimization Level to Optimize for Debug (-Og).
4. Click Apply and Close. If prompted, allow it to Rebuild the Index.

For your lab report, screenshot disassembly of a C function of your choice with and without optimization.



TESTING THE CHANGES

Build your program and correct any additional errors.

Run your program and test all your existing apps to ensure that everything is working as expected. Because the SysTick still runs at 1ms, the LED timing (should remain unchanged (e.g. in the Alarm System app, the blue/green LEDs should toggle at a period of 1 second each in ARMED state)).

Update your Debug Configuration for the new System Clock frequency of 48.0MHz. Confirm that `printf()` is working in Debug mode.

ENVIRONMENTAL MONITOR

In this section, we'll use Serial Peripheral Interface (SPI) to obtain the temperature and humidity from the on-board Bosch BME680 environment sensor module.

SPI DRIVER

We'll first build a device driver for the MCU SPI controller. Like the I²C driver added in Lab 2, our SPI driver uses an object-oriented design, a linked-list request queue, and cooperative multitasking.

SPI.H

Under the `Inc` folder, add a new header file named `spi.h`. Replace its entire contents by copying and pasting the mini-text shown (below).

Compare `spi.h` to `i2c.h` and note the similarities and differences.

SPI.C

Under the `Src` folder, add a new source file named `spi.c`. Replace its entire contents by copying and pasting the mini-text shown (right).

Complete the highlighted parts of the code, referring to your [Lab User's Guide](#), [MCU Reference Manual](#) (9.8.19–21), [MCU Datasheet](#) (Table 22), and `i2c.c`. Note that the **NSS pin is a regular GPIO output**, not an alternate function with the SPI controller.

```
#ifndef SPI_H_
#define SPI_H_

#include <stdbool.h>
#include "stm32l5xx.h"
#include "gpio.h"

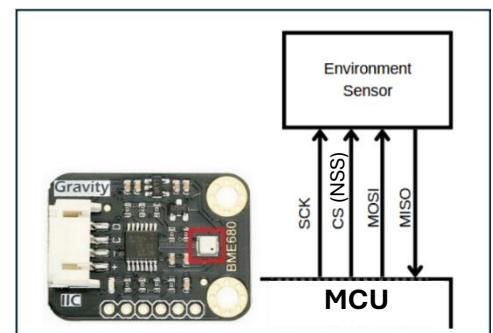
// SPI bus connection
typedef struct {
    SPI_TypeDef *iface; // Interface for SPI1-SPI3
    Pin_t pinSCLK; // MCU pin for SCLK/SCK
    Pin_t pinMISO; // MCU pin for MISO/SO
    Pin_t pinMOSI; // MCU pin for MOSI/SI
    Pin_t pinNSS; // MCU pin for NSS/CSB
} SPI_Bus_t;

extern SPI_Bus_t EnvSPI; // SPI bus for Environmental Sensor

typedef enum {RX=1, TX=0} Direction_t;

// SPI transfer record
typedef struct SPI_Xfer_t {
    SPI_Bus_t *bus; // Pointer to SPI bus structure
    Direction_t dir; // Transfer direction
    uint8_t *data; // Pointer to data buffer
    int size; // Total number of bytes in transfer
    bool last; // Last transfer in combined sequence
    volatile bool busy; // Busy indicator (queued or in progress)
    struct SPI_Xfer_t *next; // Pointer to next transfer in queue
} SPI_Xfer_t;

void SPI_Enable(SPI_Bus_t bus); // Enable SPI bus connection
void SPI_Request(SPI_Xfer_t *p); // Request a new transfer
void ServiceSPIRequests(void); // Called from main loop
#endif /* SPI_H_ */
```



spi.c

```
// SPI controller driver
#include <stddef.h>
#include <stdio.h>
#include "spi.h"
#include "gpio.h"
#include "sysTick.h"

// SPI bus for the Environmental Sensor
SPI_Bus_t EnvSPI = {<highlighted>0</highlighted>}; // SPI controller 1
Pin_t pinSCLK = <highlighted>GPIO7, 77</highlighted>; // SCLK pin
Pin_t pinMISO = <highlighted>GPIO5, 77</highlighted>; // MISO pin
Pin_t pinMOSI = <highlighted>GPIO5, 78</highlighted>; // MOSI pin
Pin_t pinNSS = <highlighted>GPIO5, 79</highlighted>; // NSS/CSB pin

// Pointers to head and tail of the transfer queue
static SPI_Xfer_t *head = NULL;
static SPI_Xfer_t *tail = NULL;

static int ncs = -1; // Number of bytes transferred, -1 when idle

// Enable SPI controller and configure associated GPIO pins
void SPI_Enable(SPI_Bus_t bus) {
    if (<highlighted>(bus.iface == SPI1 || bus.iface == SPI2 || bus.iface == SPI3)</highlighted>)
        return; // Already enabled

    // Enable clock to selected SPI controller
    // See Reference Manual, Table 22
    RCC->APR1 |= bus.iface == SPI1 ? RCC_APRI1_SPI1EN : 0;
    RCC->APR1 |= bus.iface == SPI2 ? RCC_APRI1_SPI2EN : 0;
    RCC->APR1 |= bus.iface == SPI3 ? RCC_APRI1_SPI3EN : 0;

    // Enable clocks to GPIO ports containing SPI pins
    // See Datasheet, Table 22
    RCC->AHB1ENR |= RCC_AHB1ENR_GPIO5EN;

    // Select alternate function as SPI (SCLK, MISO, MOSI only)
    // See MCU Datasheet, Table 22
    // ... (code for setting alternate functions)

    // Configure SPI peripheral
    bus.iface->CRH |= SPI_CR1_SPE;
    bus.iface->CRH |= SPI_CR1_MSTR | (5<<3) | SPI_CR1_SSM | SPI_CR1_SSI;
    bus.iface->CRH |= SPI_CR1_DPS | (8 - 1) << SPI_CR1_DPS_Pos;
    bus.iface->CR1 |= SPI_CR1_BSE;
}

// Add a transfer request to the queue
void SPI_Request(SPI_Xfer_t *p) {
    if (head == NULL) // Add to empty queue
        head = p;
    else
        tail->next = p; // Add to tail of non-empty queue
    tail = p;
    p->next = NULL;
    p->busy = true; // Mark transfer as in-progress
}

// Polling implementation, called from main loop every tick
void ServiceSPIRequests(void) {
    if (head == NULL)
        return; // Nothing to do right now

    SPI_Xfer_t *p = head;
    SPI_TypeDef *SPI = p->bus->iface;
    volatile uint8_t *DR = (volatile uint8_t *) &SPI->DR; // Workaround

    if (n == -1) {
        // Begin a new transfer
        n = 0;
        GPIO_Output(p->bus->pinNSS, LOW); // Assert select
        if (p->dir == RX) {
            *DR = 0; // Dummy transmit
        }
    } else if (n < p->size) {
        if (n > 0)
            *DR = 0; // Dummy receive
        // Copy transmit data from memory buffer to hardware buffer
        *p->data[n] = *DR;
    } else if (p->dir == RX && SPI->SR & SPI_SR_RXNE) {
        if (p->data[n] == *DR)
            *DR = 0; // Dummy transmit
    } else {
        // Remove transfer from head of queue
        head = p->next;
        p->next = NULL;
        p->busy = 0; // Prepare for next transfer
    }
    if (p->dir == RX)
        // Drain the receive data buffer
        while (SPI->SR & SPI_SR_RXNE)
            *DR = 0; // Dummy receive
    if (p->last)
        GPIO_Output(p->bus->pinNSS, HIGH); // De-assert select
}
```

ENIRO APP

In this section, you'll build an app that uses our new SPI driver to read raw data from the environmental sensor module, combines it to determine the ambient temperature and humidity—a process known as *sensor fusion*—and print the information on the alphanumeric display.

Throughout this section, you'll need to refer to the [Environmental Sensor Datasheet](#). This document was explored in detail during Tutorial 9 in preparation for this lab.

ENIRO.H

```
#enviro.h X
1 #ifndef ENIRO_H_
2 #define ENIRO_H_
3
4 void Init_Enviro(void);
5 void Task_Enviro(void);
6
7 #endif /* ENIRO_H_ */
```

Under the `Inc` folder, add a new header file named `enviro.h`. Replace its entire contents by manually typing in the text shown.

DISPLAY.H

Open your existing `display.h` file. Add `ENIRO` to the display page enumeration.

ENIRO.C

Under the `Src` folder, add a new source file named `enviro.c`. Replace its entire contents by copying and pasting the skeleton code in the mini-text shown (right).

Complete the highlighted sections of code by referring to the datasheet.

Hint: For burst reads of multiple bytes, always specify the lowest address, whether than be LSB or MSB. **Do not use generative AI.**

MAIN.C

Open your existing `main.c` file. Add include directives for `spi.h` and `enviro.h`. In the Housekeeping section, add a call to `ServiceSPIRequests()` immediately after the I²C equivalent. Also add calls to `Init_Enviro()` and `Task_Enviro()` in the appropriate places.

enviro.c Updated

```
environmental_header
1 #include <spi.h>
2 #include <math.h>
3 #include <mathlib.h>
4 #include "enviro.h"
5 #include "display.h"
6 #include "task.h"
7
8 static enum {SHTP_IDLE, SHTP_PRAMO, MTF_PRAMO, MTF_MEAN, MTF_STDEV, MTF_READY} state;
9
10 static const uint8_t enviro_i2c[1] = {0x40}; // I2C slave address
11
12 static const uint8_t enviro_raddr[1] = {0x00}; // Register address
13
14 static const uint8_t enviro_rdata[1]; // Read buffer
15
16 static const uint8_t enviro_wdata[1]; // Write buffer
17
18 static const uint8_t enviro_raddr2[1] = {0x01}; // Register address
19 static const uint8_t enviro_rdata2[1];
20
21 static const uint8_t enviro_waddr[1] = {0x00}; // Write address
22 static const uint8_t enviro_wdata2[1];
23
24 static const uint8_t enviro_raddr3[1] = {0x02}; // Register address
25 static const uint8_t enviro_rdata3[1];
26
27 static const uint8_t enviro_waddr2[1] = {0x01}; // Write address
28 static const uint8_t enviro_wdata3[1];
29
30 static const uint8_t enviro_raddr4[1] = {0x03}; // Register address
31 static const uint8_t enviro_rdata4[1];
32
33 static const uint8_t enviro_waddr3[1] = {0x02}; // Write address
34 static const uint8_t enviro_wdata4[1];
35
36 static const uint8_t enviro_raddr5[1] = {0x04}; // Register address
37 static const uint8_t enviro_rdata5[1];
38
39 static const uint8_t enviro_waddr4[1] = {0x03}; // Write address
40 static const uint8_t enviro_wdata5[1];
41
42 static const uint8_t enviro_raddr6[1] = {0x05}; // Register address
43 static const uint8_t enviro_rdata6[1];
44
45 static const uint8_t enviro_waddr5[1] = {0x04}; // Write address
46 static const uint8_t enviro_wdata6[1];
47
48 static const uint8_t enviro_raddr7[1] = {0x06}; // Register address
49 static const uint8_t enviro_rdata7[1];
50
51 static const uint8_t enviro_waddr6[1] = {0x05}; // Write address
52 static const uint8_t enviro_wdata7[1];
53
54 static const uint8_t enviro_raddr8[1] = {0x07}; // Register address
55 static const uint8_t enviro_rdata8[1];
56
57 static const uint8_t enviro_waddr7[1] = {0x06}; // Write address
58 static const uint8_t enviro_wdata8[1];
59
60 static const uint8_t enviro_raddr9[1] = {0x08}; // Register address
61 static const uint8_t enviro_rdata9[1];
62
63 static const uint8_t enviro_waddr8[1] = {0x07}; // Write address
64 static const uint8_t enviro_wdata9[1];
65
66 static const uint8_t enviro_raddr10[1] = {0x09}; // Register address
67 static const uint8_t enviro_rdata10[1];
68
69 static const uint8_t enviro_waddr9[1] = {0x08}; // Write address
70 static const uint8_t enviro_wdata10[1];
71
72 static const uint8_t enviro_raddr11[1] = {0x0A}; // Register address
73 static const uint8_t enviro_rdata11[1];
74
75 static const uint8_t enviro_waddr10[1] = {0x09}; // Write address
76 static const uint8_t enviro_wdata11[1];
77
78 static const uint8_t enviro_raddr12[1] = {0x0B}; // Register address
79 static const uint8_t enviro_rdata12[1];
80
81 static const uint8_t enviro_waddr11[1] = {0x0A}; // Write address
82 static const uint8_t enviro_wdata12[1];
83
84 static const uint8_t enviro_raddr13[1] = {0x0C}; // Register address
85 static const uint8_t enviro_rdata13[1];
86
87 static const uint8_t enviro_waddr12[1] = {0x0B}; // Write address
88 static const uint8_t enviro_wdata13[1];
89
90 static const uint8_t enviro_raddr14[1] = {0x0D}; // Register address
91 static const uint8_t enviro_rdata14[1];
92
93 static const uint8_t enviro_waddr13[1] = {0x0C}; // Write address
94 static const uint8_t enviro_wdata14[1];
95
96 static const uint8_t enviro_raddr15[1] = {0x0E}; // Register address
97 static const uint8_t enviro_rdata15[1];
98
99 static const uint8_t enviro_waddr14[1] = {0x0D}; // Write address
100 static const uint8_t enviro_wdata15[1];
101
102 static const uint8_t enviro_raddr16[1] = {0x0F}; // Register address
103 static const uint8_t enviro_rdata16[1];
104
105 static const uint8_t enviro_waddr15[1] = {0x0E}; // Write address
106 static const uint8_t enviro_wdata16[1];
107
108 static const uint8_t enviro_raddr17[1] = {0x10}; // Register address
109 static const uint8_t enviro_rdata17[1];
110
111 static const uint8_t enviro_waddr16[1] = {0x0F}; // Write address
112 static const uint8_t enviro_wdata17[1];
113
114 static const uint8_t enviro_raddr18[1] = {0x11}; // Register address
115 static const uint8_t enviro_rdata18[1];
116
117 static const uint8_t enviro_waddr17[1] = {0x10}; // Write address
118 static const uint8_t enviro_wdata18[1];
119
120 static const uint8_t enviro_raddr19[1] = {0x12}; // Register address
121 static const uint8_t enviro_rdata19[1];
122
123 static const uint8_t enviro_waddr18[1] = {0x11}; // Write address
124 static const uint8_t enviro_wdata19[1];
125
126 static const uint8_t enviro_raddr20[1] = {0x13}; // Register address
127 static const uint8_t enviro_rdata20[1];
128
129 static const uint8_t enviro_waddr19[1] = {0x12}; // Write address
130 static const uint8_t enviro_wdata20[1];
131
132 static const uint8_t enviro_raddr21[1] = {0x14}; // Register address
133 static const uint8_t enviro_rdata21[1];
134
135 static const uint8_t enviro_waddr20[1] = {0x13}; // Write address
136 static const uint8_t enviro_wdata21[1];
137
138 static const uint8_t enviro_raddr22[1] = {0x15}; // Register address
139 static const uint8_t enviro_rdata22[1];
140
141 static const uint8_t enviro_waddr21[1] = {0x14}; // Write address
142 static const uint8_t enviro_wdata22[1];
143
144 static const uint8_t enviro_raddr23[1] = {0x16}; // Register address
145 static const uint8_t enviro_rdata23[1];
146
147 static const uint8_t enviro_waddr22[1] = {0x15}; // Write address
148 static const uint8_t enviro_wdata23[1];
149
150 static const uint8_t enviro_raddr24[1] = {0x17}; // Register address
151 static const uint8_t enviro_rdata24[1];
152
153 static const uint8_t enviro_waddr23[1] = {0x16}; // Write address
154 static const uint8_t enviro_wdata24[1];
155
156 static const uint8_t enviro_raddr25[1] = {0x18}; // Register address
157 static const uint8_t enviro_rdata25[1];
158
159 static const uint8_t enviro_waddr24[1] = {0x17}; // Write address
160 static const uint8_t enviro_wdata25[1];
161
162 static const uint8_t enviro_raddr26[1] = {0x19}; // Register address
163 static const uint8_t enviro_rdata26[1];
164
165 static const uint8_t enviro_waddr25[1] = {0x18}; // Write address
166 static const uint8_t enviro_wdata26[1];
167
168 static const uint8_t enviro_raddr27[1] = {0x1A}; // Register address
169 static const uint8_t enviro_rdata27[1];
170
171 static const uint8_t enviro_waddr26[1] = {0x19}; // Write address
172 static const uint8_t enviro_wdata27[1];
173
174 static const uint8_t enviro_raddr28[1] = {0x1B}; // Register address
175 static const uint8_t enviro_rdata28[1];
176
177 static const uint8_t enviro_waddr27[1] = {0x1A}; // Write address
178 static const uint8_t enviro_wdata28[1];
179
180 static const uint8_t enviro_raddr29[1] = {0x1C}; // Register address
181 static const uint8_t enviro_rdata29[1];
182
183 static const uint8_t enviro_waddr28[1] = {0x1B}; // Write address
184 static const uint8_t enviro_wdata29[1];
185
186 static const uint8_t enviro_raddr30[1] = {0x1D}; // Register address
187 static const uint8_t enviro_rdata30[1];
188
189 static const uint8_t enviro_waddr29[1] = {0x1C}; // Write address
190 static const uint8_t enviro_wdata30[1];
191
192 static const uint8_t enviro_raddr31[1] = {0x1E}; // Register address
193 static const uint8_t enviro_rdata31[1];
194
195 static const uint8_t enviro_waddr30[1] = {0x1D}; // Write address
196 static const uint8_t enviro_wdata31[1];
197
198 static const uint8_t enviro_raddr32[1] = {0x1F}; // Register address
199 static const uint8_t enviro_rdata32[1];
200
201 static const uint8_t enviro_waddr31[1] = {0x1E}; // Write address
202 static const uint8_t enviro_wdata32[1];
203
204 static const uint8_t enviro_raddr33[1] = {0x20}; // Register address
205 static const uint8_t enviro_rdata33[1];
206
207 static const uint8_t enviro_waddr32[1] = {0x1F}; // Write address
208 static const uint8_t enviro_wdata33[1];
209
210 static const uint8_t enviro_raddr34[1] = {0x21}; // Register address
211 static const uint8_t enviro_rdata34[1];
212
213 static const uint8_t enviro_waddr33[1] = {0x20}; // Write address
214 static const uint8_t enviro_wdata34[1];
215
216 static const uint8_t enviro_raddr35[1] = {0x22}; // Register address
217 static const uint8_t enviro_rdata35[1];
218
219 static const uint8_t enviro_waddr34[1] = {0x21}; // Write address
220 static const uint8_t enviro_wdata35[1];
221
222 static const uint8_t enviro_raddr36[1] = {0x23}; // Register address
223 static const uint8_t enviro_rdata36[1];
224
225 static const uint8_t enviro_waddr35[1] = {0x22}; // Write address
226 static const uint8_t enviro_wdata36[1];
227
228 static const uint8_t enviro_raddr37[1] = {0x24}; // Register address
229 static const uint8_t enviro_rdata37[1];
230
231 static const uint8_t enviro_waddr36[1] = {0x23}; // Write address
232 static const uint8_t enviro_wdata37[1];
233
234 static const uint8_t enviro_raddr38[1] = {0x25}; // Register address
235 static const uint8_t enviro_rdata38[1];
236
237 static const uint8_t enviro_waddr37[1] = {0x24}; // Write address
238 static const uint8_t enviro_wdata38[1];
239
240 static const uint8_t enviro_raddr39[1] = {0x26}; // Register address
241 static const uint8_t enviro_rdata39[1];
242
243 static const uint8_t enviro_waddr38[1] = {0x25}; // Write address
244 static const uint8_t enviro_wdata39[1];
245
246 static const uint8_t enviro_raddr40[1] = {0x27}; // Register address
247 static const uint8_t enviro_rdata40[1];
248
249 static const uint8_t enviro_waddr39[1] = {0x26}; // Write address
250 static const uint8_t enviro_wdata40[1];
251
252 static const uint8_t enviro_raddr41[1] = {0x28}; // Register address
253 static const uint8_t enviro_rdata41[1];
254
255 static const uint8_t enviro_waddr40[1] = {0x27}; // Write address
256 static const uint8_t enviro_wdata41[1];
257
258 static const uint8_t enviro_raddr42[1] = {0x29}; // Register address
259 static const uint8_t enviro_rdata42[1];
260
261 static const uint8_t enviro_waddr41[1] = {0x28}; // Write address
262 static const uint8_t enviro_wdata42[1];
263
264 static const uint8_t enviro_raddr43[1] = {0x2A}; // Register address
265 static const uint8_t enviro_rdata43[1];
266
267 static const uint8_t enviro_waddr42[1] = {0x29}; // Write address
268 static const uint8_t enviro_wdata43[1];
269
270 static const uint8_t enviro_raddr44[1] = {0x2B}; // Register address
271 static const uint8_t enviro_rdata44[1];
272
273 static const uint8_t enviro_waddr43[1] = {0x2A}; // Write address
274 static const uint8_t enviro_wdata44[1];
275
276 static const uint8_t enviro_raddr45[1] = {0x2C}; // Register address
277 static const uint8_t enviro_rdata45[1];
278
279 static const uint8_t enviro_waddr44[1] = {0x2B}; // Write address
280 static const uint8_t enviro_wdata45[1];
281
282 static const uint8_t enviro_raddr46[1] = {0x2D}; // Register address
283 static const uint8_t enviro_rdata46[1];
284
285 static const uint8_t enviro_waddr45[1] = {0x2C}; // Write address
286 static const uint8_t enviro_wdata46[1];
287
288 static const uint8_t enviro_raddr47[1] = {0x2E}; // Register address
289 static const uint8_t enviro_rdata47[1];
290
291 static const uint8_t enviro_waddr46[1] = {0x2D}; // Write address
292 static const uint8_t enviro_wdata47[1];
293
294 static const uint8_t enviro_raddr48[1] = {0x2F}; // Register address
295 static const uint8_t enviro_rdata48[1];
296
297 static const uint8_t enviro_waddr47[1] = {0x2E}; // Write address
298 static const uint8_t enviro_wdata48[1];
299
300 static const uint8_t enviro_raddr49[1] = {0x30}; // Register address
301 static const uint8_t enviro_rdata49[1];
302
303 static const uint8_t enviro_waddr48[1] = {0x2F}; // Write address
304 static const uint8_t enviro_wdata49[1];
305
306 static const uint8_t enviro_raddr50[1] = {0x31}; // Register address
307 static const uint8_t enviro_rdata50[1];
308
309 static const uint8_t enviro_waddr49[1] = {0x30}; // Write address
310 static const uint8_t enviro_wdata50[1];
311
312 static const uint8_t enviro_raddr51[1] = {0x32}; // Register address
313 static const uint8_t enviro_rdata51[1];
314
315 static const uint8_t enviro_waddr50[1] = {0x31}; // Write address
316 static const uint8_t enviro_wdata51[1];
317
318 static const uint8_t enviro_raddr52[1] = {0x33}; // Register address
319 static const uint8_t enviro_rdata52[1];
320
321 static const uint8_t enviro_waddr51[1] = {0x32}; // Write address
322 static const uint8_t enviro_wdata52[1];
323
324 static const uint8_t enviro_raddr53[1] = {0x34}; // Register address
325 static const uint8_t enviro_rdata53[1];
326
327 static const uint8_t enviro_waddr52[1] = {0x33}; // Write address
328 static const uint8_t enviro_wdata53[1];
329
330 static const uint8_t enviro_raddr54[1] = {0x35}; // Register address
331 static const uint8_t enviro_rdata54[1];
332
333 static const uint8_t enviro_waddr53[1] = {0x34}; // Write address
334 static const uint8_t enviro_wdata54[1];
335
336 static const uint8_t enviro_raddr55[1] = {0x36}; // Register address
337 static const uint8_t enviro_rdata55[1];
338
339 static const uint8_t enviro_waddr54[1] = {0x35}; // Write address
340 static const uint8_t enviro_wdata55[1];
341
342 static const uint8_t enviro_raddr56[1] = {0x37}; // Register address
343 static const uint8_t enviro_rdata56[1];
344
345 static const uint8_t enviro_waddr55[1] = {0x36}; // Write address
346 static const uint8_t enviro_wdata56[1];
347
348 static const uint8_t enviro_raddr57[1] = {0x38}; // Register address
349 static const uint8_t enviro_rdata57[1];
350
351 static const uint8_t enviro_waddr56[1] = {0x37}; // Write address
352 static const uint8_t enviro_wdata57[1];
353
354 static const uint8_t enviro_raddr58[1] = {0x39}; // Register address
355 static const uint8_t enviro_rdata58[1];
356
357 static const uint8_t enviro_waddr57[1] = {0x38}; // Write address
358 static const uint8_t enviro_wdata58[1];
359
360 static const uint8_t enviro_raddr59[1] = {0x3A}; // Register address
361 static const uint8_t enviro_rdata59[1];
362
363 static const uint8_t enviro_waddr58[1] = {0x39}; // Write address
364 static const uint8_t enviro_wdata59[1];
365
366 static const uint8_t enviro_raddr60[1] = {0x3B}; // Register address
367 static const uint8_t enviro_rdata60[1];
368
369 static const uint8_t enviro_waddr59[1] = {0x3A}; // Write address
370 static const uint8_t enviro_wdata60[1];
371
372 static const uint8_t enviro_raddr61[1] = {0x3C}; // Register address
373 static const uint8_t enviro_rdata61[1];
374
375 static const uint8_t enviro_waddr60[1] = {0x3B}; // Write address
376 static const uint8_t enviro_wdata61[1];
377
378 static const uint8_t enviro_raddr62[1] = {0x3D}; // Register address
379 static const uint8_t enviro_rdata62[1];
380
381 static const uint8_t enviro_waddr61[1] = {0x3C}; // Write address
382 static const uint8_t enviro_wdata62[1];
383
384 static const uint8_t enviro_raddr63[1] = {0x3E}; // Register address
385 static const uint8_t enviro_rdata63[1];
386
387 static const uint8_t enviro_waddr62[1] = {0x3D}; // Write address
388 static const uint8_t enviro_wdata63[1];
389
390 static const uint8_t enviro_raddr64[1] = {0x3F}; // Register address
391 static const uint8_t enviro_rdata64[1];
392
393 static const uint8_t enviro_waddr63[1] = {0x3E}; // Write address
394 static const uint8_t enviro_wdata64[1];
395
396 static const uint8_t enviro_raddr65[1] = {0x40}; // Register address
397 static const uint8_t enviro_rdata65[1];
398
399 static const uint8_t enviro_waddr64[1] = {0x3F}; // Write address
400 static const uint8_t enviro_wdata65[1];
401
402 static const uint8_t enviro_raddr66[1] = {0x41}; // Register address
403 static const uint8_t enviro_rdata66[1];
404
405 static const uint8_t enviro_waddr65[1] = {0x40}; // Write address
406 static const uint8_t enviro_wdata66[1];
407
408 static const uint8_t enviro_raddr67[1] = {0x42}; // Register address
409 static const uint8_t enviro_rdata67[1];
410
411 static const uint8_t enviro_waddr66[1] = {0x41}; // Write address
412 static const uint8_t enviro_wdata67[1];
413
414 static const uint8_t enviro_raddr68[1] = {0x43}; // Register address
415 static const uint8_t enviro_rdata68[1];
416
417 static const uint8_t enviro_waddr67[1] = {0x42}; // Write address
418 static const uint8_t enviro_wdata68[1];
419
420 static const uint8_t enviro_raddr69[1] = {0x44}; // Register address
421 static const uint8_t enviro_rdata69[1];
422
423 static const uint8_t enviro_waddr68[1] = {0x43}; // Write address
424 static const uint8_t enviro_wdata69[1];
425
426 static const uint8_t enviro_raddr70[1] = {0x45}; // Register address
427 static const uint8_t enviro_rdata70[1];
428
429 static const uint8_t enviro_waddr69[1] = {0x44}; // Write address
430 static const uint8_t enviro_wdata70[1];
431
432 static const uint8_t enviro_raddr71[1] = {0x46}; // Register address
433 static const uint8_t enviro_rdata71[1];
434
435 static const uint8_t enviro_waddr70[1] = {0x45}; // Write address
436 static const uint8_t enviro_wdata71[1];
437
438 static const uint8_t enviro_raddr72[1] = {0x47}; // Register address
439 static const uint8_t enviro_rdata72[1];
440
441 static const uint8_t enviro_waddr71[1] = {0x46}; // Write address
442 static const uint8_t enviro_wdata72[1];
443
444 static const uint8_t enviro_raddr73[1] = {0x48}; // Register address
445 static const uint8_t enviro_rdata73[1];
446
447 static const uint8_t enviro_waddr72[1] = {0x47}; // Write address
448 static const uint8_t enviro_wdata73[1];
449
450 static const uint8_t enviro_raddr74[1] = {0x49}; // Register address
451 static const uint8_t enviro_rdata74[1];
452
453 static const uint8_t enviro_waddr73[1] = {0x48}; // Write address
454 static const uint8_t enviro_wdata74[1];
455
456 static const uint8_t enviro_raddr75[1] = {0x4A}; // Register address
457 static const uint8_t enviro_rdata75[1];
458
459 static const uint8_t enviro_waddr74[1] = {0x49}; // Write address
460 static const uint8_t enviro_wdata75[1];
461
462 static const uint8_t enviro_raddr76[1] = {0x4B}; // Register address
463 static const uint8_t enviro_rdata76[1];
464
465 static const uint8_t enviro_waddr75[1] = {0x4A}; // Write address
466 static const uint8_t enviro_wdata76[1];
467
468 static const uint8_t enviro_raddr77[1] = {0x4C}; // Register address
469 static const uint8_t enviro_rdata77[1];
470
471 static const uint8_t enviro_waddr76[1] = {0x4B}; // Write address
472 static const uint8_t enviro_wdata77[1];
473
474 static const uint8_t enviro_raddr78[1] = {0x4D}; // Register address
475 static const uint8_t enviro_rdata78[1];
476
477 static const uint8_t enviro_waddr77[1] = {0x4C}; // Write address
478 static const uint8_t enviro_wdata78[1];
479
480 static const uint8_t enviro_raddr79[1] = {0x4E}; // Register address
481 static const uint8_t enviro_rdata79[1];
482
483 static const uint8_t enviro_waddr78[1] = {0x4D}; // Write address
484 static const uint8_t enviro_wdata79[1];
485
486 static const uint8_t enviro_raddr80[1] = {0x4F}; // Register address
487 static const uint8_t enviro_rdata80[1];
488
489 static const uint8_t enviro_waddr79[1] = {0x4E}; // Write address
490 static const uint8_t enviro_wdata80[1];
491
492 static const uint8_t enviro_raddr81[1] = {0x50}; // Register address
493 static const uint8_t enviro_rdata81[1];
494
495 static const uint8_t enviro_waddr80[1] = {0x4F}; // Write address
496 static const uint8_t enviro_wdata81[1];
497
498 static const uint8_t enviro_raddr82[1] = {0x51}; // Register address
499 static const uint8_t enviro_rdata82[1];
500
501 static const uint8_t enviro_waddr81[1] = {0x50}; // Write address
502 static const uint8_t enviro_wdata82[1];
503
504 static const uint8_t enviro_raddr83[1] = {0x52}; // Register address
505 static const uint8_t enviro_rdata83[1];
506
507 static const uint8_t enviro_waddr82[1] = {0x51}; // Write address
508 static const uint8_t enviro_wdata83[1];
509
510 static const uint8_t enviro_raddr84[1] = {0x53}; // Register address
511 static const uint8_t enviro_rdata84[1];
512
513 static const uint8_t enviro_waddr83[1] = {0x52}; // Write address
514 static const uint8_t enviro_wdata84[1];
515
516 static const uint8_t enviro_raddr85[1] = {0x54}; // Register address
517 static const uint8_t enviro_rdata85[1];
518
519 static const uint8_t enviro_waddr84[1] = {0x53}; // Write address
520 static const uint8_t enviro_wdata85[1];
521
522 static const uint8_t enviro_raddr86[1] = {0x55}; // Register address
523 static const uint8_t enviro_rdata86[1];
524
525 static const uint8_t enviro_waddr85[1] = {0x54}; // Write address
526 static const uint8_t enviro_wdata86[1];
527
528 static const uint8_t enviro_raddr87[1] = {0x56}; // Register address
529 static const uint8_t enviro_rdata87[1];
530
531 static const uint8_t enviro_waddr86[1] = {0x55}; // Write address
532 static const uint8_t enviro_wdata87[1];
533
534 static const uint8_t enviro_raddr88[1] = {0x57}; // Register address
535 static const uint8_t enviro_rdata88[1];
536
537 static const uint8_t enviro_waddr87[1] = {0x56}; // Write address
538 static const uint8_t enviro_wdata88[1];
539
540 static const uint8_t enviro_raddr89[1] = {0x58}; // Register address
541 static const uint8_t enviro_rdata89[1];
542
543 static const uint8_t enviro_waddr88[1] = {0x57}; // Write address
544 static const uint8_t enviro_wdata89[1];
545
546 static const uint8_t enviro_raddr90[1] = {0x59}; // Register address
547 static const uint8_t enviro_rdata90[1];
548
549 static const uint8_t enviro_waddr89[1] = {0x58}; // Write address
550 static const uint8_t enviro_wdata90[1];
551
552 static const uint8_t enviro_raddr91[1] = {0x5A}; // Register address
553 static const uint8_t enviro_rdata91[1];
554
555 static const uint8_t enviro_waddr90[1] = {0x59}; // Write address
556 static const uint8_t enviro_wdata91[1];
557
558 static const uint8_t enviro_raddr92[1] = {0x5B}; // Register address
559 static const uint8_t enviro_rdata92[1];
560
561 static const uint8_t enviro_waddr91[1] = {0x5A}; // Write address
562 static const uint8_t enviro_wdata92[1];
563
564 static const uint8_t enviro_raddr93[1] = {0x5C}; // Register address
565 static const uint8_t enviro_rdata93[1];
566
567 static const uint8_t enviro_waddr92[1] = {0x5B}; // Write address
568 static const uint8_t enviro_wdata93[1];
569
570 static const uint8_t enviro_raddr94[1] = {0x5D}; // Register address
571 static const uint8_t enviro_rdata94[1];
572
573 static const uint8_t enviro_waddr93[1] = {0x5C}; // Write address
574 static const uint8_t enviro_wdata94[1];
575
576 static const uint8_t enviro_raddr95[1] = {0x5E}; // Register address
577 static const uint8_t enviro_rdata95[1];
578
579 static const uint8_t enviro_waddr94[1] = {0x5D}; // Write address
580 static const uint8_t enviro_wdata95[1];
581
582 static const uint8_t enviro_raddr96[1] = {0x5F}; // Register address
583 static const uint8_t enviro_rdata96[1];
584
585 static const uint8_t enviro_waddr95[1] = {0x5E}; // Write address
586 static const uint8_t enviro_wdata96[1];
587
588 static const uint8_t enviro_raddr97[1] = {0x60}; // Register address
589 static const uint8_t enviro_rdata97[1];
590
591 static const uint8_t enviro_waddr96[1] = {0x5F}; // Write address
592 static const uint8_t enviro_wdata97[1];
593
594 static const uint8_t enviro_raddr98[1] = {0x61}; // Register address
595 static const uint8_t enviro_rdata98[1];
596
597 static const uint8_t enviro_waddr97[1] = {0x60}; // Write address
598 static const uint8_t enviro_wdata98[1];
599
600 static const uint8_t enviro_raddr99[1] = {0x62}; // Register address
601 static const uint8_t enviro_rdata99[1];
602
603 static const uint8_t enviro_waddr98[1] = {0x61}; // Write address
604 static const uint8_t enviro_wdata99[1];
605
606 static const uint8_t enviro_raddr100[1] = {0x63}; // Register address
607 static const uint8_t enviro_rdata100[1];
608
609 static const uint8_t enviro_waddr99[1] = {0x62}; // Write address
610 static const uint8_t enviro_wdata100[1];
611
612 static const uint8_t enviro_raddr101[1] = {0x64}; // Register address
613 static const uint8_t enviro_rdata101[1];
614
615 static const uint8_t enviro_waddr100[1] = {0x63}; // Write address
616 static const uint8_t enviro_wdata101[1];
617
618 static const uint8_t enviro_raddr102[1] = {0x65}; // Register address
619 static const uint8_t enviro_rdata102[1];
620
621 static const uint8_t enviro_waddr101[1] = {0x64}; // Write address
622 static const uint8_t enviro_wdata102[1];
623
624 static const uint8_t enviro_raddr103[1] = {0x66}; // Register address
625 static const uint8_t enviro_rdata103[1];
626
627 static const uint8_t enviro_waddr102[1] = {0x65}; // Write address
628 static const uint8_t enviro_wdata103[1];
629
630 static const uint8_t enviro_raddr104[1] = {0x67}; // Register address
631 static const uint8_t enviro_rdata104[1];
632
633 static const uint8_t enviro_waddr103[1] = {0x66}; // Write address
634 static const uint8_t enviro_wdata104[1];
635
636 static const uint8_t enviro_raddr105[1] = {0x68}; // Register address
637 static const uint8_t enviro_rdata105[1];
638
639 static const uint8_t enviro_waddr104[1] = {0x67}; // Write address
640 static const uint8_t enviro_wdata105[1];
641
642 static const uint8_t enviro_raddr106[1] = {0x69}; // Register address
643 static const uint8_t enviro_rdata106[1];
644
645 static const uint8_t enviro_waddr105[1] = {0x68}; // Write address
646 static const uint8_t enviro_wdata106[1];
647
648 static const uint8_t enviro_raddr107[1] = {0x6A}; // Register address
649 static
```

USER INTERFACE

DISPLAY FORMAT

The figure to the right shows a sample format for the Enviro app display of temperature and relative humidity, using floating point numbers.

Temp: 25.1°C
Hum: 30.2 %

Note: Enviro app does not need to process any Touchpad input.

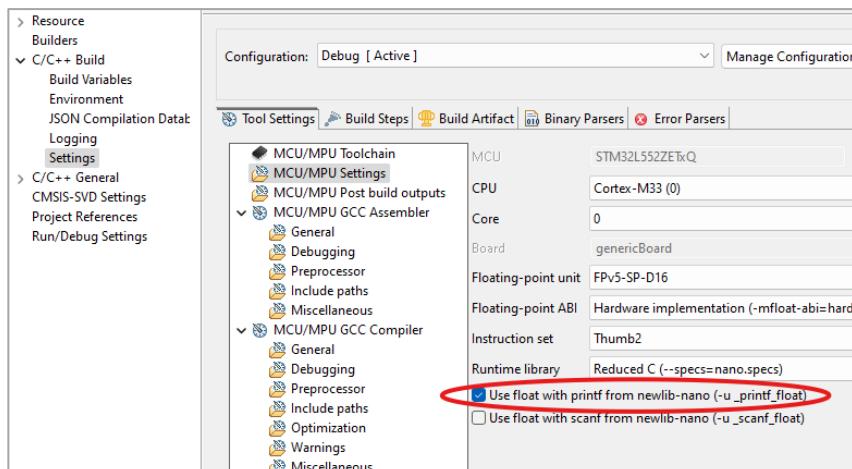
Sample Enviro app display

FORMATTING FLOATING-POINT VALUES

If you'd like to use `%f` floating-point format specifiers in `printf()` and `DisplayPrint()`, you'll first need to change a setting for your project, as shown in the screenshot below:

1. In Project Explorer, right click your project and select Preferences.
2. Under C/C++ Build, open Settings.
3. Under MCU/CPU Settings, check the box to **Use float with printf from newlib-nano**.
4. Click Apply and Close. If asked, confirm rebuilding the index.

PROTIP: The format specifier `%n.mf` (where `n` and `m` are integers) guarantees a fixed spacing/digits on the left/right side of the decimal point respectively. For example, `%2.2f` will format `9.2` as “ `9.20` ” (one leading space) and `17.3333` as “`17.33`”. This is quite handy when you want the field width and precision to stay consistent over a range of values.

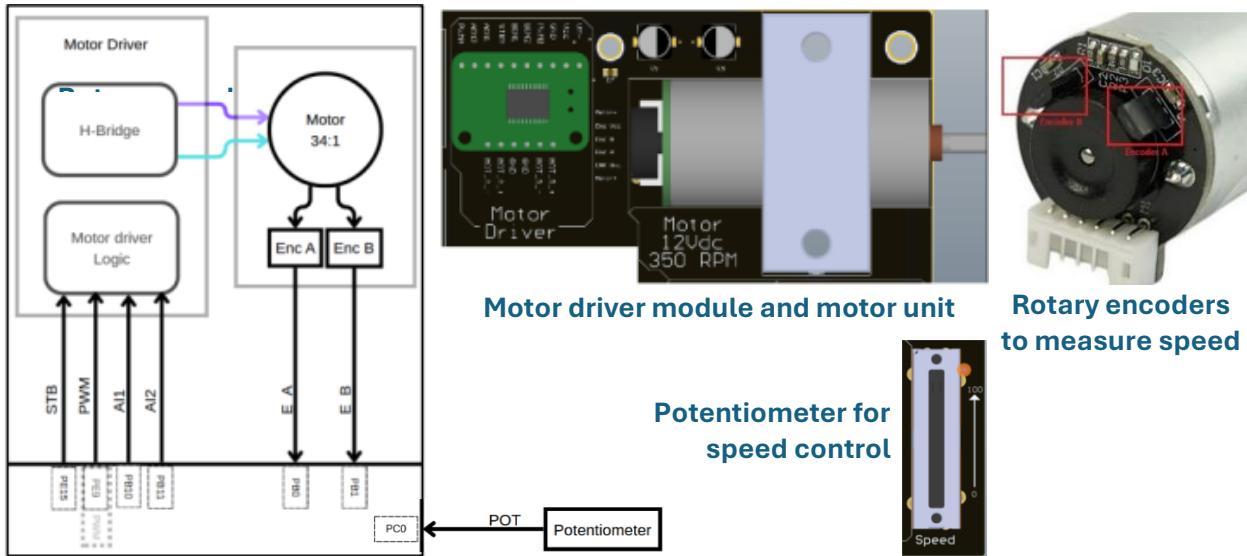


Required project setting for printing floating point numbers

MOTOR CONTROLLER

In the section, we'll build an app to control the on-board DC motor via its driver IC with a PWM signal generated by a timer. We'll use an ADC to measure the position of the on-board slider potentiometer, acting as a speed control, and GPIO interrupts to measure the speed from the motor's built-in rotary encoders.

MCU pin connections for each component are shown in the diagram below:



ADC DRIVER

We will start by creating a simple driver for our MCU's analog-to-digital converter (ADC) peripheral so we can detect the position of the slider potentiometer.

The driver supports one channel in discontinuous mode, with conversions performed on-demand. However, it could be expanded to support multiple channels and continuous mode conversions.

ADC.H

Under the `Inc` folder, add a new header file named `adc.h`. Replace its entire contents by copying and pasting the mini-text shown (right above).

adc.h

```
#include "ADC.h"
#include "GPIO.h"
#include "ADC12.h"
#include "RCC.h"

// ADC input structure
typedef struct {
    ADC_SQR_TypeDef *rface; // ADC peripheral
    int chan; // Channel number
    int pin; // Input/output pin
} ADCInput_t;

void ADC_Enable(ADCInput_t ai);
uint32_t ADC_Read(ADCInput_t ai);
float ADC_Conv(ADCInput_t ai);
```

adc.c

```
// ADC driver
#include "adc.h"
#include "GPIO.h"
void ADC_Enable(ADCInput_t ai) {
    ADC_TypeDef ADC = ai.interface;
    // Configure GPIO pin for analog
    GPIO_Enable(ai.pin);
    GPIO_Mode(ai.pin, ANALOG);

    RCC->AHB2ENR |= RCC_AHB2ENR_ADCEN; // Enable ADC clock
    RCC->APB2ENR |= RCC_APB2ENR_SYSCFG8; // Enable SYSCFG clock
    RCC->APB2ENR |= RCC_APB2ENR_CCOPIE; // Select PLLAOClock
    ADC->CR |= ADC_CR_ADEN; // Enable ADC
    ADC->CR |= ADC_CR_DREFPD; // Disable deep power down
    ADC12_COMMON_NSR->CCR |= ADC_CCR_PRES; // Clear prescaler
    ADC12_COMMON_NSR->CCR |= ADC_CCR_CONODE_1; // Set clock to HCLK/1
    // 1 conversion, selected channel, discontinuous mode
    ADC->SQR1 = 0 << ADC_SQR1_L;
    ADC->SQR1 |= (ai.chan << ADC_SQR1_SQ1_Pos);
    ADC->SQR1 |= ADC_SQR1_TS;

    ADC->CR |= (ADC_CR_JADSTART | ADC_CR_ACSTART | ADC_CR_ADDIS);
    ADC->CR |= ADC_CR_ADREGEN; // Enable ADC voltage regulator
    ADC->CR |= ADC_CR_ANEN; // Enable ADC

    // Read ADC
    uint32_t ADC_Read(ADCInput_t ai) {
        ADC_TypeDef ADC = ai.interface;
        ADC->CR |= ADC_CR_ADSTART; // Start first ADC conversion, software triggered
        while (!(ADC->ISR & ADC_ISR_EOC)) // Wait for conversion to complete
            ;
        return ADC1->DR; // Conversion result
    }
}
```

ADC.C

Under the `Src` folder, add a new source file named `adc.c`. Replace its entire contents by copying and pasting the mini-text shown (right).

TIMER DRIVER

In this section, we'll build a driver for the general-purpose timer peripheral provided by the MCU. Our driver supports the following subset of the timer features:

- Configuring the timer period in terms of pre-scaler and automatic reload values.
 - *Output Compare* mode to toggle an output or generate a pulse-width modulated (PWM) signal.
 - *Input Capture* mode to timestamp rising or falling edge transitions observed on a pin.
 - Interrupt callbacks for timer update and capture/compare events.

TIMER.H

Under the `Inc` folder, add a new header file named `timer.h`. Replace its entire contents by copying and pasting the mini-text shown (right).

TIMER.C

Under the `Src` folder, add a new source file named `timer.c`. Replace its entire contents by copying and pasting the mini-text shown (right below).

Complete the highlighted code to configure the GPIO pin associated with the timer capture/compare channel. Hint: Check your SPI and I²C drivers for examples and refer to the TimerIO_t structure definition in timer.h.

timer.h

timer.c

Updated

MOTOR APP

We will now use our ADC and Timer drivers to create an app to control the motor.

MOTOR.H

Under the `Inc` folder, add a new header file named `motor.h`. Replace its entire contents by copying and pasting the text shown (right).

MOTOR.C

Under the `Src` folder, add a new source file named `motor.c`. Replace its entire contents by copying and pasting the skeleton code in the mini-text shown (right).

Read the next few sections of the lab manual before attempting to complete the code.

DISPLAY.H

Open your existing `display.h` file. Add `MOTOR` to the display page enumeration. If necessary, increase the number of display PAGES. You can set the default backlight colour for any added pages beyond the initial four in the `dispColor[]` array defined in `display.c`.

MAIN.C

Open your existing `main.c` file. Add an include directive `motor.h`. Also add calls to `Init_Motor()` and `Task_Motor()` in the appropriate places.

Note: Our ADC and Timer drivers do not require any housekeeping tasks to be run in the main loop.

```
motor.h
#ifndef MOTOR_H_
#define MOTOR_H_

void Init_Motor(void);
void Task_Motor(void);

#endif /* MOTOR_H_ */
```

```
motor.c
Updated
motor.c
// Motor controller API
#include <math.h>
#include "math.h"
#include "math.h"
#include "motor.h"
#include "display.h"
#include "display.h"
#include "pin.h"
#include "pin.h"
#include "adc.h"
#include "adc.h"
#include "adc.h"
#include "adc.h"

// GPOD pins
// Refer to Lab User's Guide
#define Pin_1_A01 = DIO27, T1P // Pin 27 -> Motor driver
static const Pin_1_A01 = DIO27, T1P // Motor driver
#define Pin_1_A02 = DIO28, T1N // Pin 28 -> Motor driver
static const Pin_1_A02 = DIO28, T1N // Motor driver
#define Pin_1_EncoderA = DIO29, T2P // Pin 29 -> Rotary encoder A
static const Pin_1_EncoderA = DIO29, T2P // Rotary encoder A
#define Pin_1_EncoderB = DIO30, T2N // Pin 30 -> Rotary encoder B
static const Pin_1_EncoderB = DIO30, T2N // Rotary encoder B

// ADC pins
// Refer to Lab User's Guide
#define ADCInputA = ADC1, 0 // ADC Input A
static const ADCInputA = ADC1, 0 // ADC Input A

// Timer channels
// Refer to Lab User's Guide
#define Timer0_Motor = TMR0, 0 // Timer0 Channel A
static const Timer0_Motor = TMR0, 0 // Timer0 Channel A

// Timer period
#define TMR0_Period = (1000 * 11) // Prescaler
#define PWM_ADR = (240 * 1) // Auto-reload
#define PWM_BCR = (30 * 1) // Repetition count

// Motor enum
static enum {CCW=0, CW=1} direction = CCW // Clockwise or counter-clockwise
static float rpmDesiredMotor;
static float desiredRPM = 0;
static float errorRPM = 0;
static float errorSum = 0;
static float dI = 0;
static float dI0 = 0.001;

uint8_t pulsesA = 0;
uint8_t pulsesB = 0;
uint8_t totalPulses = 0;
uint8_t measureSPW = 0;

// Timer callbacks
static void CALLBACKmotor(void);
static void CALLBACKencoder(void);
static void CALLBACKADC(void);

// Change motor direction
// Set desired RPM and direction
void MotorDirection(EMAC_DIR dir) {
    if (dir == CCW) {
        direction = CCW;
    } else {
        direction = Counter-clockwise;
    }
}

// Initialize app
void Init_Motor (void) {
    #if !_FPU_PRESENT || !_FPU_USED
        SDA_VREFC = ((RCAL << 10*2))((SCL << 11*2));
    #endif

    ADC_Enable(Pot);
    // Configure GPOD pins
    // Set up for motor driver
    // Register callbacks for rotary encoder reports
    // ...
}

// Initialize motor
void InitializeMotor(MOTOR_DIRECTION dir) {
    if (dir == CCW) {
        direction = CCW;
    } else {
        direction = Counter-clockwise;
    }

    // Initialize app
    void Init_Motor (void) {
        #if !_FPU_PRESENT || !_FPU_USED
            SDA_VREFC = ((RCAL << 10*2))((SCL << 11*2));
        #endif

        ADC_Enable(Pot);
        // Configure GPOD pins
        // Set up for motor driver
        // Register callbacks for rotary encoder reports
        // ...
    }

    TimerEnable(Motor);
    TimerPeriod(Motor);
    TimerPrescaler(Motor, PWM_ADR, PWM_BCR);
    TimerClockSource(Motor, CallbackMotor, SPI);
    TimerCalibration(Motor);
    MotorDirection(direction);
    rpmScalingFactor = 0;
}

void Task_Motor (void) {
    float motorDrivePercentage = (float)ADC_Read(Pot) / 4096.0;
    desiredRPM = motorDrivePercentage * MAX_SPEED;
    if (timeCount >= prevTimeCount > 0) {
        timeCount = (float)totalPulses * rpmScalingFactor;
        if (logMode == 0) {
            LogOutput(Motor, (uint16_t) motorDrivePercentage * (float)(PWM_ADR + 1));
        }
        // Display status for Open Loop mode
        DisplayPrint(MOTOR, 1, "RPM", v32_RPM, direction == CCW ? "C" : "S", (int)measureSPW);
    }
    else {
        // Closed Loop control
        float Rp = (float)Rp * dpc;
        float Rn = (float)Rn * dI;
        errorSPW = desiredSPW - measuredSPW;
        errorSum += errorSPW;
        // Scale errorSPW to Time SPW
        errorSPW = errorSPW / MAX_SPEED * (float)(PWM_ADR + 1);
        errorSum = errorSum / MAX_SPEED * (float)(PWM_ADR + 1);
        timeInput(Motor, round((Rp*errorSPW + K1*errorSum)));
        if (loopMode == CCW) {
            // Display status for Closed Loop mode / tuning enabled
        } else {
            // Display status for normal Closed Loop mode
        }
    }
    totalPulses = 0;
    prevTimeCount = timeCount;
}

// Touchpad control
switch (TouchPadInput(MOTOR)) {
    // Motor direction: update and apply
    // ...
    // Controller operating mode
    // ...
    // Controller tuning (must be in CLP mode to take effect)
    // ...
    default: break;
}

// Timer 1 output
void CALLBACKmotor (void) {
    timeCount++;
    totalPulses += pulsesA + pulsesB;
    pulsesA = 0;
    pulsesB = 0;
}

// Rotary encoder A rising edge
void CALLBACKencoder (void) {
    pulsesA++;
}

// Potentiometer A rising edge
void CALLBACKADC (void) {
    pulsesB++;
}
```

SPEED MEASUREMENT

Assigned at the end of `Init_Motor()` and used in `Task_Motor()`, calculated variable `rpmScalingFactor` converts `totalPulses` (from the rotary encoders) counted during the timer update interval into a rotational speed in revolutions per minute (RPM):

```
measuredRPM = totalPulses * rpmScalingFactor;
```

`rpmScalingFactor` can be expressed as the ratio of the *timer update frequency* (f_{UP}) to the number of *pulses per revolution* (PPR) generated by the two rotary encoders. A factor of 60 converts frequency from a per-second to per-minute time base (i.e. Hz to RPM):

$$rpmScalingFactor = \frac{f_{UP}}{PPR} \times \frac{60s}{1 \text{ min}}$$

- Express f_{UP} by starting with the `SYCLK` frequency (see `sysclk.h`) and dividing by the timer pre-scaler, auto-reload period, and repetition count (see constants in `motor.c`). Remember to add 1 to each constant to undo the effect of subtracting 1!
- Express PPR as the pulses per rotation of the rotary encoder multiplied by the gear ratio to the motor drive shaft, then further doubled because we're counting pulses from two encoders. See your [Lab User's Guide](#) for these specifications.

Your final code should contain a **documented expression to calculate `rpmScalingFactor`**. To check your work, highlight the hidden value in the box below and copy and paste it into a comment in your code:

Reveal the correct value here:

`rpmScalingFactor =`

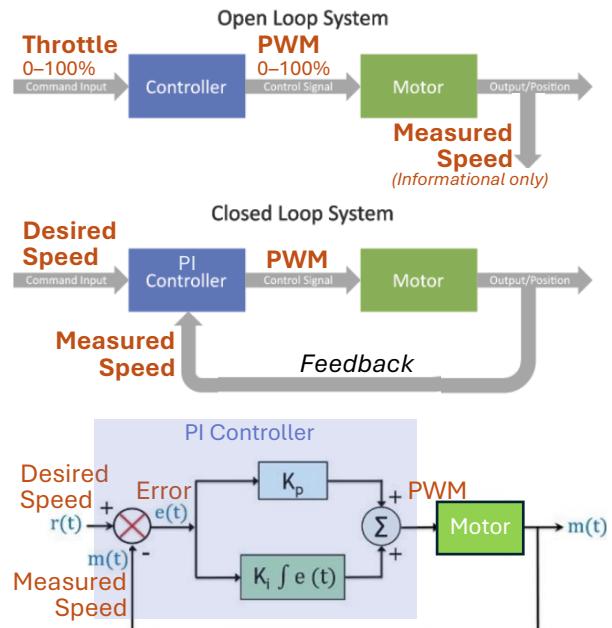
CONTROL SCHEMES

Our Motor app supports two basic control schemes:

Open Loop—The slide potentiometer acts as a throttle control, which is directly converted to PWM duty cycle between 0% and 100%. The actual motor speed is displayed for informational purposes only.

Closed Loop—Features a *proportional-integral* (PI) controller. The slide potentiometer sets the desired motor speed in RPM. Measured speed is subtracted from desired speed to obtain the speed error. In the proportional path, the most recent error value is scaled by parameter K_p . In the integral path, the sum of all previous error values is scaled by parameter K_i . The outputs of both paths are summed to produce PWM values on an ongoing basis.

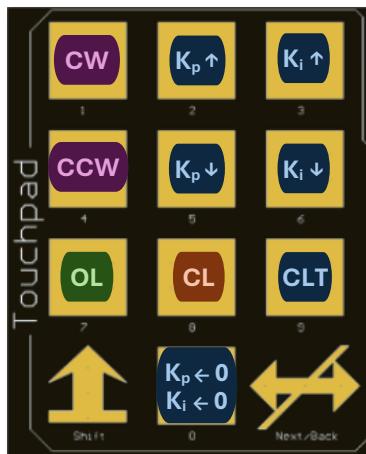
Note: To ensure proper motor control, K_p and K_i must be carefully selected. See below for a tuning guide.



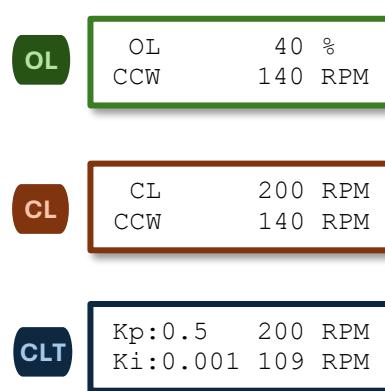
USER INTERFACE

The Motor App includes a user interface (UI) using the on-board display and touchpad:

- Pads 1 & 4 select the motor direction as clockwise (CW) or counterclockwise (CCW). Make sure to apply the new setting after updating the loopMode variable.
- Pads 7–9 select Open Loop (OL), Closed Loop (CL), or Closed Loop Tuning (CLT).
- Pads 2 & 5 and 3 & 6 respectively adjust K_p and K_i up & down by incrementing or decrementing the variables N_p and N_i. The variables must not be allowed to decrement below zero.
- Pad 0 immediately clears K_p and K_i to begin the tuning process.
- Adjustments to K_p and K_i are permitted in CLT mode only. Otherwise, those pad presses are ignored.



Touchpad Controls



Sample Display for Each Mode

CONTROLLER TUNING

The following procedure is recommended for tuning the PI controller:

1. Press 9 to enter **Closed Loop Tuning (CLT) mode**.
2. Press 0 to **clear K_p and K_i** to 0. The motor should be stopped.
3. Adjust the target speed to 200 RPM using the slide potentiometer.
PROTIP: Tuning the motor at its intended operating speed improves controller performance.
4. **Slowly increase K_p**, while listening to the motor sound. Stop when it oscillates periodically.
5. **Reduce K_p to half** (or slightly less than half) of the maximum value reached. The oscillations should stop, but the measured speed will have a significant offset from the target speed.
6. **Slowly increase K_i** to minimize the steady state error (approximately 5 RPM is a good result).
Note: The measured motor speed may cross the target speed during this process.
7. If necessary, further fine-tune K_p and K_i through experimentation.
8. In your `motor.c` file, update the default values of variables N_p and N_i to produce your chosen K_p and K_i values when your program is restarted.

Practice tuning your controller a few times. You'll be asked to show and explain the process to your lab TA during your demo!

BONUS: REAL-TIME CLOCK

In this bonus activity, you're challenged to leverage the MCU real-time clock (RTC) peripheral to create an app that displays the time and date and allows the user to change them using the numeric touchpad.

Because there is no battery on the lab platform, the time and date will be lost whenever the board loses power (glowing button). However, you will need to demonstrate that the RTC will survive MCU reset.

RTC DRIVER

The first step is to build a driver for the real-time clock peripheral.

RTC.H

Under the `Inc` folder, add a new header file named `rtc.h`.

Replace its entire contents by copying and pasting the mini-text shown (right above).

RTC.C

Under the `Src` folder, add a new source file named `rtc.c`.

Replace its entire contents by copying and pasting the mini-text shown (right).

Complete the code at the highlighted locations, with help from your [MCU Reference Manual](#). The device header file `stm311552xx.h` is also useful for finding constants for the various register files.

To convert from binary coded decimal (BCD) to ASCII, add the value to character ‘0’. Subtracting character ‘0’ will convert in the opposite direction. Make sure to do your masks and shifts in the correct order!

```
rtc.h
#ifndef RTC_H_
#define RTC_H_

void RTC_Enable(void);
void RTC_GetTime(char *time); // "HH:MM:SS" (9 bytes, including NUL)
void RTC_GetDate(char *date); // "YYYY-MM-DD" (11 bytes, including NUL)
void RTC_SetTime(const char *time);
void RTC_SetDate(const char *date);

#endif /* RTC_H */
```

```
rtc.c
// Real-time clock (RTC) driver

#include <stdio.h>
#include "stm32l15xx.h"
#include "rtc.h"

// Async/sync pre-scalers (recommended values)
#define PDA (128 - 1)
#define PDS (256 - 1)

// -----
// Initialization
// -----

void RTC_Enable (void) {
    // Enable clock to PWR block and disable backup domain write protection
    // Refer to MCU RM 41.8.19 and 8.6.1 and device header file
    // 0x9200
    // Enable LSE 32.768kHz, select LSE for RTC clock, and enable RTC clock
    // Refer to MCU RM 9.8.29 and device header file
    // 0x9200
    // Enable APB clock to RTC peripheral
    // Refer to MCU RM 9.8.19 and device header file
    // 0x9200
    // Disable RTC write protection (2 writes)
    // Refer to MCU RM 41.6.10 and 41.3.11, 2nd subsection
    // 0x9200
    // Asynchronous and synchronous pre-scalers
    // Refer to MCU RM 41.3.6 and 41.6.5. Use the PDA and PDS constants.
    // 0x9200
}

// Get time/date from RTC
// -----
// Generate time string from RTC, formatted HH:MM:SS (24-hour clock)
// Refer to MCU RM 41.6.1
void RTC_GetTime (char *time) {
    uint32_t tr;
    char ht, hu, mn, mn0, st, su;

    tr = RTC->TR;
    ht = '0' + ((tr & RTC_TR_HT_Msk) >> RTC_TR_HT_Pos); // Hour
    hu = '0' + ((tr & RTC_TR_HU_Msk) >> RTC_TR_HU_Pos); // Minute
    mn = '0' + ((tr & RTC_TR_MN_Msk) >> RTC_TR_MN_Pos); // Second
    mn0 = '0' + ((tr & RTC_TR_MN0_Msk) >> RTC_TR_MN0_Pos); // Subsecond
    su = '0' + ((tr & RTC_TR_SU_Msk) >> RTC_TR_SU_Pos); // Subsecond

    sprintf(time, "%tc:%tc:%tc", ht, hu, mn);
    if (mn0 > 0) {
        sprintf(time, "%c", su);
    }
}

// Generate date string from RTC, formatted YYYY-MM-DD
// Refer to MCU RM 41.6.2
void RTC_GetDate (char *date) {
    uint32_t dr;
    int ym, ye, yt, ys, mt, mu, dt, du;

    dr = RTC->DR;
    ym = '0' + ((dr & RTC_DR_YM_Msk) >> RTC_DR_YM_Pos); // Year
    ye = '0' + ((dr & RTC_DR_YE_Msk) >> RTC_DR_YE_Pos); // Year
    ys = '0' + ((dr & RTC_DR_YS_Msk) >> RTC_DR_YS_Pos); // Year
    mt = '0' + ((dr & RTC_DR_MT_Msk) >> RTC_DR_MT_Pos); // Month
    mu = '0' + ((dr & RTC_DR_MU_Msk) >> RTC_DR_MU_Pos); // Month
    dt = '0' + ((dr & RTC_DR_DT_Msk) >> RTC_DR_DT_Pos); // Day
    du = '0' + ((dr & RTC_DR_DU_Msk) >> RTC_DR_DU_Pos); // Day

    sprintf(date, "%c%c-%c%c-%c%c", ye, ys, mt, mu, dt, du);
}

// Set RTC time/date
// -----
// Set RTC time from string, formatted HH:MM:SS
void RTC_SetTime (const char *time) {
    uint32_t tr;

    // Enter initialization mode
    // Refer to MCU RM 41.6.4 and 41.3.11, 3rd subsection, items 1-2
    // 0x9200
    while (!((RTC->CR & RTC_CR_INIT) == 0)) {}

    tr = 0;
    tr |= (time[0] - '0') << RTC_TR_HU_Pos & RTC_TR_HU_Msk; // Hour
    tr |= '0' + ((time[1] - '0') << RTC_TR_MN_Pos & RTC_TR_MN_Msk); // Minute
    tr |= '0' + ((time[2] - '0') << RTC_TR_MN0_Pos & RTC_TR_MN0_Msk); // Second
    RTC->TR = tr;
    RTC->CR |= -RTC_CR_FMT; // FMT=0 (24-hour)

    // Return to free-running mode
    // Refer to MCU RM 41.6.4 and 41.3.11, 3rd subsection, item 5
    // 0x9200
}

// Get RTC date from string, formatted YYYY-MM-DD
void RTC_SetDate (const char *date) {
    uint32_t dr;

    // Enter initialization mode
    // Refer to MCU RM 41.6.4 and 41.3.11, 3rd subsection, items 1-2
    // 0x9200
    dr = 0;
    dr |= '0' + ((date[0] - '0') << RTC_DR_YE_Pos & RTC_DR_YE_Msk); // Year
    dr |= '0' + ((date[1] - '0') << RTC_DR_YS_Pos & RTC_DR_YS_Msk); // Year
    dr |= '0' + ((date[2] - '0') << RTC_DR_MT_Pos & RTC_DR_MT_Msk); // Month
    dr |= '0' + ((date[3] - '0') << RTC_DR_MU_Pos & RTC_DR_MU_Msk); // Month
    dr |= '0' + ((date[4] - '0') << RTC_DR_DT_Pos & RTC_DR_DT_Msk); // Day
    dr |= '0' + ((date[5] - '0') << RTC_DR_DU_Pos & RTC_DR_DU_Msk); // Day
    RTC->DR = dr;

    // Return to free-running mode
    // Refer to MCU RM 41.6.4 and 41.3.11, 3rd subsection, item 5
    // 0x9200
}
```

CLOCK APP

Using the RTC driver, the Clock application shall:

- Display the RTC time in 24-hour format: HH:MM:SS
- Display the RTC date in ISO format: YYYY-MM-DD
- When pad 1 is pressed, allow the user to set the time in the RTC
- When pad 2 is pressed, allow the user to set the date in the RTC

CLOCK.H

Under the `Inc` folder, add a new header file named `clock.h`. Replace its entire contents by copying and pasting the text shown (right).

clock.h

```
#ifndef CLOCK_H
#define CLOCK_H

void Init_Clock(void);
void Task_Clock(void);

#endif /* CLOCK_H */
```

CLOCK.C

Under the `Src` folder, add a new source file named `clock.c`. Replace its entire contents by copying and pasting the skeleton code in the mini-text shown (right).

Complete the highlighted section of code, being careful to account for the format of the time/date string!

clock.c

```
// Clock app

#include <stddef.h>
#include <stdbool.h>
#include <string.h>
#include "clock.h"
#include "rtc.h"
#include "display.h"
#include "touchpad.h"

static char time[9] = "HH:MM:SS";
static char date[11] = "YYYY-MM-DD";

static uint32_t entry; // Touchpad numeric entry

static enum (SHOW, SETDATE, SETTIME) state;

// Facilitate numeric entry of new time or date
bool TimeDateEntry (char *td) {
    bool done;

    DisplayPrint(CLOCK, 1, entry ? "%u" : "", entry);

    done = TouchEntry(CLOCK, &entry);
    if (entry > 99999)
        entry /= 10; // remove last entered digit
    entry /= 10;
    else if (done && entry > 100000) {
        // Convert 6 digits to ASCII and populate string
        // char td[8] = "HH:MM:SS"
        td[7] = '0' + entry % 10; entry /= 10;
        ...
        return true;
    }
    return false; // Incomplete entry
}

// App initialization
void Init_Clock (void) {
    RTC_Enable();
}

// App execution
void Task_Clock (void) {
    switch (state) {

        case SHOW:
            // Display time and date from RTC
            RTC_GetTime(time);
            RTC_GetDate(date);
            DisplayPrint(CLOCK, 0, time);
            DisplayPrint(CLOCK, 1, date);

            switch (TouchInput(CLOCK)) {
                case 1: state = SETTIME; entry = 0; break;
                case 2: state = SETDATE; entry = 0; break;
                default: break;
            }
            break;

        case SETTIME:
            DisplayPrint(CLOCK, 0, "Set time: HHHHSS");
            if (TimeDateEntry(&time[0])) {
                RTC_SetTime(time);
                state = SHOW;
            }
            break;

        case SETDATE:
            DisplayPrint(CLOCK, 0, "Set date: YYYYMMDD");
            if (TimeDateEntry(&date[2])) {
                RTC_SetDate(date);
                state = SHOW;
            }
            break;
    }
}
```

MAIN.C

Open your existing `main.c` file. Add an include directive for `clock.h`. Also add calls to `Init_Clock()` and `Task_Clock()` in the appropriate places.

Note: Our RTC driver does not require a housekeeping task to be run in the main loop.

DELIVERABLES

The following sections detail the key deliverables for this laboratory exercise.

LAB DEMO

After you've completed and tested your project, you will demonstrate your final program to your lab TA.

Grading criteria:

- **Environmental monitor:** Accurate display of temperature and humidity in proper format.
- **Motor controller:** Proper display format for each mode. Motor operation via slide pot and touchpad controls. Demonstrate tuning process to TA.
- **Real-time clock (Optional):** Display time and date in proper format. Set time and date. Survive MCU reset. *Up to 10% bonus awarded for successful completion.*

LAB REPORT

Following your demo, your group will submit two files to Brightspace under **Lab 4 Report**:

1. A standalone .pdf file containing the following:
 - A title page containing the current date, course code, lab number, lab section, student names, and student IDs.
 - Source code authored by your group for Lab 4:
 - Complete listings of `enviro.c`, `motor.c`, and (if applicable) `rtc.c` and `clock.c`
 - `SPI_Enable()` in `spi.c` and `TimerEnable()` in `timer.c`
 - A figure of your own creation showing the relationship between the various software components in the composite program created throughout Labs 1-4.
 - Show each component as a box containing its name (i.e. source file without the .s or .c extension) and a list of public functions and structures.
 - Organize the components into driver layer, application layer (tasks), and management layer (main).
 - Add interconnecting lines to show when one component uses the services of another (e.g. an app uses a driver, main code executes an app).
 - Note: There are **no exploration questions** in Lab 4.
2. A .zip file containing your IDE project used to demo. Follow these instructions to generate it:
 - In the STM32CubeIDE, right click your project and select Clean Project.
 - Exit the IDE completely.
 - **Delete the Debug folder within your project folder.**
 - Compress your entire project folder into a single .zip archive.

IMPORTANT: Submit the .pdf and .zip as *two separate files*, added to the same assignment submission. **Do not archive them together**, as this makes it more difficult to grade your report.