"A" CONNECTOR

1	GND	GND	2
3	+5V (PS)	+5V (PS)	4
5	POWER FAIL (PS)		6
7		+5 OK (PS)	8
9	MEM OK (PS)		10
11	EXEC (CP)	DEFER (CP)	12
13	FETCH (CP)	RUN (CP)	14
15	CARRY (CP)	ION (CP)	16
17	ISTP (CP)		18
19	PL (CP)	MSTP (CP)	20
21		CON INST (CP)	22
23			24
25	CONT + ISTP + MSTP (CP)		26
27	CON RQ (CP)	CON DATA (CP)	28
29		RST (CP)	30
31	STOP (CP)	RESTART ENAB (CP)	32
33	GND	GND	34
35	MEM 13	MEM 12	36
37	MBO 13	MSKO (IO)	38
39	MBO 12	INTA (IO)	40
41	MBO 15	DATIB (IO)	42
43	MBO 14	DATIA (IO)	44
45	MEM 10	DS 3 (IO)	46
47	EXT LOAD	DATOC (IO)	48
49		CLR (IO)	50
51	MEM 11	STRT (IO)	52
53	MEM 9	DATIC (IO)	54
55	MEM 8	DATOB (IO)	56
57		DATOA (IO)	58
59		DCHA (IO)	60
61		DS 4 (IO)	62
63		DS 5 (IO)	64
65		DS 2 (IO)	66
67		DS 1 (IO)	68
69		IORST (IO)	70
71		DSO (IO)	72
73	LOAD IR	IOPLS (IO)	74
75			76
77	LOAD AC		78
79		SELA (IO)	80
81		SELB (IO)	82
83			84
85	FORCE LOAD IR		86
87	MD SEL 1		88
89	WAS JSR		90
91	DIV	TEST	92
93			94
95			96
97	+5V (PS)	+5V (PS)	98
99	GND	GND	100

NOTE: Signal lines connected to the control panel are designated (CP), while those lines connected to the power supply are designated (PS). Signals appearing on the I/O bus are labeled (IO).

"B" CONNECTOR

1	GND	GND	2
3	+5V (PS)	+5V (PS)	4
5	MBO 11	WHOA	6
7	MA LOAD	MBO 10	8
9	MBO 9		10
11		MBO 8	12
13		MBO 7	14
15		MBO 6	16
17	DCHMO (IO)	MEM 15	18
19		STROBE	20
21	DCHMI (IO)	MEM 6	22
23		MEM 7	24
25		MEM 5	26
27		MEM 4	28
29	INTR (IO)	INHIBIT	30
31		MBO 5	32
33	DCHO (IO)		34
35	DCHR (IO)		36
37	DCHI (IO)		38
39	OVFLO (IO)		40
41	RQENB (IO)	MBO 4	42
43	MBO 3	MBO 2	44
45	INH TRANS		46
47	MEM 2	CLK FLOP	48
49	EXT LOAD	GND	50
51			52
53			54
55	DATA 7 (IO)	DATA 14 (IO)	56
57	DATA 5 (IO)	DATA 11 (IO)	58
59	DATA 12 (IO)	DATA 8 (IO)	60
61	DATA 4 (IO)	DATA 0 (IO)	62
63	DATA 9 (IO)	DATA 13 (IO)	64
65	DATA 1 (IO)	DATA 15 (IO)	66
67		MEM 3	68
69	SKIP	MEM 1	70
71	MEM 0		72
73	DATA 3 (IO)	MB LOAD	74
75	DATA 10 (IO)	MEM 14	76
77	MBO 1		78
79	MBO 0		80
81		DATA 2 (IO)	82
83	READ IO		84
85		MB CLEAR	86
87	READ 1	DRIVE IO	88
89		READ 2	90
91	SHIFT 3		92
93	SHIFT 2	SHIFT 0	94
95	DATA 6 (IO)	SHIFT 1	96
97	+5V (PS)	+5V (PS)	98
99	GND	GND	100

D-116 Back Panel Slot 1 (CPU) Rev Drawing No. 400341 - Blatt 1 von 2

NIXDORF COMPUTER SERVICE

D-116 Back Panel I/O or Memory Slots Rev Drawing No. 400341 - Blatt 2 von 2

"A" CONNECTOR

1	GND	GND	2
3	+5V (PS)	+5V (PS)	4
5	RINH0	-5V (PS)	6
7	RINH 1	INH GND	8
9	RINH 2	+15V (PS)	10
11	RINH 3		12
13	RINH 4	INH GND	14
15	RINH 5	INH GND	16
17	RINH 7	RINH 6	18
19	RINH 8	INH GND	20
21	RINH 11	INH GND	22
23	RINH 10	RINH 9	24
25	RINH 13	INH GND	26
27	RINH 15	RINH 12	28
29	RINH 14	INH GND	30
31	INH GND		32
33	GND	GND	34
35	MEM 13	MEM 12	36
37	MBO 13	MSKO (IO)	38
39	MBO 12	INTA (IO)	40
41	MBO 15	DATIB (IO)	42
43	MBO 14	DATIA (IO)	44
45	MEM 10	DS3 (IO)	46
47		DATOC (IO)	48
49		CLR (IO)	50
51	MEM 11	STRT (IO)	52
53	MEM 9	DATIC (IO)	54
55	MEM 8	DATOB (IO)	56
57		DATOA (IO)	58
59		DCHA (IO)	60
61		DS 4 (IO)	62
63		DS 5 (IO)	64
65		DS 2 (IO)	66
67		DS 1 (IO)	68
69		IORST (IO)	70
71		DS 0 (IO)	72
73		IOPLS (IO)	74
75			76
77			78
79		SELD (IO)	80
81		SELB (IO)	82
83			84
85			86
87			88
89			90
91			92
93	DCHP OUT (IO)	DCHP IN (IO)	94
95	INTP OUT (IO)	INTP IN (IO)	96
97	+5V (PS)	+5V (PS)	98
99	GND	GND	100

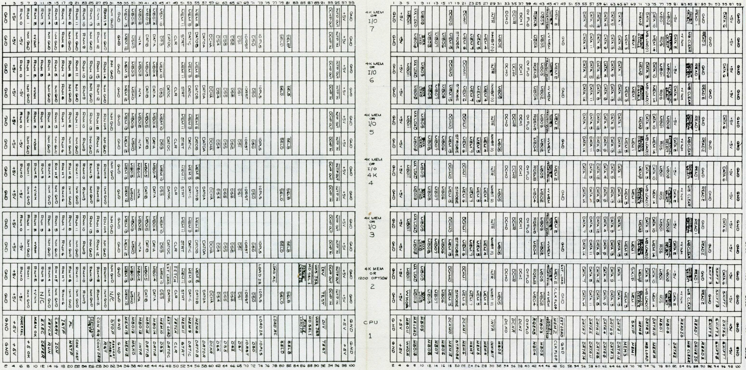
NOTE: Signal lines connected to the control panel are designated (CP) while those lines connected to the power supply are designated (PS). Signals appearing on the I/O bus are labeled (IO).

"B" CONNECTOR

1	GND	GND	2
3	+5V (PS)	+5V (PS)	4
5	MBO 11		6
7	MA LOAD	MBO 10	8
9	MBO 9		10
11		MBO 8	12
13		MBO 7	11
15		MBO 6	16
17	DCH M0 (IO)	MEM 15	18
19		STROBE	20
21	DCH M1 (IO)	MEM 6	22
23		MEM 7	24
25		MEM 5	26
27		MEM 4	28
29	INTR (IO)	INHIBIT	30
31		MBO 5	32
33	DCHO (IO)		34
35	DCHR (IO)		36
37	DCHI (IO)		38
39	OVFLO (IO)		40
41	RQENB (IO)	MBO 4	42
43	MBO 3	MBO 2	44
45	INH TRANS	+V MEM	46
47	MEM 2		48
49		GND	50
51			52
53			54
55	DATA 7 (IO)	DATA 14 (IO)	56
57	DATA 5 (IO)	DATA 11 (IO)	58
59	DATA 12 (IO)	DATA 8 (IO)	60
61	DATA 4 (IO)	DATA 0 (IO)	62
63	DATA 9 (IO)	DATA 13 (IO)	64
65	DATA 1 (IO)	DATA 15 (IO)	66
67		MEM 3	68
69		MEM 1	70
71	MEM 0	RELOAD DISABLE	72
73	DATA 3 (IO)	MB LOAD	74
75	DATA 10 (IO)	MEM 14	76
77	MBO 1		78
79	MBO 0	EXT SELECT	80
81	-5V (PS)	DATA 2 (IO)	82
83		+15V (PS)	84
85	INHIBIT SELECT	MB CLEAR	86
87	READ 1		88
89	GND	READ 2	90
91		GND	92
93			94
95	DATA 6 (IO)		96
97	+5V (PS)	+5V (PS)	98
99	GND	GND	100

DSS 620

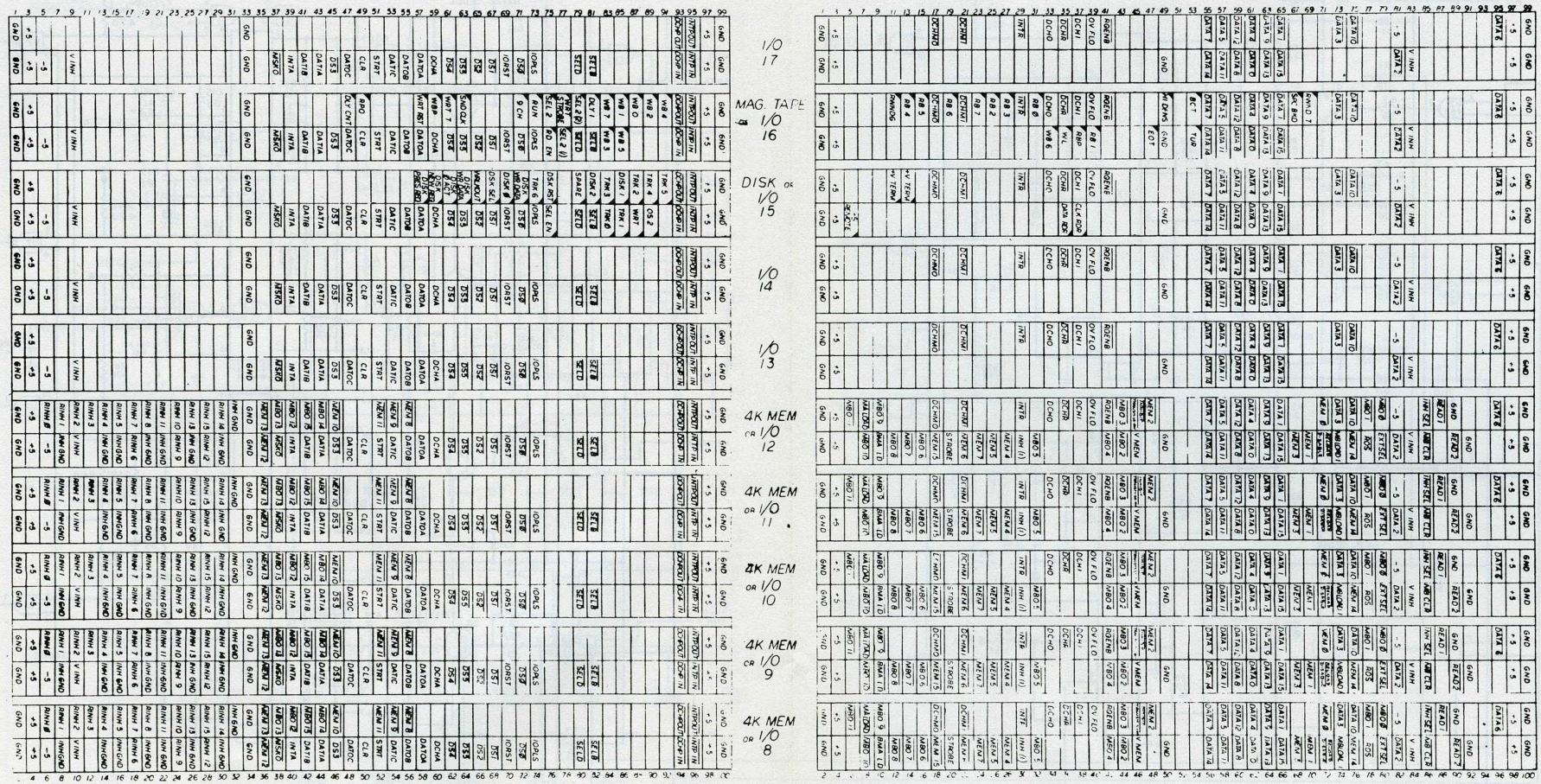
REVISIONS				
REV	DESCRIPTION	DRFTG	APP BY	DATE
01	RSV PER MKD PRINT	RAS		3/9/71
02	ECO 889	[illegible]		5/72



Back Panel Nova 1200

Drawing No. 000090

Blatt 1 von 1



NOTES:
1. REFER TO D.G.C. DRAWING 001-000-090 FOR SLOTS 1-7.
2. BOTTOM POWER SUPPLY GENERATES ALL VMEM AND VINH VOLTAGES AND SUPPLIES +5 AND -5 UP TO SLOT 7. TOP POWER SUPPLY GENERATES THE +5 FOR SLOTS 8-12, .. SECOND FOR SLOTS 13-17 AND -5 FOR SLOTS 1-17
3. [illegible] AND ARE MARKED WITH A ▼ ARE PRESENT ONLY IF THE [illegible] INSTALLED.

Jumbo Backpanel
Nova 800/1200 Slots 8-17
Drawing No. 000169
Blatt 1 von 1