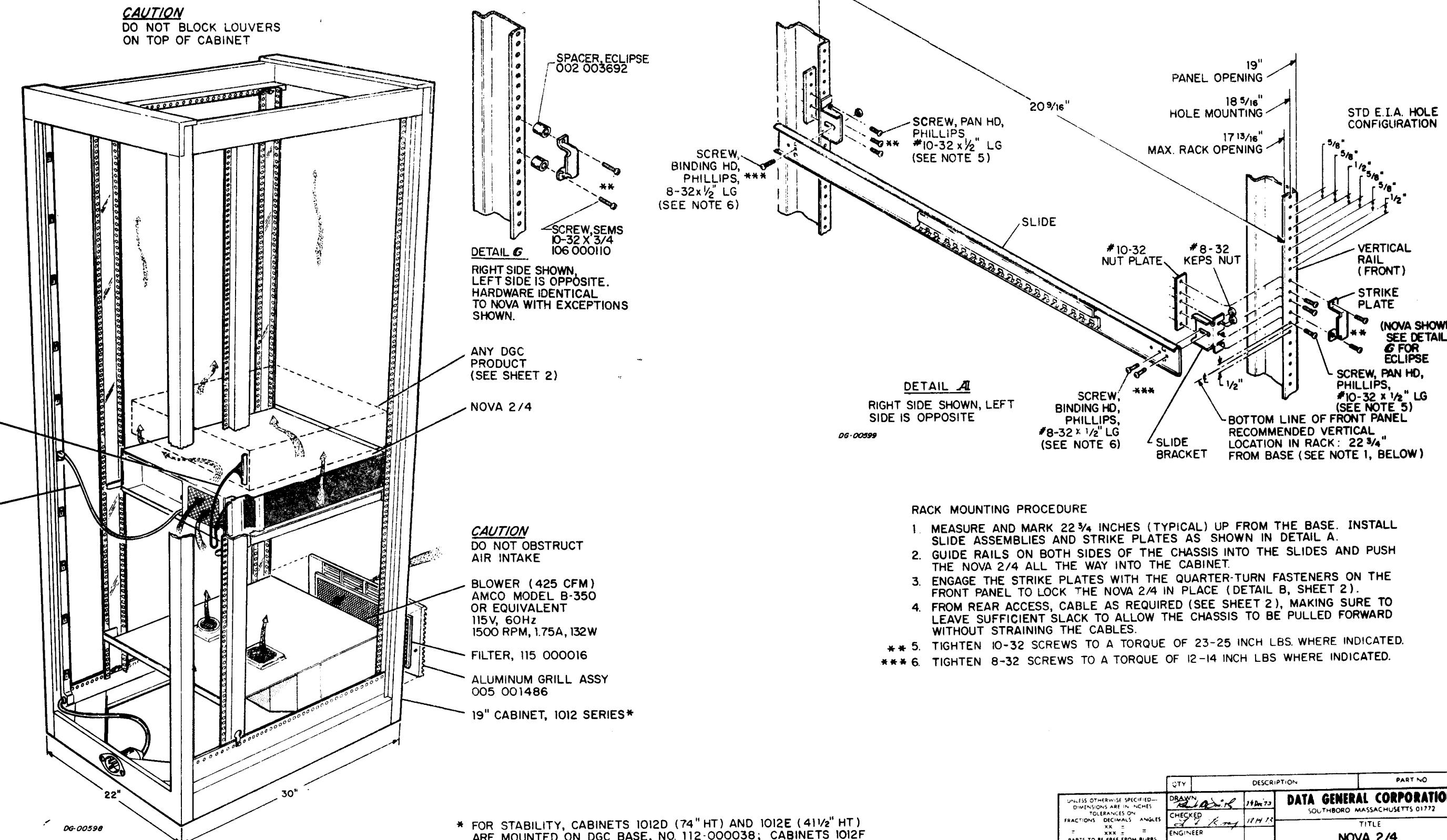


THE DRAWINGS AND SPECIFICATIONS HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION, AND MAY NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

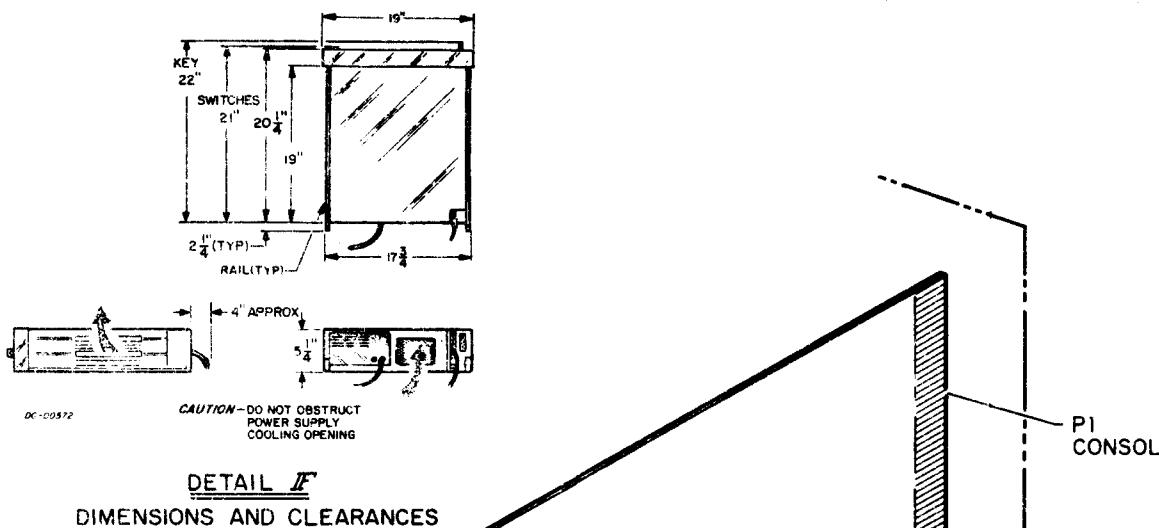
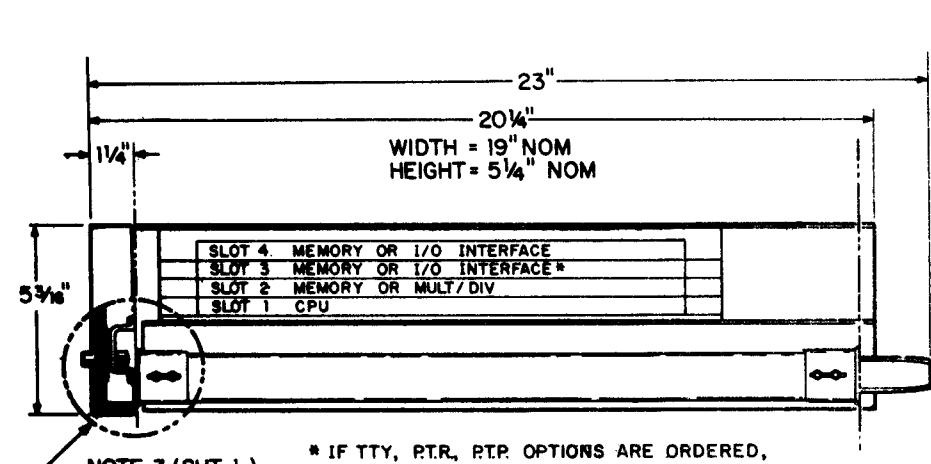
REVISIONS			
REV	DESCRIPTION	DRFTG	APP BY DATE
O1	P/D ECO 32881A	W/DR	10/10/74



CTY	DESCRIPTION	PART NO
DRAWN R. J. Davis	1/4 Dec 73	<b>DATA GENERAL CORPORATION</b>
CHECKED C. J. Koenig	1/14/74	SOUTH BORO MASSACHUSETTS 01772
ENGINEER		TITLE
APPROVED R. J. Davis	1/16/74	NOVA 2/4
MATERIAL	~	RACK
FINISH	~	INSTALLATION
SCALE	~	
SIZE	CODE	DRAWING NUMBER
D	OIO	000044
REV		O1

THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION AND ARE NOT TO BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

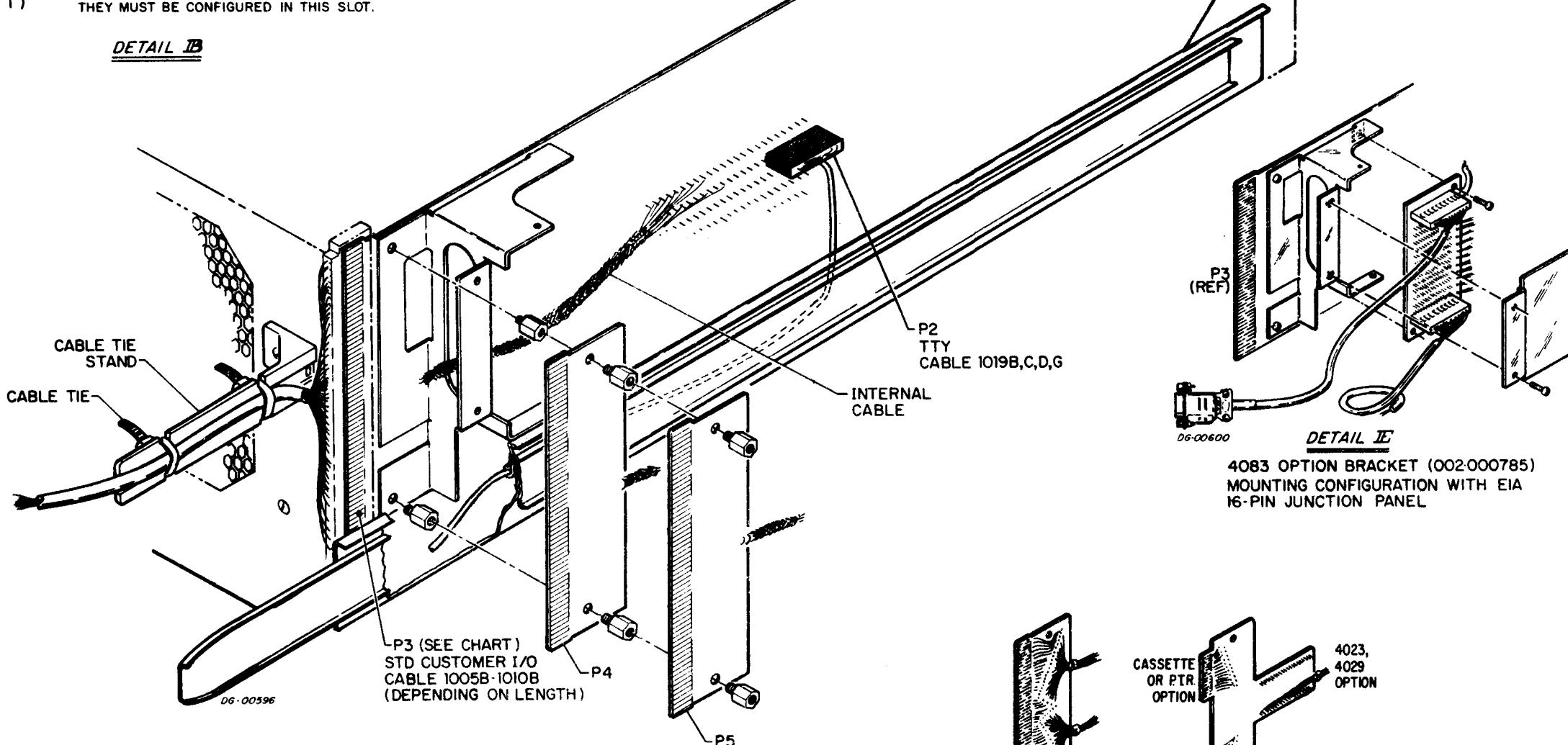
REVISIONS			
REV	DESCRIPTION	DRFTG	APP BY DATE



DETAIL D  
DIMENSIONS AND CLEARANCES

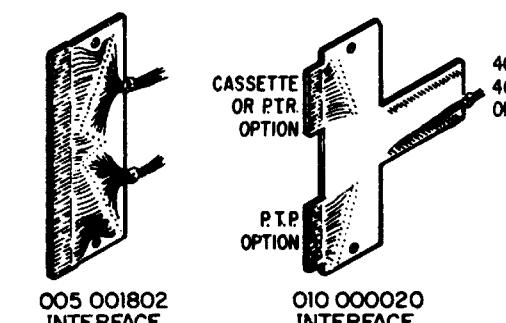
NOTE 3 (SHT 1)  
\* IF TTY, PTR, PTP OPTIONS ARE ORDERED,  
THEY MUST BE CONFIGURED IN THIS SLOT.

DETAIL 1B



DETAIL C

P4 AND P5 OPTIONS CAN BE EITHER  
DGC 005-001802 OR OIO-000020 IN-  
TERFACES (SEE DETAIL D). FOR 4083  
OPTION, SEE DETAIL E.



005 001802  
INTERFACE  
OIO 000020  
INTERFACE

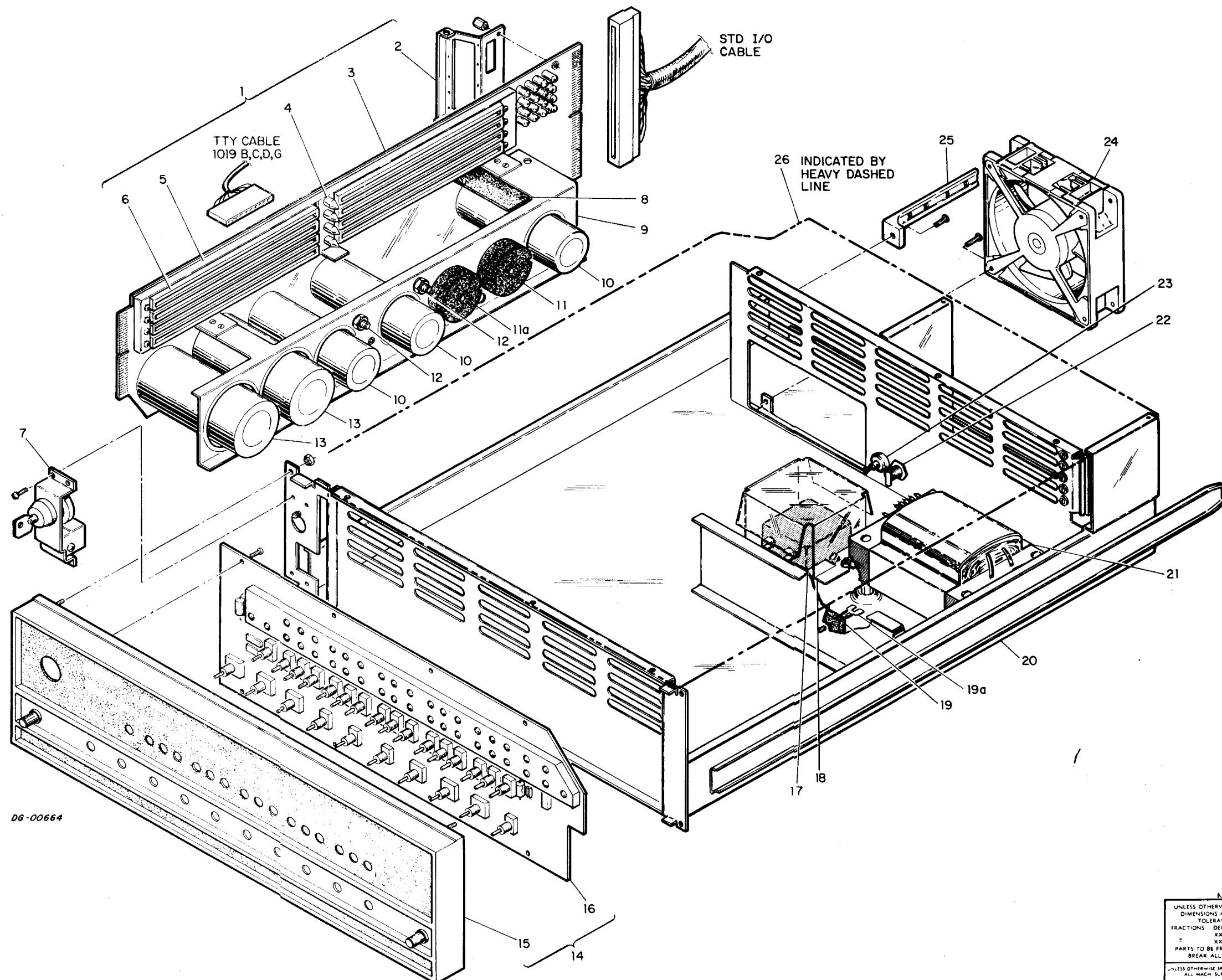
DETAIL 1D

LETTER SIDE	NUMBER SIDE	SIGNAL NAME
~	1-THRU-50 = GND	~
A	~	GND
B	~	PWR ON (+5V)
C	~	MEMO
D	~	INTA
E	~	DATIB
F	~	DATIA
H	~	DSS
J	~	DATOC
K	~	CLR
L	~	STRT
M	~	DATIC
N	~	DATOB
P	~	DATOA
R	~	DCHA
S	~	DSS4
T	~	DSS6
U	~	DSS2
V	~	DST
W	~	IOPST
X	~	DSO
Y	~	IOPLS
Z	~	SEL0
a	~	SEL1
b	~	DCHP OUT
c	~	INTP OUT
d	~	DCHM0
e	~	DCHM1
f	~	INTR
h	~	DCHO
j	~	DCHR
k	~	DCHI
l	~	OVFLO
m	~	ROENB
n	~	DATA7
p	~	DATA14
r	~	DATA5
s	~	DATA11
t	~	DATA12
u	~	DATA6
v	~	DATA4
w	~	DATA0
x	~	DATA9
y	~	DATA15
z	~	DATA1
AA	~	DATA15
AB	~	DATA3
AC	~	DATA0
AD	~	DATA2
AE	~	DATA8
AF	~	GND

QTY	DESCRIPTION	PART NO
1	DATA GENERAL CORPORATION SOUTHBORO MASSACHUSETTS 01772	
1	TITLE NOVA 2/4 RACK INSTALLATION	
1	SIZE D 010	DRAWING NUMBER 000044
1	REV 01	

THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION

REVISIONS				
REV	DESCRIPTION	DRFTG	APP BY	DATE
01	ECO 8948 Item 83	TTT		MM/TT
02	PER ECO 2498	RASmith	J	C-7-4 M

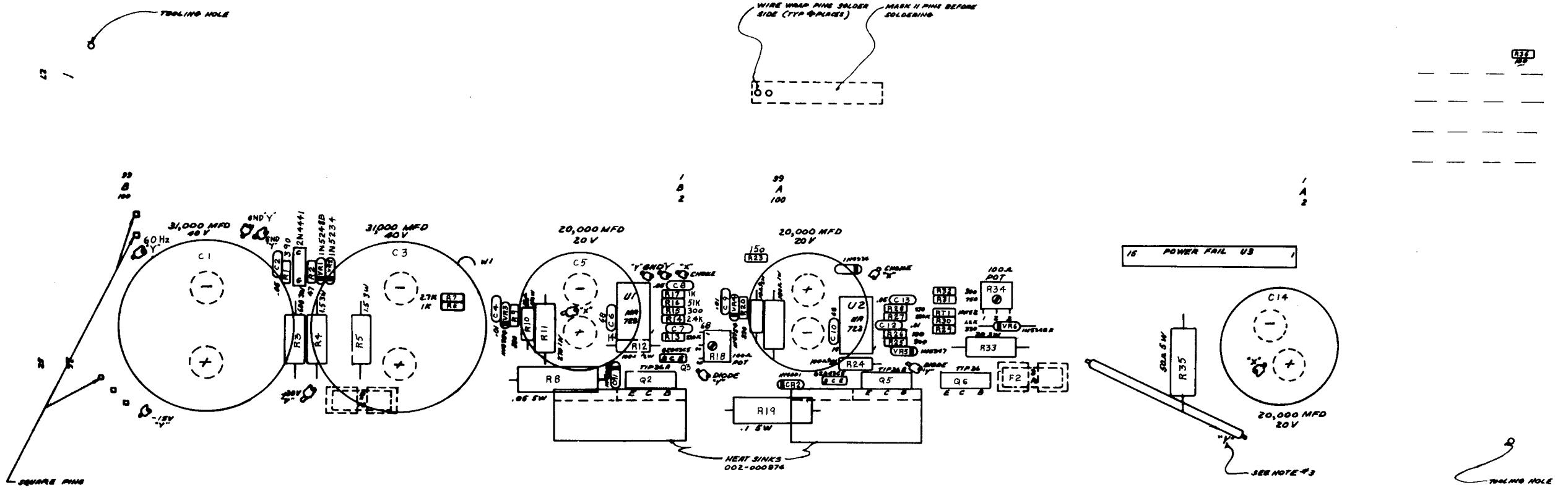


ITEM	QTY	DESCRIPTION	PART NO
26	1	TOP COVER	002 000445
	8	SCREW, FH, 4-40 x 1/4	106 000128
25	1	STRAIN RELIEF SUPPORT	123 000053
	1	SEMS SCREW, 10-32 x 1/2	106 000108
24	1	FAN	115 000002
	2	SCREW, 6-32 x 1/2	106 000345
23	1	GROMMET	123 000015
	1	6 FT POWER CABLE	109 000149
22	1	FUSE HOLDER	113 000018
	1	FUSE, 5 AMP	113 000033
21	1	TRANSFORMER	104 000041
20	1	MAIN FRAME ASSY	005 001904
19	1	DIODE BRIDGE	101 000090
	1	SEMS SCREW, 6-32 x 5/8	106 000346
19a	1	DUAL TERMINAL ADAPTER	111 000273
18	1	PROTECTIVE COVER	003 002122
	2	KEPS NUT, 6-32	106 000256
17	1	RF LINE FILTER	168 000240
	2	KEPS NUT, 6-32	106 000256
16	1	CONSOLE, PCB, ASSY	005 001774
	1	IPL CONSOLE PC	016 000097
	8	SEMS SCREW, 2-56 x 3/16	106 000051
15	1	CONSOLE, MECH. ASSY	005 001902
	4	KEPS NUT, 6-32	106 000256
14	1	MAIN CONSOLE ASSY	005 001901
13	2	CAPACITOR, 31,000 $\mu$ F, 40Vdc	103 000060
12	2	DIODE, IN3899R, 20AMP	101 000065
11	1	CHOKE ASSY	005 001980
11a	1	CHOKE ASSY	005 001531
10	3	CAPACITOR, 20,000 $\mu$ F, 20Vdc	103 000061
9	1	POWER SUPPLY ASSY	005 001894
8	1	POWER FAIL MODULE	005 001895
7	1	LOCKSWITCH ASSY	005 000293
	4	SEMS SCREW, 4-40 x 1/4	106 000060
6	8	CONNECTOR	111 000073
	16	SCREW, BDGH, 4-40 x 3/8	106 000280
5	1	CONNECTOR FRAME	002 000383
4	4	CENTER GUIDE	002 000035
3	1	BACK PANEL/PWR SUP PCB ASSY	005 001893
	1	IPL BACK PANEL/PWR SUP PCB	016 000099
2	1	4-SLOT I/O BRACKET	002 000482
	2	HEX SPACER	002 000538
1	1	MAIN BACK PANEL/POWER SUPPLY ASSY	005 001892

NMCC		ITEM	Q.T.	DESCRIPTION
UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES		DRAWN <i>R. L. G. S. M. H.</i>	7/16/73	<b>DATA GENERAL CORPORATION</b> SOUTHBRO. MASSACHUSETTS 01772 TITLE <b>MECH IPL</b> <b>NOVA 2/4</b>
= XXX = =		CHECKED		
PARTS TO BE FREE FROM BURRS; BREAK ALL EDGES 0.10		ENGINEER		
UNLESS OTHERWISE SPECIFIED ALL MACH SURFACES ✓		APPROVED <i>R. L. G. S. M. H.</i>		
MATERIAL		FIRST USED ON <i>1 / 14</i>		
FINISH		SCALE	D	SIZE CODE DRAWING NUMBER REV
✓		✓	016	000091 02

003-000329-04  
107-000329-01

D



UNLESS OTHERWISE SPECIFIED  
1. ALL RESISTORS ARE 1/4W  
2. THERMISTOR EX @ 50°C  
3. TERMINAL TRIMMED  
3 X INDICATES "AB AWG (SOLID)  
AND Y INDICATES "AB AWG STRANDED

NOVA 2/4 BACK PANEL  
003-000329-01

B

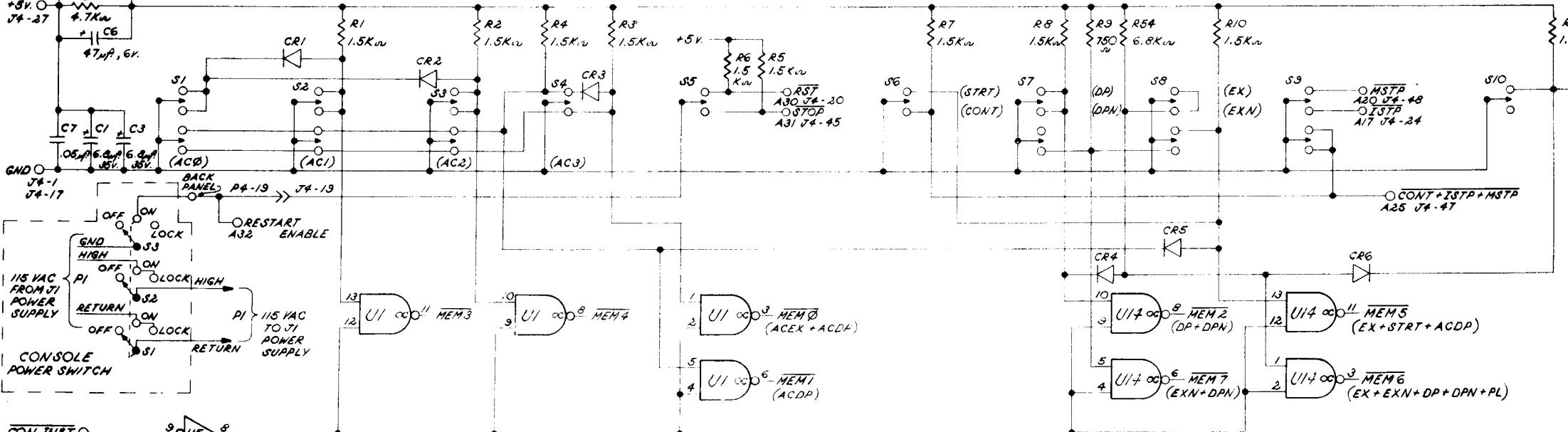
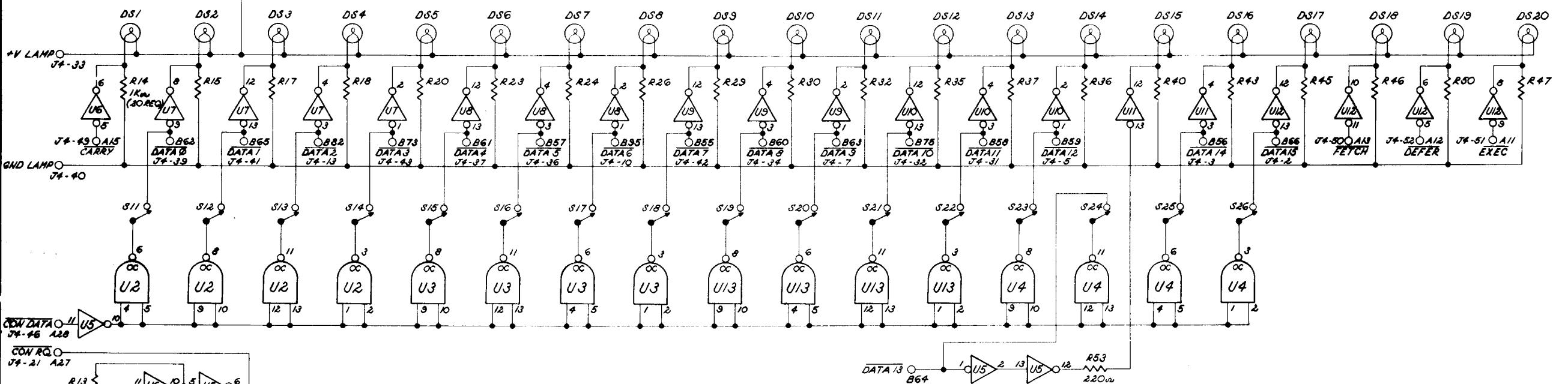
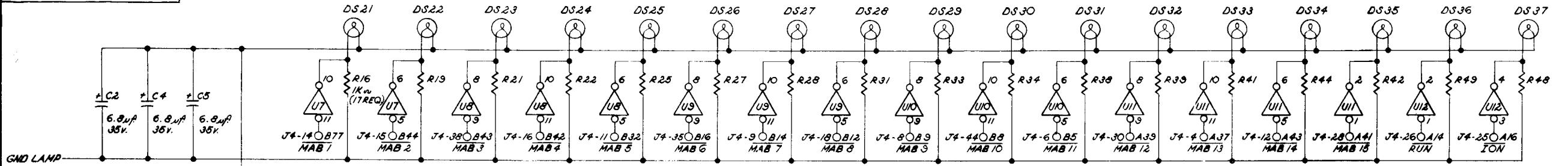
A

OTY	DESCRIPTION	PART NO
DRAWN		
CHECKED		
ENGINEER		
APPROVED		
MATERIAL		
FINISH		
SCALE		
SIZE	CODE	DRAWING NUMBER
D	016	000099
REV		
01		

**DATA GENERAL CORPORATION**  
SOUTHBORO, MASSACHUSETTS 01772  
**NOVA 2/4 BACK PANEL & POWER SUPPLY**

THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

REVISIONS			
DESCRIPTION	DRFTG.	APP. BY	DATE
ECO # 1949	R.W.C.		
ECO # 2303	R.W.C.		

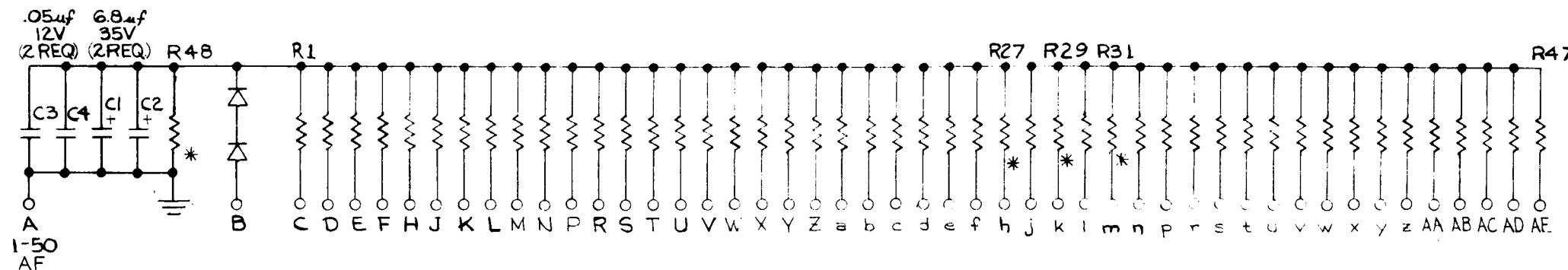


NHCC		QTY.	DESCRIPTION	PART NO.
UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES		DRAWN <i>R. W. C. Jr.</i>	4-13 73	<b>DATA GENERAL CORPORATION</b> SOUTHBORO, MASSACHUSETTS 01772
± XXX = ± PARTS TO BE FREE FROM BURRS BREAK ALL EDGES 0.0		CHECKED		TITLE
UNLESS OTHERWISE SPECIFIED— ALL MACH SURFACES ✓		ENGINEER 1973		UNIVERSAL CONSOLE
MATERIAL		APPROVED		
FINISH		FIRST USED ON N2 107-000311	SIZE D SCALE	CODE 001 DRAWING NUMBER 000448 REV. QB

THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION

## REVISIONS

REV.	DESCRIPTION	DRFTG	APP BY	DATE
O1	ECO # 1949	R.W.C.		8-27-73
O2	ECO # 2751	D.L.F.	ZKw	8-1-74



ALL DIODES IN4001  
ALL RESISTORS 180Ω 1/4W 5% UNLESS OTHERWISE NOTED  
\* 100Ω 1/4W 5% (R27, R29, R31, R48)

BRUNING 40-556 20411

QTY	DESCRIPTION		PART NO
DRAWN Dennis E.	S. 1-64	<b>DATA GENERAL CORPORATION</b> SOUTHBROOK, MASSACHUSETTS 01772	
CHECKED R.F.J.	8-1-74		
ENGINEER D.L.F.	5-27-73		
APPROVED			
UNLESS OTHERWISE SPECIFIED USE D.G.C. SPECIFICATION 108- 000028 FOR TOLER- ANCES AND WORKMAN- SHIP REQUIREMENTS	TITLE  NOVA 2 CABLE TERMINATOR		
MATERIAL			
FIRST USED ON N2 003000306 207000214			
FINISH			
SCALE	SIZE C	CODE 001	DRAWING NUMBER 000452
	REV 02		

## WIRE LIST

PAGE 1 OF 1

# **DATA GENERAL CORPORATION**

SOUTHBORO, MASSACHUSETTS 01772

## WIRE LIST

PAGE 1 OF 2

# **DATA GENERAL CORPORATION**

SOUTHBORO, MASSACHUSETTS 01772

REVISIONS				
REV	DESCRIPTION	DRFTG.	APP. BY	DATE

11. TERMINATE ONE END OF 109000074 WITH 111000188 AND 111000059 4 PLACES. CUT CABLE TO 24 1/2 INCHES.
12. CUT INSIDE WHITE AND INSIDE RED TO 21 INCHES.
13. SPLIC OUTSIDE WHITE USING 111000056 TO WHITE LEG OF LINE CORD (LC-1).
14. SOLDER OUTSIDE RED WIRE TO SIDE TERMINAL OF FUSEHOLDER (F1-2).
15. TERMINATE INSIDE WHITE WIRE WITH 111000034 TERMINAL - & SHRINK TUBING AND CONNECT TO LFC1-2.
16. TERMINATE INSIDE RED WIRE WITH 111000034 TERMINAL - & SHRINK TUBING AND CONNECT TO LFC2-2.
17. CONNECT OUTSIDE WHITE TO S1-1.
18. CONNECT INSIDE WHITE TO S1-2.
19. CONNECT OUTSIDE RED TO S2-1.
20. CONNECT INSIDE RED TO S2-2.

QTY	DESCRIPTION		PART NO.
DRAWN			DATA GENERAL CORPORATION
CHECKED			SOUTHBROOK, MASSACHUSETTS 01772
ENGINEER			TITLE
APPROVED			AC CHASSIS WIRING
FIRST USED ON			
MATERIAL	SIZE	CODE	DRAWING NUMBER
FINISH	A	000	000 491
SCALE			01

## WIRE LIST

PAGE 1 OF 1

# **DATA GENERAL CORPORATION**

SOUTHBORO, MASSACHUSETTS 01772

SIZE A	CODE 008	DWG NO. 000482	REV 00
-----------	-------------	-------------------	-----------

**TITLE**

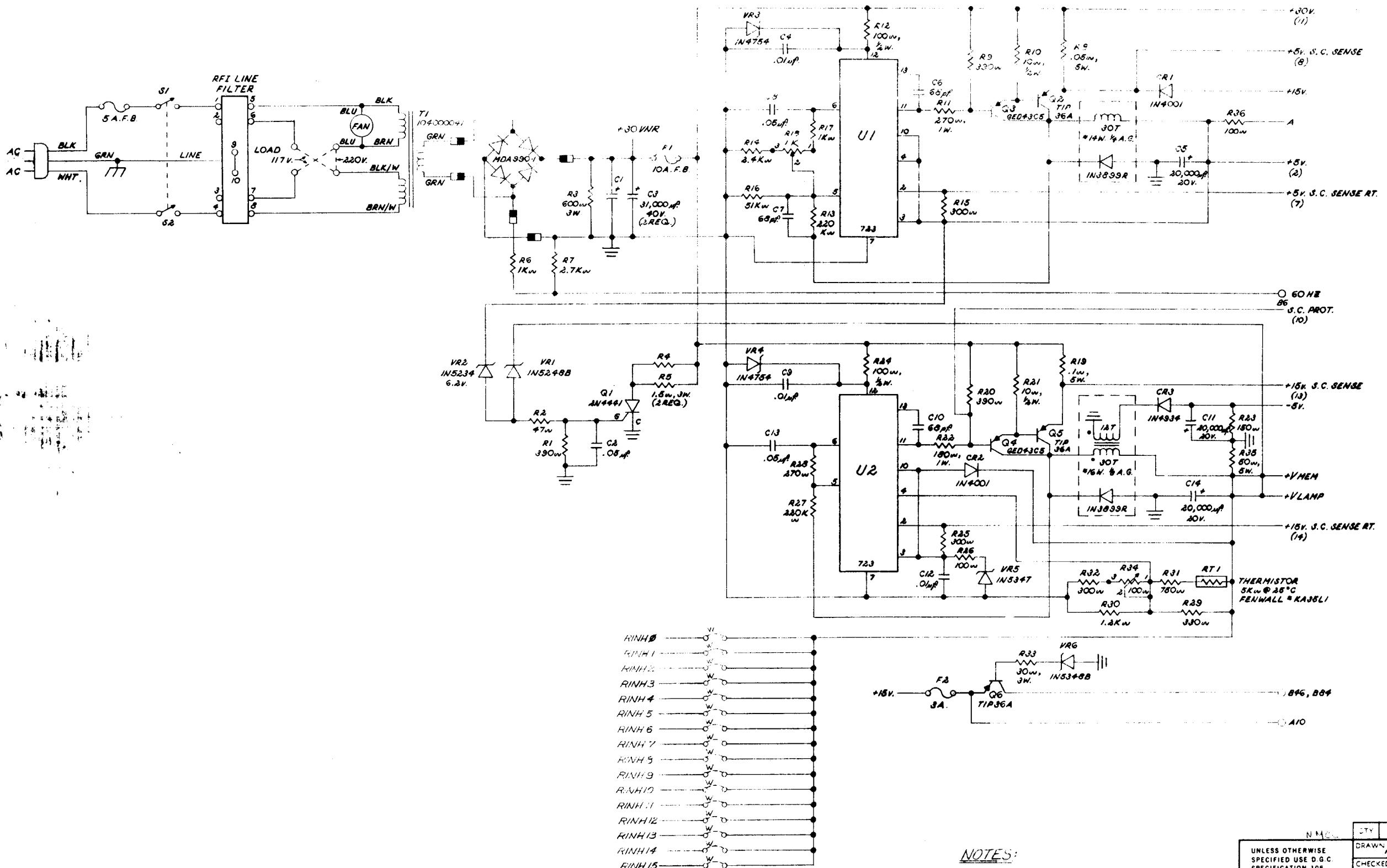
WL NOVA 2/4  
BK PNL/PWR SUP

1. TERMINATE IN 111000033
  2. TAG AND LEAVE  
DISCONNECTED.
  3. CHOKE LEADS 005001531
  4. CHOKE LEADS 005001980

ITEM NO.	FROM	SIGNAL NAME TERMINATION	TO	FUNCTION TERMINATION	COLOR	LENGTH	AWG
1	BP-3	STRIP & TIN	D1-2	NOTES 1 & 2	GRN	24	18
2	BP-6	STRIP & TIN	D1-1	NOTES 1 & 2	BLK	24	18
3	BP-7	STRIP & TIN	D1-3	NOTES 1 & 2	RED	24	18
4	CR1-1	STRIP & TIN	BP-14	STRIP & TIN	GRN	9	18
5	CR2-1	STRIP & TIN	BP-12	STRIP & TIN	RED	8	18
6	GRD-4	NOTE 1	BP-9	STRIP & TIN	BLK	6	18
7	GRD-2	NOTE 1	BP-10	STRIP & TIN	BLK	6	18
8	L1-1	NOTE 3	CR2-1	STRIP	RED	4	--
9	L1-2	NOTE 3	BP-8	STRIP	RED	14 1/2	--
10	L2-A	NOTE 4	CR1-1	STRIP	GRN	7	--
11	L2-B	NOTE 4	BP-15	STRIP	GRN	11	--
12	L2-C	NOTE 4	BP-11	STRIP	CLR	11	--
13	L2-D	NOTE 4	BP-13	STRIP	CLR	8 1/4	--

THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION

REVISIONS				
REV	DESCRIPTION	DRFTG	APP BY	DATE
O1	ECO # 2278	R.W.C.		7-27-80
O2	ECO # 2451	R.W.C.		7-27-80
O3	ECO # 2448	R.W.C.		7-27-80
O4	ECO # 2724	D.L.F.		7-29-80
O5	ECO # 2851	RASHA		7-30-80



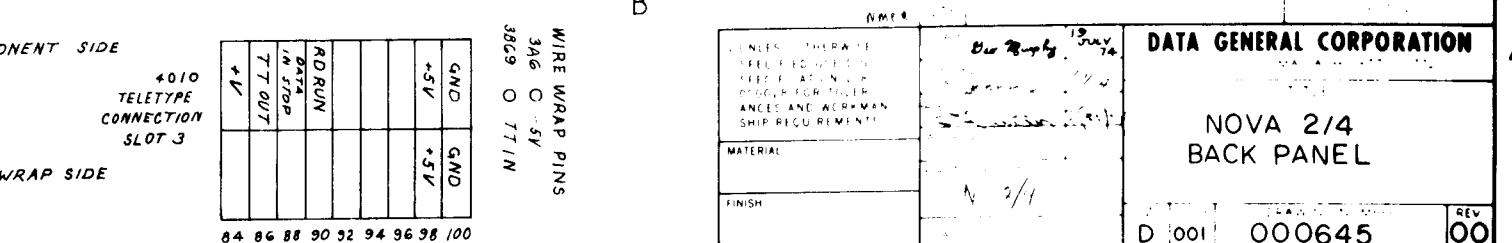
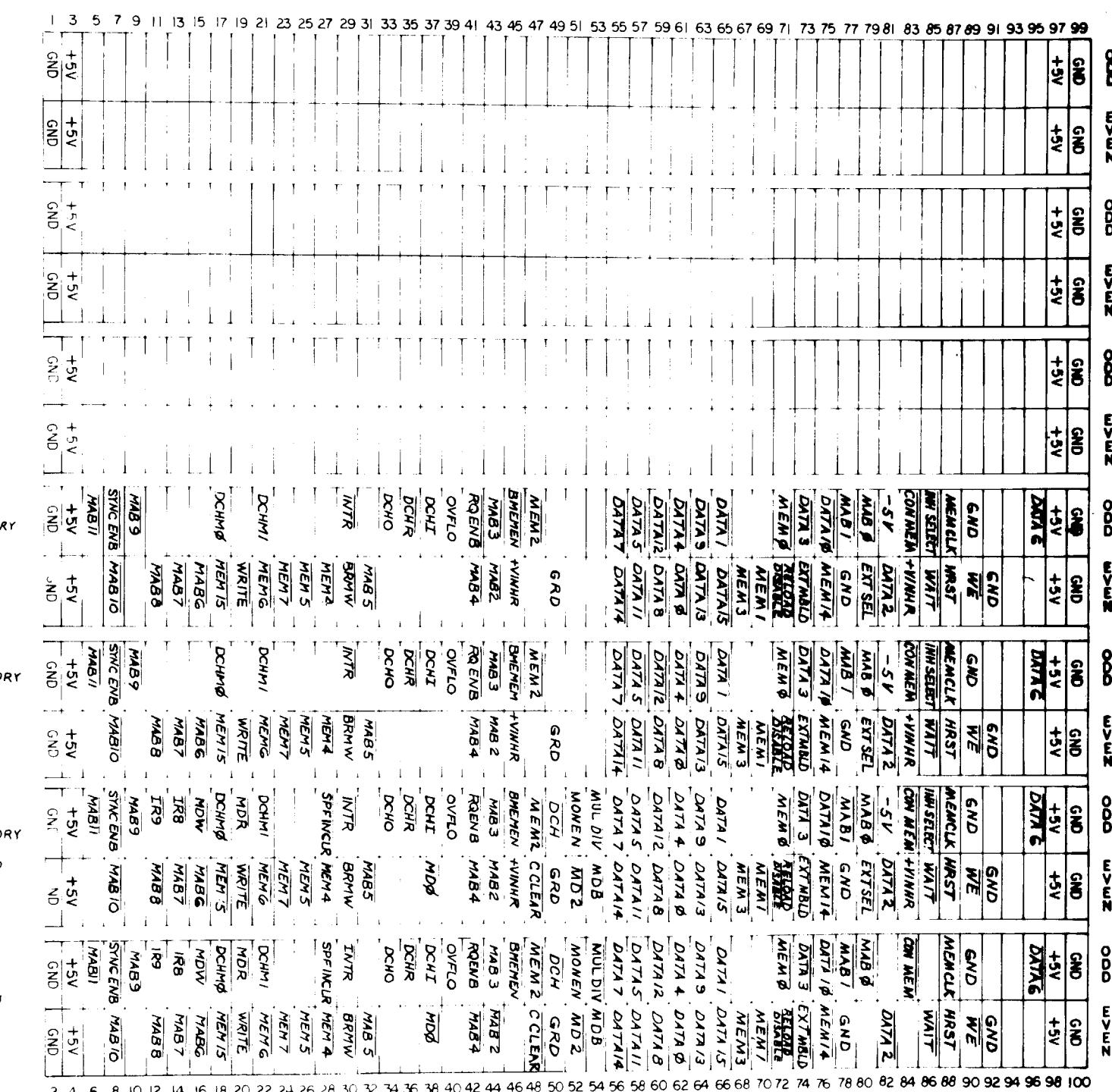
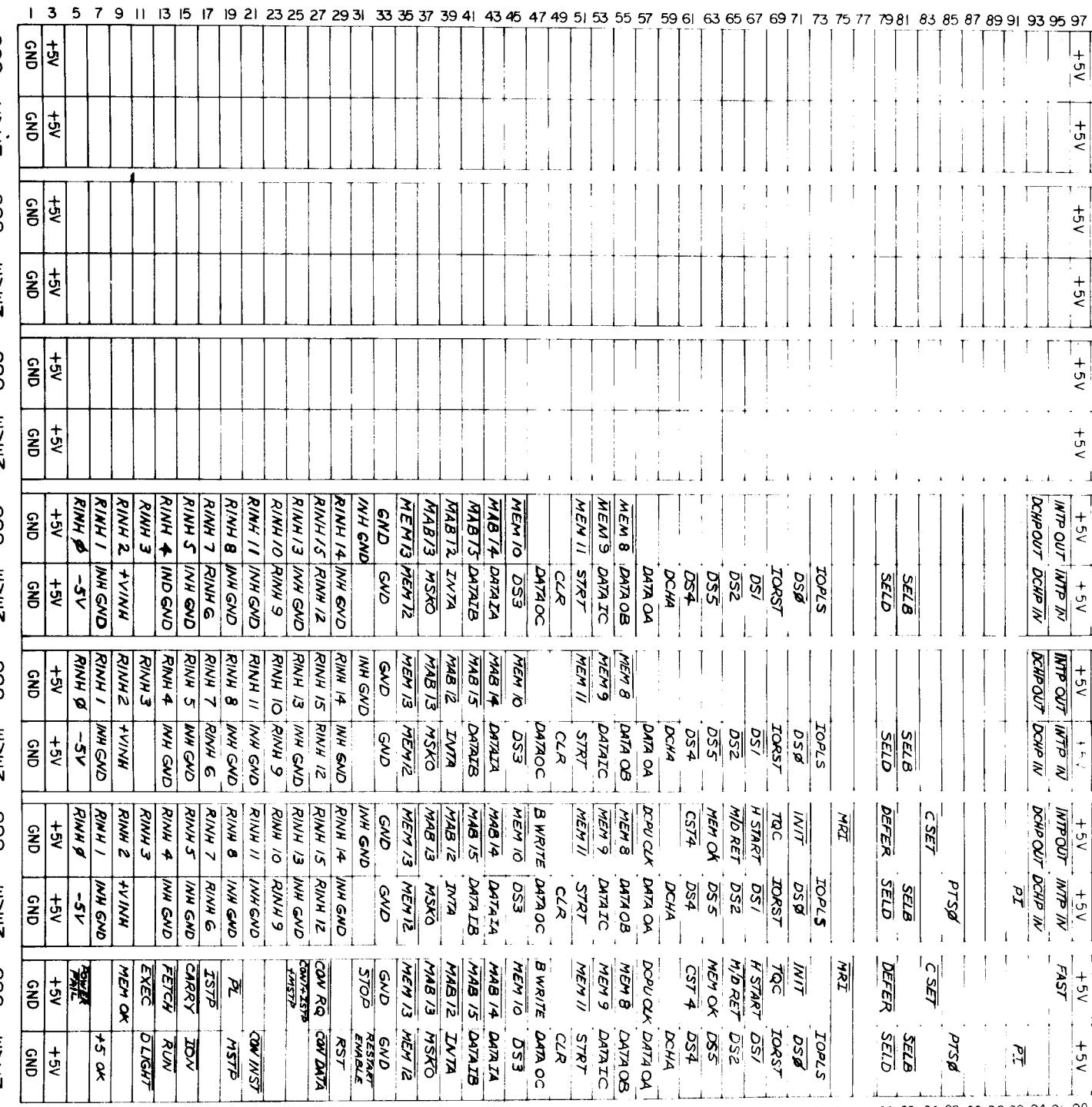
### *NOTE*

4. ■ DEPICTS AN ELECTRICAL CONNECTION  
BLACK END (MALE CONTACT) OTHER END  
(FEMALE CONTACT)
  5. FOR 220 V RMS OPERATION, USE  
3 AMP LINE FUSE

N.M.C.	STY	DESCR PT ON	PART NO
	DRAWN	R.W.C., JR.	9-19 73
UNLESS OTHERWISE SPECIFIED USE D.G.C. SPECIFICATION 108- 000028 FOR TOLER- ANCES AND WORKMAN- SHIP REQUIREMENTS	CHECKED		TITLE
MATERIAL	ENGINEERED		<b>NOVA 2/4</b> <b>BACK PANEL</b> <b>POWER SUPPLY</b>
FINISH	FIRST USED ON	N.L.	
	SCALE	SIZE	DRAWING NUMBER
		D	000560
			REV. 05

THIS DRAWING AND SPECIFICATIONS HEREIN ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR TRADE OF ITEMS WITHOUT WRITTEN PERMISSION.

REF ID: A6274



		IN-OUT BUS SIGNAL CONNECTIONS																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
A	B	C	D	E	F	H	J	K	L	M	N	P	R	S	T	U	V	W	X	Y	Z	a	b	c	d	e	f	g	h	i	j	k	l	m	n	p	q	r	s	t	u	v	w	x	y	z	AA	AB	AC	AD	AE																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																												
GND	+5V	M510	I17A	A40	A42	A44	DAT1B	DAT1A	J17A	DAT1A	DAT1B	DS3	DS4	DS5	DS6	DS7	DS8	DS9	DS10	DS11	DS12	SELB	SELD	ABO	ABP	AT4	AT5	AT6	AT7	AT8	AT9	AT10	AT11	AT12	AT13	AT14	AT15	AT16	AT17	AT18	AT19	AT20	AT21	AT22	AT23	AT24	AT25	AT26	AT27	AT28	AT29	AT30	AT31	AT32	AT33	AT34	AT35	AT36	AT37	AT38	AT39	AT40	AT41	AT42	AT43	AT44	AT45	AT46	AT47	AT48	AT49	AT50	AT51	AT52	AT53	AT54	AT55	AT56	AT57	AT58	AT59	AT60	AT61	AT62	AT63	AT64	AT65	AT66	AT67	AT68	AT69	AT70	AT71	AT72	AT73	AT74	AT75	AT76	AT77	AT78	AT79	AT80	AT81	AT82	AT83	AT84	AT85	AT86	AT87	AT88	AT89	AT90	AT91	AT92	AT93	AT94	AT95	AT96	AT97	AT98	AT99	AT100	AT101	AT102	AT103	AT104	AT105	AT106	AT107	AT108	AT109	AT110	AT111	AT112	AT113	AT114	AT115	AT116	AT117	AT118	AT119	AT120	AT121	AT122	AT123	AT124	AT125	AT126	AT127	AT128	AT129	AT130	AT131	AT132	AT133	AT134	AT135	AT136	AT137	AT138	AT139	AT140	AT141	AT142	AT143	AT144	AT145	AT146	AT147	AT148	AT149	AT150	AT151	AT152	AT153	AT154	AT155	AT156	AT157	AT158	AT159	AT160	AT161	AT162	AT163	AT164	AT165	AT166	AT167	AT168	AT169	AT170	AT171	AT172	AT173	AT174	AT175	AT176	AT177	AT178	AT179	AT180	AT181	AT182	AT183	AT184	AT185	AT186	AT187	AT188	AT189	AT190	AT191	AT192	AT193	AT194	AT195	AT196	AT197	AT198	AT199	AT200	AT201	AT202	AT203	AT204	AT205	AT206	AT207	AT208	AT209	AT210	AT211	AT212	AT213	AT214	AT215	AT216	AT217	AT218	AT219	AT220	AT221	AT222	AT223	AT224	AT225	AT226	AT227	AT228	AT229	AT230	AT231	AT232	AT233	AT234	AT235	AT236	AT237	AT238	AT239	AT240	AT241	AT242	AT243	AT244	AT245	AT246	AT247	AT248	AT249	AT250	AT251	AT252	AT253	AT254	AT255	AT256	AT257	AT258	AT259	AT260	AT261	AT262	AT263	AT264	AT265	AT266	AT267	AT268	AT269	AT270	AT271	AT272	AT273	AT274	AT275	AT276	AT277	AT278	AT279	AT280	AT281	AT282	AT283	AT284	AT285	AT286	AT287	AT288	AT289	AT290	AT291	AT292	AT293	AT294	AT295	AT296	AT297	AT298	AT299	AT300	AT301	AT302	AT303	AT304	AT305	AT306	AT307	AT308	AT309	AT310	AT311	AT312	AT313	AT314	AT315	AT316	AT317	AT318	AT319	AT320	AT321	AT322	AT323	AT324	AT325	AT326	AT327	AT328	AT329	AT330	AT331	AT332	AT333	AT334	AT335	AT336	AT337	AT338	AT339	AT340	AT341	AT342	AT343	AT344	AT345	AT346	AT347	AT348	AT349	AT350	AT351	AT352	AT353	AT354	AT355	AT356	AT357	AT358	AT359	AT360	AT361	AT362	AT363	AT364	AT365	AT366	AT367	AT368	AT369	AT370	AT371	AT372	AT373	AT374	AT375	AT376	AT377	AT378	AT379	AT380	AT381	AT382	AT383	AT384	AT385	AT386	AT387	AT388	AT389	AT390	AT391	AT392	AT393	AT394	AT395	AT396	AT397	AT398	AT399	AT400	AT401	AT402	AT403	AT404	AT405	AT406	AT407	AT408	AT409	AT410	AT411	AT412	AT413	AT414	AT415	AT416	AT417	AT418	AT419	AT420	AT421	AT422	AT423	AT424	AT425	AT426	AT427	AT428	AT429	AT430	AT431	AT432	AT433	AT434	AT435	AT436	AT437	AT438	AT439	AT440	AT441	AT442	AT443	AT444	AT445	AT446	AT447	AT448	AT449	AT450	AT451	AT452	AT453	AT454	AT455	AT456	AT457	AT458	AT459	AT460	AT461	AT462	AT463	AT464	AT465	AT466	AT467	AT468	AT469	AT470	AT471	AT472	AT473	AT474	AT475	AT476	AT477	AT478	AT479	AT480	AT481	AT482	AT483	AT484	AT485	AT486	AT487	AT488	AT489	AT490	AT491	AT492	AT493	AT494	AT495	AT496	AT497	AT498	AT499	AT500	AT501	AT502	AT503	AT504	AT505	AT506	AT507	AT508	AT509	AT510	AT511	AT512	AT513	AT514	AT515	AT516	AT517	AT518	AT519	AT520	AT521	AT522	AT523	AT524	AT525	AT526	AT527	AT528	AT529	AT530	AT531	AT532	AT533	AT534	AT535	AT536	AT537	AT538	AT539	AT540	AT541	AT542	AT543	AT544	AT545	AT546	AT547	AT548	AT549	AT550	AT551	AT552	AT553	AT554	AT555	AT556	AT557	AT558	AT559	AT560	AT561	AT562	AT563	AT564	AT565	AT566	AT567	AT568	AT569	AT570	AT571	AT572	AT573	AT574	AT575	AT576	AT577	AT578	AT579	AT580	AT581	AT582	AT583	AT584	AT585	AT586	AT587	AT588	AT589	AT590	AT591	AT592	AT593	AT594	AT595	AT596	AT597	AT598	AT599	AT600	AT601	AT602	AT603	AT604	AT605	AT606	AT607	AT608	AT609	AT610	AT611	AT612	AT613	AT614	AT615	AT616	AT617	AT618	AT619	AT620	AT621	AT622	AT623	AT624	AT625	AT626	AT627	AT628	AT629	AT630	AT631	AT632	AT633	AT634	AT635	AT636	AT637	AT638	AT639	AT640	AT641	AT642	AT643	AT644	AT645	AT646	AT647	AT648	AT649	AT650	AT651	AT652	AT653	AT654	AT655	AT656	AT657	AT658	AT659	AT660	AT661	AT662	AT663	AT664	AT665	AT666	AT667	AT668	AT669	AT670	AT671	AT672	AT673	AT674	AT675	AT676	AT677	AT678	AT679	AT680	AT681	AT682	AT683	AT684	AT685	AT686	AT687	AT688	AT689	AT690	AT691	AT692	AT693	AT694	AT695	AT696	AT697	AT698	AT699	AT700	AT701	AT702	AT703	AT704	AT705	AT706	AT707	AT708	AT709	AT710	AT711	AT712	AT713	AT714	AT715	AT716	AT717	AT718	AT719	AT720	AT721	AT722	AT723	AT724	AT725	AT726	AT727	AT728	AT729	AT730	AT731	AT732	AT733	AT734	AT735	AT736	AT737	AT738	AT739	AT740	AT741	AT742	AT743	AT744	AT745	AT746	AT747	AT748	AT749	AT750	AT751	AT752	AT753	AT754	AT755	AT756	AT757	AT758	AT759	AT760	AT761	AT762	AT763	AT764	AT765	AT766	AT767	AT768	AT769	AT770	AT771	AT772	AT773	AT774	AT775	AT776	AT777	AT778	AT779	AT780	AT781	AT782	AT783	AT784	AT785	AT786	AT787	AT788	AT789	AT790	AT791	AT792	AT793	AT794	AT795	AT796	AT797	AT798	AT799	AT800	AT801	AT802	AT803	AT804	AT805	AT806	AT807	AT808	AT809	AT810	AT811	AT812	AT813	AT814	AT815	AT816	AT817	AT818	AT819	AT820	AT821	AT822	AT823	AT824	AT825	AT826	AT827	AT828	AT829	AT830	AT831	AT832	AT833	AT834	AT835	AT836	AT837	AT838	AT839	AT840	AT841	AT842	AT843	AT844	AT845	AT846	AT847	AT848	AT849	AT850	AT851	AT852	AT853	AT854	AT855	AT856	AT857	AT858	AT859	AT860	AT861	AT862	AT863	AT864	AT865	AT866	AT867	AT868	AT869	AT870	AT871	AT872	AT873	AT874	AT875	AT876	AT877	AT878	AT879	AT880	AT881	AT882	AT883	AT884	AT885	AT886	AT887	AT888	AT889	AT890	AT891	AT892	AT893	AT894	AT895	AT896	AT897	AT898	AT899	AT900	AT901	AT902	AT903	AT904	AT905	AT906	AT907	AT908	AT909	AT910	AT911	AT912	AT913	AT914	AT915	AT916	AT917	AT918	AT919	AT920	AT921	AT922	AT923	AT924	AT925	AT926	AT927	AT928	AT929	AT930	AT931	AT932	AT933	AT934	AT935	AT936	AT937	AT938	AT939	AT940	AT941	AT942	AT943	AT944	AT945	AT946	AT947	AT948	AT949	AT950	AT951	AT952	AT953	AT954	AT955	AT956	AT957	AT958	AT959	AT960	AT961	AT962	AT963	AT964	AT965	AT966	AT967	AT968	AT969	AT970	AT971	AT972	AT973	AT974	AT975	AT976	AT977	AT978	AT979	AT980	AT981	AT982	AT983	AT984	AT985	AT986	AT987	AT988	AT989	AT990	AT991	AT992	AT993	AT994	AT995	AT996	AT997	AT998	AT999	AT1000	AT1001	AT1002	AT1003	AT1004	AT1005	AT1006	AT1007	AT1008	AT1009	AT1010	AT1011	AT1012	AT1013	AT1014	AT1015	AT1016	AT1017	AT1018	AT1019	AT1020	AT1021	AT1022	AT1023	AT1024	AT1025	AT1026	AT1027	AT1028	AT1029	AT1030	AT1031	AT1032	AT1033	AT1034	AT1035	AT1036	AT1037	AT1038	AT1039	AT1040	AT1041	AT1042	AT1043	AT1044	AT1045	AT1046	AT1047	AT1048	AT1049	AT1050	AT1051	AT1052	AT1053	AT1054	AT1055	AT1056	AT1057	AT1058	AT1059	AT1060	AT1061	AT1062	AT1063	AT1064	AT1065	AT1066	AT1067	AT1068	AT1069	AT1070	AT1071	AT1072	AT1073	AT1074	AT1075	AT1076	AT1077	AT1078	AT1079	AT1080	AT1081	AT1082	AT1083	AT1084	AT1085	AT1086	AT1087	AT1088	AT1089	AT1090	AT1091	AT1092	AT1093	AT1094	AT1095	AT1096	AT1097	AT1098	AT1099	AT1100	AT1101	AT1102	AT1103	AT1104	AT1105	AT1106	AT1107	AT1108	AT1109	AT1110	AT1111	AT1112	AT1113	AT1114	AT1115	AT1116	AT1117	AT1118	AT1119	AT1120	AT1121	AT1122	AT1123	AT1124	AT1125	AT1126	AT1127	AT1128	AT1129	AT1130	AT1131	AT1132	AT1133	AT1134	AT1135	AT1136	AT1137	AT1138	AT1139	AT1140	AT1141	AT1142	AT1143	AT1144	AT1145	AT1146	AT1147	AT1148	AT1149	AT1150	AT1151	AT1152	AT1153	AT1154	AT1155	AT1156	AT1157	AT1158	AT1159	AT1160	AT1161	AT1162	AT1163	AT1164	AT1165	AT1166	AT1167	AT1168	AT1169	AT1170	AT1171	AT1172	AT1173	AT1174	AT1175	AT1176	AT1177	AT1178	AT1179	AT1180	AT1181	AT1182	AT1183	AT1184	AT1185	AT1186	AT1187	AT1188	AT1189	AT1190	AT1191	AT1192	AT1193	AT1194	AT1195	AT1196	AT1197	AT1198	AT1199	AT1200	AT1201	AT1202	AT1203	AT1204	AT1205	AT1206	AT1207	AT1208	AT1209	AT1210	AT1211	AT1212	AT1213	AT1214	AT1215	AT1216	AT1217	AT1218	AT1219	AT1220	AT1221	AT1222	AT1223	AT1224	AT1225</

**COMPONENT SIDE**

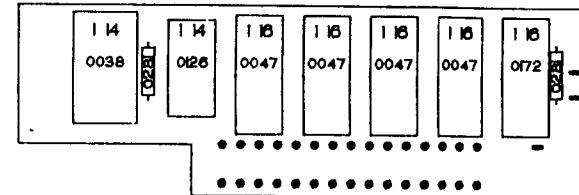
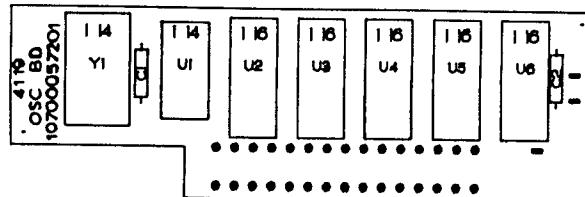
**DATA GENERAL CORPORATION**  
MA 4-1000  
**NOVA 2/4**  
**BACK PANEL**

4

TRADE NAME AND SPECIFICATION NUMBER ARE FOR  
REFERENCE ONLY. DATA GENERAL CORP. RETAINS THE  
RIGHT TO COPY OR REPRODUCE IN WHOLE OR  
PART OR THE PARTS FOR MANUFACTURE OR SALE OF  
ITEMS LISTED WITHOUT WRITTEN PERMISSION.

003000572-01  
107000572-01

REVISIONS				
REV.	DESCRIPTION	DRFTG	APP BY	DATE
00	RLSD PER ECO-3852	-	-	116175



QTY	DESCRIPTION		PART NO
DRAWN	ADS		<b>DATA GENERAL CORPORATION</b> SOUTHBORO, MASSACHUSETTS 01772
CHECKED	R. Johnson	11-6-75	
ENGINEER			
APPROVED		11/6	
MATERIAL		FIRST USED ON	4/19
FINISH		005006462	
SCALE	B	SIZE	CODE
			DRAWING NUMBER
			016000252
			REV
			00

PIN #	IR1		PL		IR7		IR5		IR0		CONDATA		3		2REN		2WRADR		IWAR		2WEN		F34	
	14	13	12	11	10	1	2	Y0	Y1	Y2	Y3	Y4	Y5	Y6	Y7	OCTAL CONTENTS	6	7	9	6	7	9		
00	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	12								
01	0	0	0	0	1	0	0	0	0	0	0	1	0	1	0	12								
02	0	0	0	1	0	0	0	0	0	0	0	1	0	1	0	12								
03	0	0	0	1	1	0	0	0	0	0	0	1	0	1	0	12								
04	0	0	1	0	0	0	0	1	1	-	0	1	1	0	146									
05	0	0	1	0	1	0	0	0	0	0	0	1	0	1	0	12								
06	0	0	1	1	0	0	0	0	0	0	0	1	0	1	0	12								
07	0	0	1	1	1	0	0	0	0	0	0	1	0	1	0	12								
10	0	1	0	0	0	0	0	0	0	0	0	1	0	1	0	12								
11	0	1	0	0	1	0	0	0	0	0	0	1	0	1	0	12								
12	0	1	0	1	0	0	0	0	0	0	0	1	0	1	0	12								
13	0	1	0	1	1	0	0	0	0	0	0	1	0	1	0	12								
14	0	1	1	0	0	0	0	0	0	0	0	1	0	1	0	12								
15	0	1	1	0	1	0	0	0	0	0	0	1	0	1	0	12								
16	0	1	1	1	0	0	0	0	0	0	0	1	0	1	0	12								
17	0	1	1	1	1	0	0	0	0	0	0	1	0	1	0	12								
20	1	0	0	0	0	0	0	0	0	0	0	1	0	1	0	12								
21	1	0	0	0	1	0	0	0	0	0	0	1	0	1	0	12								
22	1	0	0	1	0	0	0	0	0	0	0	1	0	1	0	12								
23	1	0	0	1	1	1	0	0	0	0	1	1	0	1	1	215								
24	1	0	1	0	0	0	0	0	0	0	0	1	0	1	0	12								
25	1	0	1	0	1	0	0	1	1	0	0	0	0	1	1	143								
26	1	0	1	1	0	0	0	0	0	0	0	1	0	1	0	12								
27	1	0	1	1	1	0	0	0	0	0	0	1	0	1	0	12								
30	1	1	0	0	0	0	0	0	0	0	0	1	0	1	0	12								
31	1	1	0	0	1	0	0	0	0	0	0	1	0	1	0	12								
32	1	1	0	1	0	0	0	0	0	0	0	1	0	1	0	12								
33	1	1	0	1	1	0	0	0	0	0	0	1	0	1	0	12								
34	1	1	1	0	0	0	0	0	0	0	0	1	0	1	0	12								
35	1	1	1	0	1	0	0	0	0	0	0	1	0	1	0	12								
36	1	1	1	1	0	0	0	0	0	0	0	1	0	1	0	12								
37	1	1	1	1	1	1	0	0	0	0	0	1	1	0	1	215								

- NOTES:
- 1) With chip disabled ( $C_S=1$ ), outputs are high level.
  - 2) The map is specified with pos. logic output (i.e. 1=High)
  - 3) Least significant bit of contents is  $Y_7$ .
  - 4) Stamp DGC part no. or color code per EIA standard color
  - 5) This part is made from 100000140

DRAWN		DATA GENERAL CORPORATION	
CHECKED			
ENGR'D	X. S. Deacon	TITLE	
APPROVED		CONSOLE ROM Ø	
FIRST USED ON		32 X 8 READ ONLY	
N2		MEMORY MAP	
SHEET 1 OF 1	SIZE A	CODE 100000269	REV 00

PIN #	IRG A <sub>4</sub>	IRG A <sub>3</sub>	IRG A <sub>2</sub>	IRG A <sub>1</sub>	IRG A <sub>0</sub>	RESTART Y <sub>0</sub>	PL Y <sub>1</sub>	IRQ+TR Y <sub>2</sub>	CONDAT Y <sub>3</sub>	2REN Y <sub>4</sub>	INIT Y <sub>5</sub>	SETFET Y <sub>6</sub>	PL ROM Y <sub>7</sub>	WRITE XMBLD	F34	REVIEWS						
																REV	DESCRIPTION	DRAFTG	APP BY	DATE		
00	0	0	0	0	0	1	1	0	1	1	1	1	1	1	1	337	C+1+M					
01	0	0	0	0	1	1	1	0	1	1	1	1	1	1	1	337	C+1+M					
02	0	0	0	1	0	1	1	0	1	1	1	1	1	1	1	337	C+1+M					
03	0	0	0	1	1	1	1	0	1	1	1	1	1	1	1	337	C+1+M					
04	0	0	1	0	0	1	1	0	0	1	1	1	1	1	1	317	RESTART					
05	0	0	1	0	1	1	1	0	0	1	1	1	1	1	1	317	RESTART					
06	0	0	1	1	0	1	1	0	0	0	1	1	1	1	1	317	RESTART					
07	0	0	1	1	1	1	1	0	0	1	1	1	1	1	1	317	RESTART					
10	0	1	0	0	0	1	1	1	0	0	1	1	1	1	1	317	RESTART					
11	0	1	0	0	1	0	1	1	1	1	1	0	1	1	1	377	EX, EXN					
12	0	1	0	1	0	1	1	0	0	0	0	0	1	1	1	303	BP, DPN					
13	0	1	0	1	1	1	1	0	0	1	0	1	1	1	1	313	PL					
14	0	1	1	0	0	1	1	0	0	1	1	1	1	1	1	317	Null					
15	0	1	1	0	1	1	1	0	0	0	1	1	1	1	1	317	RESTART					
16	0	1	1	1	0	1	1	1	0	0	1	1	1	1	1	317	RESTART					
17	0	1	1	1	1	1	1	0	0	1	1	1	1	1	1	317	RESTART					
20	1	0	0	0	0	1	1	1	0	1	1	1	1	1	1	337	RESTART					
21	1	0	0	0	1	1	1	0	1	1	1	1	1	1	1	337	C+1+M					
22	1	0	0	1	0	1	1	1	0	1	1	1	1	1	1	337	C+1+M					
23	1	0	0	1	1	1	1	0	1	1	1	1	1	1	1	337	C+1+M					
24	1	0	1	0	0	1	1	0	0	1	1	1	1	1	1	317	C+1+M					
25	1	0	1	0	1	1	1	1	0	0	1	1	1	1	1	317	RESTART					
26	1	0	1	1	0	1	1	1	0	0	1	1	1	1	1	317	RESTART					
27	1	0	1	1	1	1	1	1	0	0	1	1	1	1	1	317	RESTART					
30	1	1	0	0	0	1	1	1	0	0	1	1	1	1	1	317	START					
31	1	1	0	0	1	1	0	1	1	1	1	1	0	0	0	274	ACEX, ACDP					
32	1	1	0	1	0											Null						
33	1	1	0	1	1											Null						
34	1	1	1	0	0											317	RESTART					
35	1	1	1	0	1											317	RESTART					
36	1	1	1	1	0											317	RESTART					
37	1	1	1	1	1											317	RESTART					

- NOTES:
- 1) With chip disabled ( $C_S=1$ ), outputs are high level.
  - 2) The map is specified with pos. logic output (i.e. 1=High)
  - 3) Least significant bit of contents is  $Y_7$ .
  - 4) Stamp DGC part no. or color code per EIA standard color.
  - 5) This part is made from 100000140.

DRAWN	DATA GENERAL CORPORATION		
CHECKED			
SPEC'D BY	TITLE		
APPROVED	CONSOLE ROM 1		
FIRST USED ON	32 X 8 READ ONLY		
MEMORY MAP			
N2		REV	
SHEET 1	X 1	1	00
A	CODE	DRAWING NUMBER	
		100000270	

PIN #	IR1	IR2	IR3	IR4	PTSØ	JSR	2REN	2WRAD	1WEN	2WEN	SET FE	PTR H		
OCTAL ADDRESS	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	Y <sub>0</sub>	Y <sub>1</sub>	Y <sub>2</sub>	Y <sub>3</sub>	Y <sub>4</sub>	Y <sub>5</sub>	Y <sub>6</sub>	Y <sub>7</sub>	OCTAL CONTENTS
00	0	0	0	0	0									
01	0	0	0	0	1									
02	0	0	0	1	0									
03	0	0	0	1	1									
04	0	0	1	0	0									
05	0	0	1	0	1									
06	0	0	1	1	0									
07	0	0	1	1	1									
10	0	1	0	0	0									
11	0	1	0	0	1	1	0	1	1	1	1	0	0	274
12	0	1	0	1	0									
13	0	1	0	1	1	1	0	1	1	1	1	0	0	274
14	0	1	1	0	0									
15	0	1	1	0	1	1	0	1	1	1	1	0	0	274
16	0	1	1	1	0									
17	0	1	1	1	1	1	0	1	1	1	1	0	0	274
20	1	0	0	0	0									
21	1	0	0	0	1	1	1	1	1	0	1	0	0	364
22	1	0	0	1	0									
23	1	0	0	1	1	1	1	1	1	0	1	0	0	364
24	1	0	1	0	0									
25	1	0	1	0	1	1	1	1	1	0	1	0	0	364
26	1	0	1	1	0									
27	1	0	1	1	1	1	1	1	1	0	1	0	0	364
30	1	1	0	0	0	1	0	1	0	1	1	0	1	255
31	1	1	0	0	1	1	1	1	0	1	0	1	1	353
32	1	1	0	1	0	1	0	1	0	1	1	0	1	255
33	1	1	0	1	1	1	1	1	0	1	0	1	1	353
34	1	1	1	0	0									0
35	1	1	1	0	1	0	0	0	1	0	0	0	0	20
36	1	1	1	1	0									0
37	1	1	1	1	1	1	1	0	1	1	0	0	0	330

- NOTES:
- 1) With chip disabled ( $C_S=1$ ), outputs are high level.
  - 2) The map is specified with pos. logic output (i.e. 1=High)
  - 3) Least significant bit of contents is  $Y_7$ .
  - 4) Stamp DGC part no. or color code per EIA standard color.
  - 5) This part is made from 100000140.

STA

LDA

DSZ.TS1  
DSZ.TSØ  
ISZ.TS1  
ISZ.TSØ

JSR

JMP

## MRI ROM Ø

DRAWN		DATA GENERAL CORPORATION	
CHECKED			
<i>J. Sorenson</i>		TITLE	
APPROVED		MRI ROM Ø	
FIRST USED ON	N2	32 X 8 READ ONLY	
SHEET	1	MEMORY MAP	
NO.	1	DRAWING NUMBER	100000271
REV	00	REV	

PIN #	<u>IR1</u>		<u>IR2</u>		<u>IR3</u>		<u>IR4</u>		PINS/		<u>R/W</u>		ALU/CARRY		SET SKIP		WRITE		INCA		<u>F34</u>		DECA		
	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	Y <sub>0</sub>	Y <sub>1</sub>	Y <sub>2</sub>	Y <sub>3</sub>	Y <sub>4</sub>	Y <sub>5</sub>	Y <sub>6</sub>	Y <sub>7</sub>	OCTAL ADDRESS	OCTAL CONTENTS	1	2	3	4	5	6	7	9		
00	0	0	0	0	0																				
01	0	0	0	0	1																				
02	0	0	0	1	0																				
03	0	0	0	1	1																				
04	0	0	1	0	0																				
05	0	0	1	0	1																				
06	0	0	1	1	0																				
07	0	0	1	1	1																				
10	0	1	0	0	0																				
11	0	1	0	0	1	1	1				1	0	1	0	1	325									
12	0	1	0	1	0																				
13	0	1	0	1	1	1	1																		
14	0	1	1	0	0																				
15	0	1	1	0	1	1	1				1	0	1	0	1	325									
16	0	1	1	1	0																				
17	0	1	1	1	1	1	1				1	0	1	0	1	325									
20	1	0	0	0	0																				
21	1	0	0	0	1	1	1				1	1	1	0	1	335									
22	1	0	0	1	0																				
23	1	0	0	1	1	1	1				1	1	1	0	1	335									
24	1	0	1	0	0																				
25	1	0	1	0	1	1	1				1	1	1	0	1	335									
26	1	0	1	1	0																				
27	1	0	1	1	1	1	1				1	1	1	0	1	335									
30	1	1	0	0	0	1	0				1	0	1	1	1	227									
31	1	1	0	0	1	0	1				1	0	1	1	1	116									
32	1	1	0	1	0	1	0				1	0	1	1	1	227									
33	1	1	0	1	1	0	1				0	1	0	1	1	113									
34	1	1	1	0	0																				
35	1	1	1	0	1	1	1				1	1	1	1	1	337									
36	1	1	1	1	0																				
37	1	1	1	1	1	1	1				1	1	1	1	1	337									

- NOTES:
- 1) With chip disabled ( $C_S=1$ ), outputs are high level.
  - 2) The map is specified with pos. logic output (i.e. 1=High)
  - 3) Least significant bit of contents is  $Y_7$ .
  - 4) Stamp DGC part no. or color code per EIA standard color.
  - 5) This part is made from 100000140.

DRAWN		DATA GENERAL CORPORATION	
CHECKED			
ENGINEER		TITLE	
APPROVED		MRI ROM 1	
DATE ISSUED ON		32 X 8 READ ONLY	
		MEMORY MAP	
		N2	
SHEET	1	SIZE	CODE
		DRAWING NUMBER	
		100000272	
		REV	00

PIN #	14	13	12	11	10	PTS0	CLEAR SKIP	LOAD1	IWEN	ZWEN	ZWRAD	2REN	PTG H	
OCTAL ADDRESS	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	Y <sub>0</sub>	Y <sub>1</sub>	Y <sub>2</sub>	Y <sub>3</sub>	Y <sub>4</sub>	Y <sub>5</sub>	Y <sub>6</sub>	Y <sub>7</sub>	OCTAL CONTENTS
00	0	0	0	0	0									
01	0	0	0	0	1									
02	0	0	0	1	0									
03	0	0	0	1	1									
04	0	0	1	0	0	1	0	1	0	1	1	1	1	257
05	0	0	1	0	1	1	1	1	1	1	0	0	1	371
06	0	0	1	1	0									
07	0	0	1	1	1	0	1	1	0	1	0	0	0	150
10	0	1	0	0	0									
11	0	1	0	0	1									
12	0	1	0	1	0									
13	0	1	0	1	1									
14	0	1	1	0	0									
15	0	1	1	0	1									
16	0	1	1	1	0									
17	0	1	1	1	1									
20	1	0	0	0	0									
21	1	0	0	0	1									
22	1	0	0	1	0									
23	1	0	0	1	1									
24	1	0	1	0	0									
25	1	0	1	0	1	1	0	0	0	1	0	0	1	211
26	1	0	1	1	0									
27	1	0	1	1	1	0	1	1	0	1	0	0	0	150
30	1	1	0	0	0	1	1	1	1	1	1	0	1	375
31	1	1	0	0	1	1	1	1	0	1	1	0	1	355
32	1	1	0	1	0	1	1	1	1	1	1	0	1	375
33	1	1	0	1	1	1	1	1	0	1	1	0	1	355
34	1	1	1	0	0	1	1	1	1	0	1	1	1	357
35	1	1	1	0	1	1	1	0	0	0	1	1	0	306
36	1	1	1	1	0									
37	1	1	1	1	1	1	1	1	1	1	1	1	1	377

- NOTES: 1) With chip disabled ( $C_s=1$ ), outputs are high level.  
 2) The map is specified with pos. logic output  
    (i.e. 1=High)  
 3) Least significant bit of contents is Y<sub>7</sub>.  
 4) Stamp DGC part no. or color code per EIA standard color.  
 5) This part is made from 100000140.

REVISIONS

REV	DESCRIPTION	DA	APP BY DATE
-----	-------------	----	-------------

PI PTS1  
 PI PTS0  
 SKIP. PI. PTS0  
 (Do skip first)

FETCH

FETCH.SKIP

AUTO INC PTS 1

AUTO INC PTS 0

AUTO DEC PTS 1

AUTO DEC PTS 0

EFA

DEFER

Null

SPEC ROM 0

DRAWN		DATA GENERAL CORPORATION	
CHECKED		TITLE	
ENGINEER	<i>[Signature]</i>	SPEC ROM 0	
APPROVED		32 X 8 READ ONLY	
FIRST USED ON	N2	MEMORY MAP	
SHEET	1	SIZE	CODE
	*	DRAWING NUMBER	REV
	1	100000273	b0

PIN #	PI	PT	FETCH	AUTO	SKIP	AUTO	PTS#	AUTO F	WRITE	INC/A	CLEAR	ZERC	DCHTR	DEQA		REVISIONS				
OCTAL ADDRESS	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	Y <sub>0</sub>	Y <sub>1</sub>	Y <sub>2</sub>	Y <sub>3</sub>	Y <sub>4</sub>	Y <sub>5</sub>	Y <sub>6</sub>	Y <sub>7</sub>	OCTAL CONTENTS	REV	DESCRIPTION	DRFTG	APP BY	DATE	
00	0	0	0	0	0															
01	0	0	0	0	1															
02	0	0	0	1	0															
03	0	0	0	1	1															
04	0	0	1	0	0	1	1	0		1	1	1	1	1	317					
05	0	0	1	0	1	1	0	1		1	0	1	1	1	253					
06	0	0	1	1	0															
07	0	0	1	1	1	1	1	1	0		1	1	1	1	317					
10	0	1	0	0	0															
11	0	1	0	0	1															
12	0	1	0	1	0															
13	0	1	0	1	1															
14	0	1	1	0	0															
15	0	1	1	0	1															
16	0	1	1	1	0															
17	0	1	1	1	1															
20	1	0	0	0	0															
21	1	0	0	0	1															
22	1	0	0	1	0															
23	1	0	0	1	1															
24	1	0	1	0	0															
25	1	0	1	0	1	1	1	0		1	1	1	1	1	317					
26	1	0	1	1	0															
27	1	0	1	1	1	1	1	1	0		1	1	1	1	317					
30	1	1	0	0	0	1	0	1		0	1	1	1	1	247					
31	1	1	0	0	1	1	1	1	0		1	1	0	1	315					
32	1	1	0	1	0	1	0	1		0	1	1	1	1	247					
33	1	1	0	1	1	1	1	1	1		1	1	0	0	354					
34	1	1	1	0	0	1	1	1	1		1	1	1	1	357					
35	1	1	1	0	1	0	1	1	1		0	1	1	1	147					
36	1	1	1	1	0															
37	1	1	1	1	1	1	1	1	1		1	1	1	1	377					

- NOTES:
- 1) With chip disabled ( $C_s=1$ ), outputs are high level.
  - 2) The map is specified with pos. logic output (i.e. 1=High)
  - 3) Least significant bit of contents is  $Y_7$ .
  - 4) Stamp DGC part no. or color code per EIA standard color.
  - 5) This part is made from 100000140.

### SPEC ROM 1

DRAWN		DATA GENERAL CORPORATION	
CHECKED			
ENGINERED <i>K. Jorgensen</i>			
APPROVED			
FIRST USED ON	N2		
SHEET 1	x 1	SIZE A	CODE
		DRAWING NUMBER 100000274	
		REV 00	

M

PIN #	14	13	12	11	10	1	2	3	4	5	6	7	9	OCTAL CONTENTS
OCTAL ADDRESS	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	Y <sub>0</sub>	Y <sub>1</sub>	Y <sub>2</sub>	Y <sub>3</sub>	Y <sub>4</sub>	Y <sub>5</sub>	Y <sub>6</sub>	Y <sub>7</sub>	
00	0	0	0	0	0	1	1	1	1	1	1	1	1	377
01	0	0	0	0	1	0	0	1	1	1	1	1	1	077
02	0	0	0	1	0	1	0	1	0	1	0	1	0	252
03	0	0	0	1	1	1	1	1	1	1	1	1	1	377
04	0	0	1	0	0	1	0	0	0	1	1	1	1	377
05	0	0	1	0	1	0	1	1	1	1	1	1	0	216
06	0	0	1	1	0	1	1	1	1	1	0	0	1	171
07	0	0	1	1	1	0	1	1	1	1	1	1	1	377
10	0	1	0	0	0	0	1	0	0	1	1	0	0	031
11	0	1	0	0	1	1	1	1	1	1	1	1	1	377
12	0	1	0	1	0	1	1	1	1	1	1	1	1	377
13	0	1	0	1	1	1	1	1	1	0	1	1	1	367
14	0	1	1	0	0	1	1	0	0	1	1	1	1	367
15	0	1	1	0	1	1	1	1	0	1	1	0	1	315
16	0	1	1	1	0	1	1	1	1	0	1	1	1	357
17	0	1	1	1	1	1	0	1	1	1	1	1	1	377
20	1	0	0	0	0	1	1	1	0	1	1	1	1	276
21	1	0	0	0	1	1	1	1	1	1	1	1	1	357
22	1	0	0	1	0	1	0	1	1	1	1	1	0	276
23	1	0	0	1	1	1	1	1	1	1	1	1	0	276
24	1	0	1	0	0	1	1	1	1	1	0	0	1	371
25	1	0	1	0	1	1	1	1	1	0	1	0	1	365
26	1	0	1	1	0	1	1	1	1	0	0	1	1	363
27	1	0	1	1	1	0	0	1	0	1	0	1	0	052
30	1	1	0	0	0	1	1	1	1	0	1	1	1	367
31	1	1	0	0	1	1	1	1	1	0	1	1	1	367
32	1	1	0	1	0	1	1	1	1	0	1	1	1	367
33	1	1	0	1	1	1	1	1	0	1	0	1	1	367
34	1	1	1	0	0	0	0	0	0	1	1	1	0	352
35	1	1	1	0	1	1	1	1	0	1	0	1	1	016
36	1	1	1	1	0	0	1	1	1	1	0	0	1	353
37	1	1	1	1	1	0	1	0	1	1	0	0	1	171
														131

- NOTES:
- 1) With chip disabled ( $C_S=1$ ), outputs are high level.
  - 2) The map is specified with pos. logic output (i.e. 1=High)
  - 3) Least significant bit of contents is Y<sub>7</sub>.
  - 4) Stamp DGC part no. or color code per EIA standard color.
  - 5) This part is made from 100000140.

DRAWN		DATA GENERAL CORPORATION	
CHECKED			
X. J. Jorgenson			
APPROVED			
TITLE		PL ROM Ø	
FIRST USED ON		32 X 8 READ ONLY	
N2		MEMORY MAP	
SHEET 1 OF 1	SIZE A	CODE	DRAWING NUMBER 100000275
			REV 00

PIN #	14	13	12	11	10	1	2	3	4	5	6	7	9	OCTAL CONTENTS
OCTAL ADDRESS	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	Y <sub>0</sub>	Y <sub>1</sub>	Y <sub>2</sub>	Y <sub>3</sub>	Y <sub>4</sub>	Y <sub>5</sub>	Y <sub>6</sub>	Y <sub>7</sub>	
00	0	0	0	0	0	1	1	1	1	1	1	1	1	377
01	0	0	0	0	1	1	1	1	1	1	1	1	1	377
02	0	0	0	1	0	1	1	1	1	1	1	0	0	374
03	0	0	0	1	1	1	1	1	0	0	1	1	1	347
04	0	0	1	0	0	0	0	1	1	0	0	0	0	060
05	0	0	1	0	1	0	0	0	0	0	0	1	1	003
06	0	0	1	1	0	1	1	1	0	0	1	1	1	347
07	0	0	1	1	1	0	0	0	0	0	0	0	1	001
10	0	1	0	0	0	1	1	1	1	0	1	1	1	367
11	0	1	0	0	1	0	0	0	0	0	0	1	1	003
12	0	1	0	1	0	1	0	1	1	0	1	1	1	267
13	0	1	0	1	1	1	1	1	1	1	1	0	1	375
14	0	1	1	0	0	1	0	0	1	0	1	1	1	227
15	0	1	1	0	1	0	0	0	1	0	1	1	1	027
16	0	1	1	1	0	0	0	0	0	1	1	1	1	017
17	0	1	1	1	1	0	1	0	1	0	0	1	1	123
20	1	0	0	0	0	1	1	1	0	0	1	1	1	347
21	1	0	0	0	1	0	0	0	0	0	0	0	0	0
22	1	0	0	1	0	1	0	1	1	1	1	0	1	275
23	1	0	0	1	1	0	0	0	0	0	0	1	1	003
24	1	0	1	0	0	0	0	0	0	0	0	0	0	0
25	1	0	1	0	1	1	0	0	0	1	1	1	1	217
26	1	0	1	1	0	0	1	0	1	1	1	1	1	137
27	1	0	1	1	1	1	1	0	1	1	1	1	1	337
30	1	1	0	0	0	1	0	1	0	0	1	1	1	247
31	1	1	0	0	1	1	1	0	0	1	1	1	1	347
32	1	1	0	1	0	1	1	0	0	1	1	1	1	317
33	1	1	0	1	1	1	1	1	1	1	1	1	1	377
34	1	1	1	0	0	1	1	1	1	1	1	1	1	377
35	1	1	1	0	1	1	0	0	1	0	1	1	1	227
36	1	1	1	1	0	0	0	0	0	0	0	1	1	003
37	1	1	1	1	1	0	0	0	0	0	0	1	0	002

- NOTES:
- 1) With chip disabled ( $C_S=1$ ), outputs are high level.
  - 2) The map is specified with pos. logic output (i.e. 1=High)
  - 3) Least significant bit of contents is Y<sub>7</sub>.
  - 4) Stamp DGC part no. or color code per EIA standard color.
  - 5) This part is made from 100000140.

DRAWN		DATA GENERAL CORPORATION	
CHECKED			
SPECIFIED			
APPROVED			
FIRST USED ON			
N2			
SHEET 1	* 1	SIZE A	DRAWING NUMBER 100000276
		REV C	

RESS	$\overline{CAL}$	EFA	IR6	IR7	DECA	ALU M	ALUS#	ALU1	ALU2	ALU3	ALU	DISP#	XT	REV	DESCRIPTION	DRAFTG	APP BY	DATE
	14	13	12	11	10	1	2	3	4	5	6	7	9	OCTAL CONTENTS				
00	0	0	0	0	0													
01	0	0	0	0	1													
02	0	0	0	1	0													
03	0	0	0	1	1													
04	0	0	1	0	0													
05	0	0	1	0	1													
06	0	0	1	1	0													
07	0	0	1	1	1													
10	0	1	0	0	0													
11	0	1	0	0	1	0	1	0	0	1	1	0	1	115				
12	0	1	0	1	0													
13	0	1	0	1	1	0	1	0	0	1	0	0	1	111				
14	0	1	1	0	0													
15	0	1	1	0	1	0	0	1	1	0	1	0	1	65				
16	0	1	1	1	0													
17	0	1	1	1	1	0	0	1	1	0	0	0	1	61				
20	1	0	0	0	0													
21	1	0	0	0	1	0	1	0	0	1	0	0	1	111				
22	1	0	0	1	0													
23	1	0	0	1	1	0	1	0	0	1	0	0	1	111				
24	1	0	1	0	0													
25	1	0	1	0	1	0	1	0	0	1	0	0	1	111				
26	1	0	1	1	0													
27	1	0	1	1	1	0	1	1	1	1	0	0	0	170				
30	1	1	0	0	0	0	0	0	0	0	0	0	0	1				
31	1	1	0	0	1	1	0	1	0	1	0	0	1	251				
32	1	1	0	1	0	0	0	0	0	0	0	0	1	1				
33	1	1	0	1	1	1	0	1	0	1	0	0	1	251				
34	1	1	1	0	0	0	0	0	0	0	0	0	1	1				
35	1	1	1	0	1	1	0	1	0	1	0	0	1	251				
36	1	1	1	1	0	0	0	0	0	0	0	0	1	1				
37	1	1	1	1	1	1	0	1	0	1	0	0	1	251				

- NOTES: 1) With chip disabled ( $C_s=1$ ), outputs are high level.  
 2) The map is specified with pos. logic output (i.e. 1=High)  
 3) Least significant bit of contents is  $Y_7$ .  
 4) Stamp DGC part no. or color code per EIA standard color.  
 5) This part is made from 100000140.

DRAWN		DATA GENERAL CORPORATION		
CHECKED				
NEER				
APPROVED				
FIRST USED ON				
N2		TITLE		
SHEET 1	1	ALU ROM 32 X 8 READ ONLY MEMORY MAP		
SIZE A	CODE	DRAWING NUMBER 10000279		
REV				

REVISIONS  
REV: 01 ECO 2530 RC 5/1  
DESCRIPTION RETYPED NO CHANGES

PIN #	14	IR8	IR7	IR8&I1	IR5.I1	PTS#	SET/FEE	EXTEN	IWEN	IN	PTG	OUT	2REN	AND	OCTAL ADDRESS	OCTAL CONTENTS
	A4	A3	A2	A1	A0		Y0	Y1	Y2	Y3	Y4	Y5	Y6	Y7		
00	0	0	0	0	0	0	0	1	1	1	1	1	1	0	176	
01	0	0	0	0	1											
02	0	0	0	1	0	0	1	1	1	1	1	1	1	1	177	
03	0	0	0	1	1											
04	0	0	1	0	0	0	1	1	1	1	1	1	1	0	176	
05	0	0	1	0	1											
06	0	0	1	1	0	0	1	1	1	1	1	1	1	1	177	
07	0	0	1	1	1											
10	0	1	0	0	0	0	0	1	1	1	1	1	1	1	177	
11	0	1	0	0	1											
12	0	1	0	1	0	0	1	1	1	1	1	1	1	1	177	
13	0	1	0	1	1											
14	0	1	1	0	0	0	1	1	1	1	1	1	1	1	177	
15	0	1	1	0	1											
16	0	1	1	1	0	0	0	1	1	1	1	1	1	1	177	
17	0	1	1	1	1											
20	1	0	0	0	0	0	0	0	1	0	1	1	1	1	057	
21	1	0	0	0	1		0	1	1	1	0	1	1	1	167	
22	1	0	0	1	0	0	1	0	0	0	1	1	1	1	217	
23	1	0	0	1	1		0	1	1	1	0	1	1	1	167	
24	1	0	1	0	0	0	0	0	1	0	1	1	1	1	057	
25	1	0	1	0	1		0	1	1	1	0	1	1	1	167	
26	1	0	1	1	0	1	0	0	0	1	1	1	1	1	217	
27	1	0	1	1	1		0	0	1	1	0	1	1	1	067	
30	1	1	0	0	0	0	1	0	1	1	1	0	0	1	271	
31	1	1	0	0	1		0	1	1	1	0	1	1	1	167	
32	1	1	0	1	0	1	0	1	1	1	0	0	0	1	271	
33	1	1	0	1	1		0	1	1	1	0	1	1	1	167	
34	1	1	1	0	0	0	1	0	1	1	1	0	0	1	271	
35	1	1	1	0	1		0	0	1	1	0	1	1	1	067	
36	1	1	1	1	0	1	0	1	1	1	0	0	0	1	271	
37	1	1	1	1	1		0	0	1	1	0	1	1	1	67	

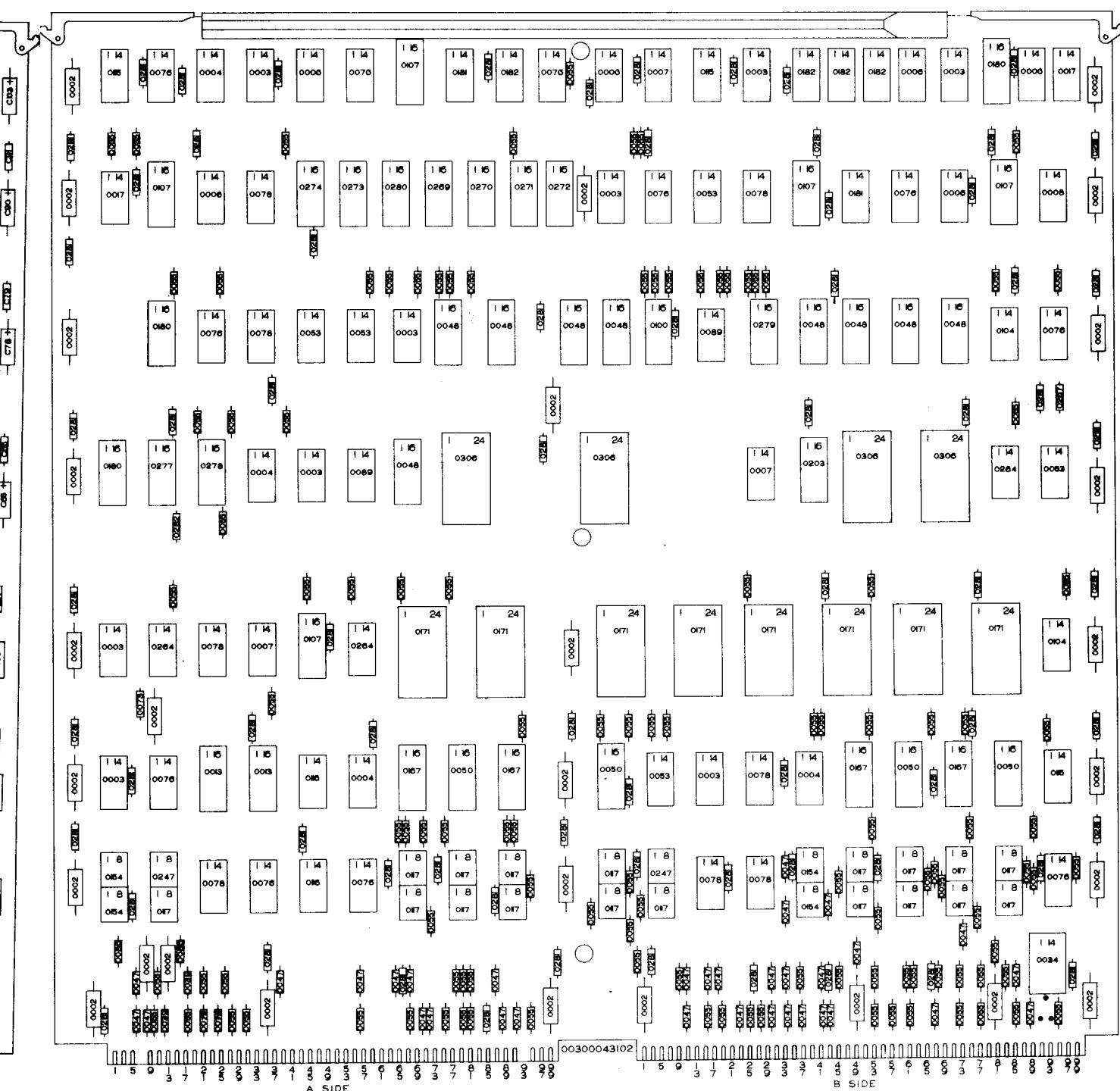
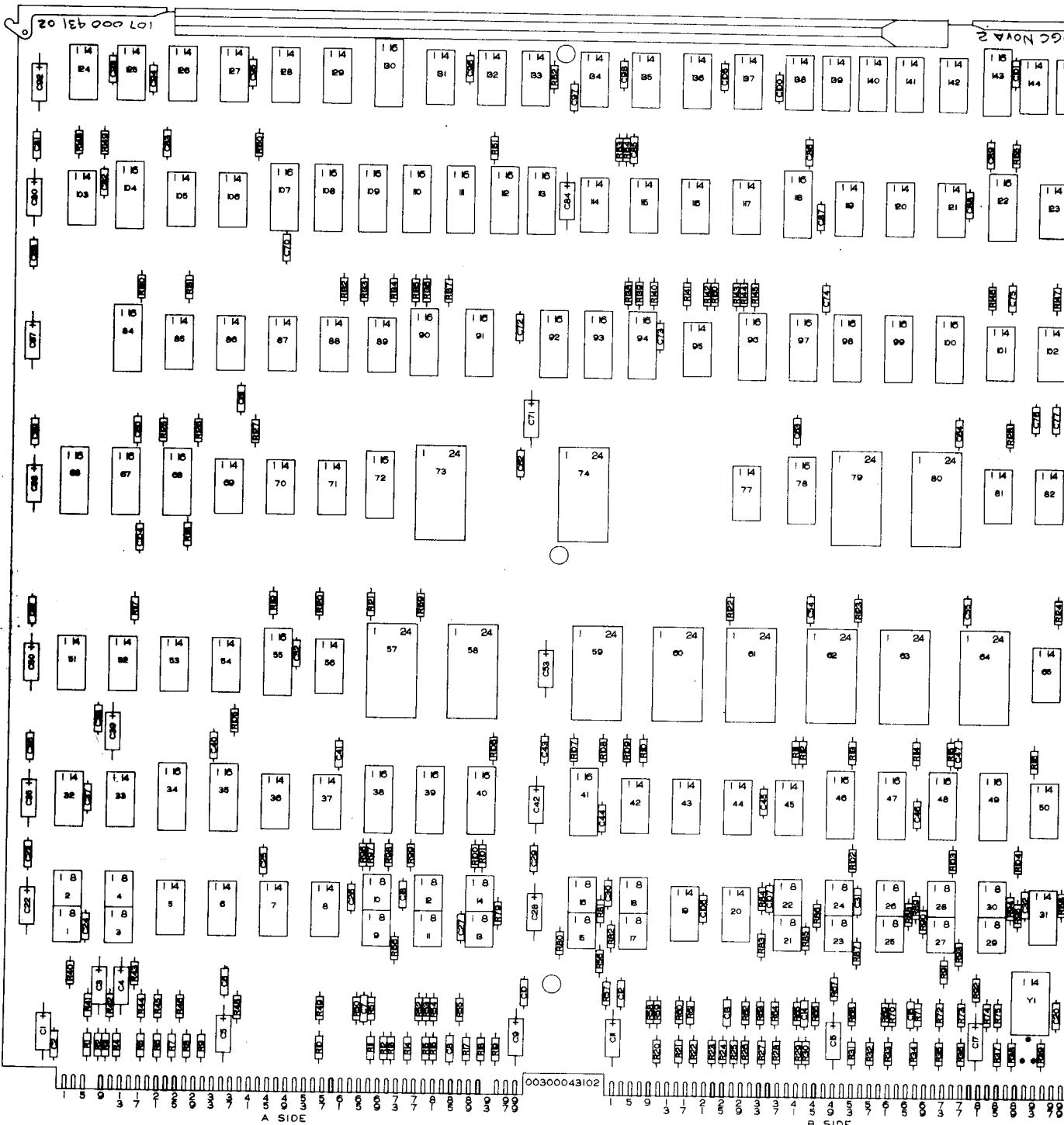
- NOTES: 1) With chip disabled ( $C_s=1$ ), outputs are high level.  
 2) The map is specified with pos. logic output (i.e. 1=High)  
 3) Least significant bit of contents is Y7.  
 4) Stamp DGC part no. or color code per EIA standard color.  
 5) This part is made from 100000140.

DRAWN		DATA GENERAL CORPORATION	
CHECKED			
R. Sorenson		I/O ALC ROM	
APPROVED		32 X 8 READ ONLY	
FIRST USED ON		MEMORY MAP	
N2			
SHEET	1 x 1	SIZE	A
CODE		DRAWING NUMBER	100000280
REV		REV	01

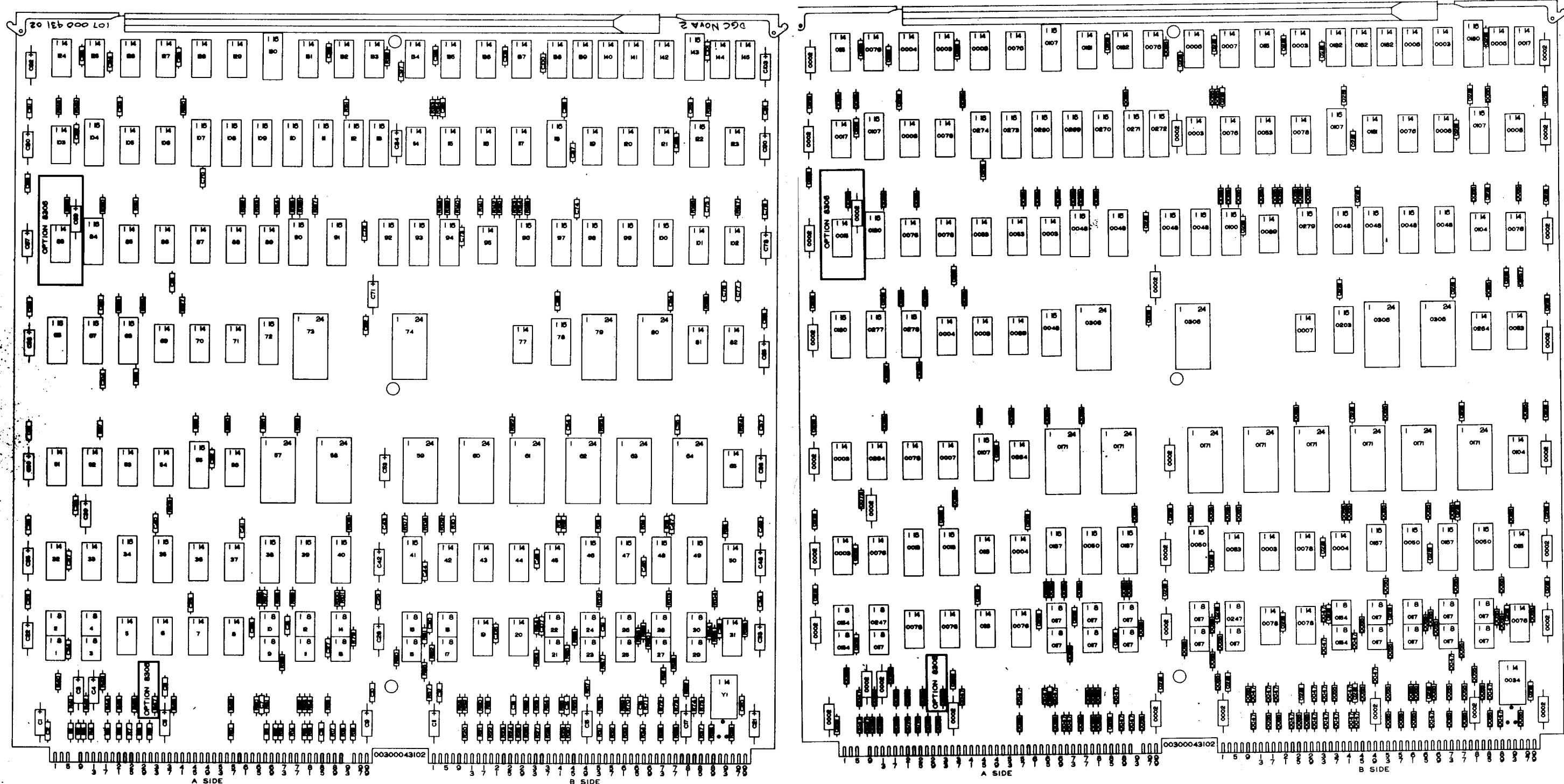
THIS DRAWING AND SPECIFICATIONS HEREIN ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

## REVISIONS

REV	DESCRIPTION	DATE ISS	APP BY	DATE
00	<b>FIRST RELEASE - ECO 4430</b>	WFB	D.L.W.	2/19/94



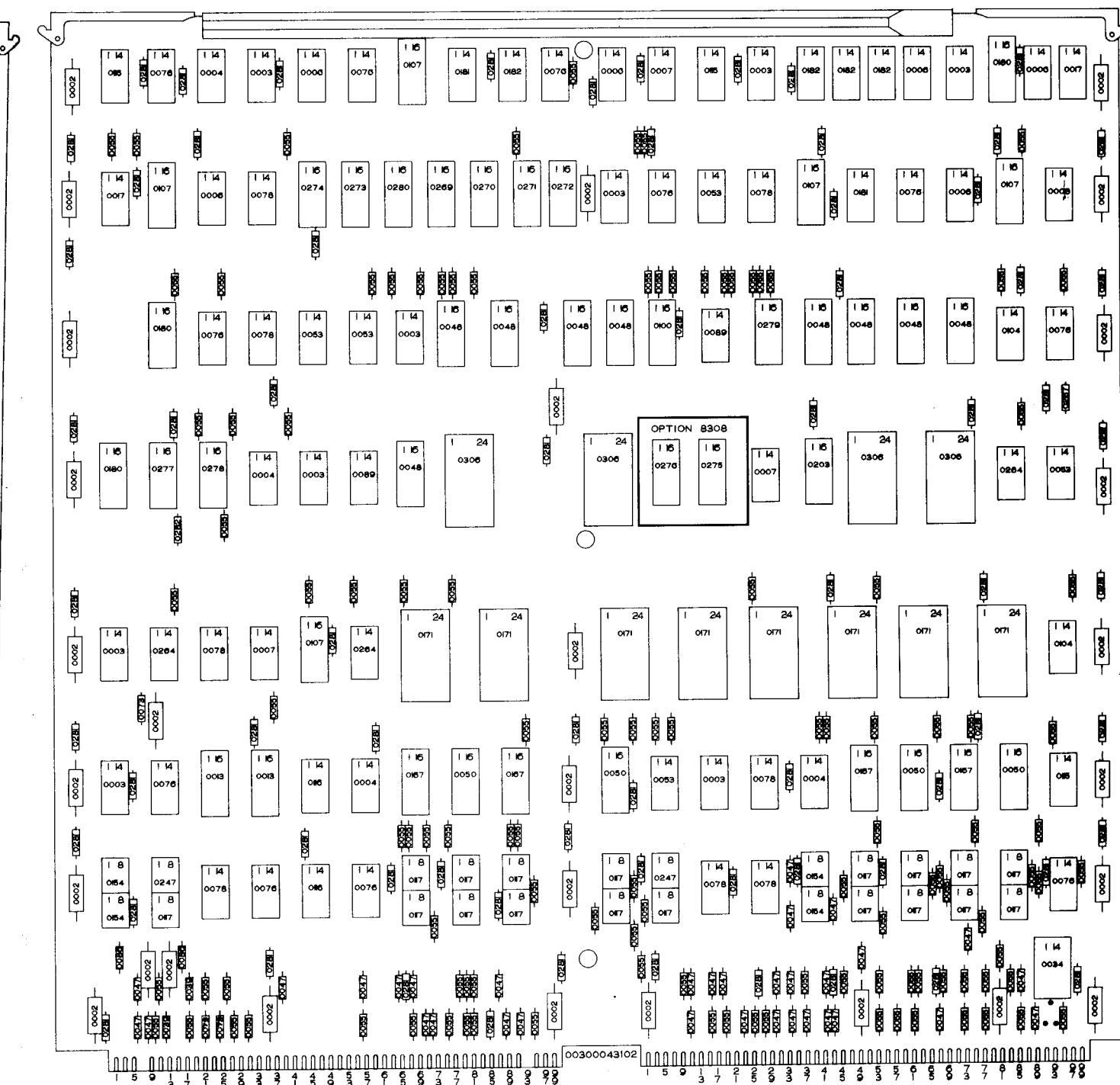
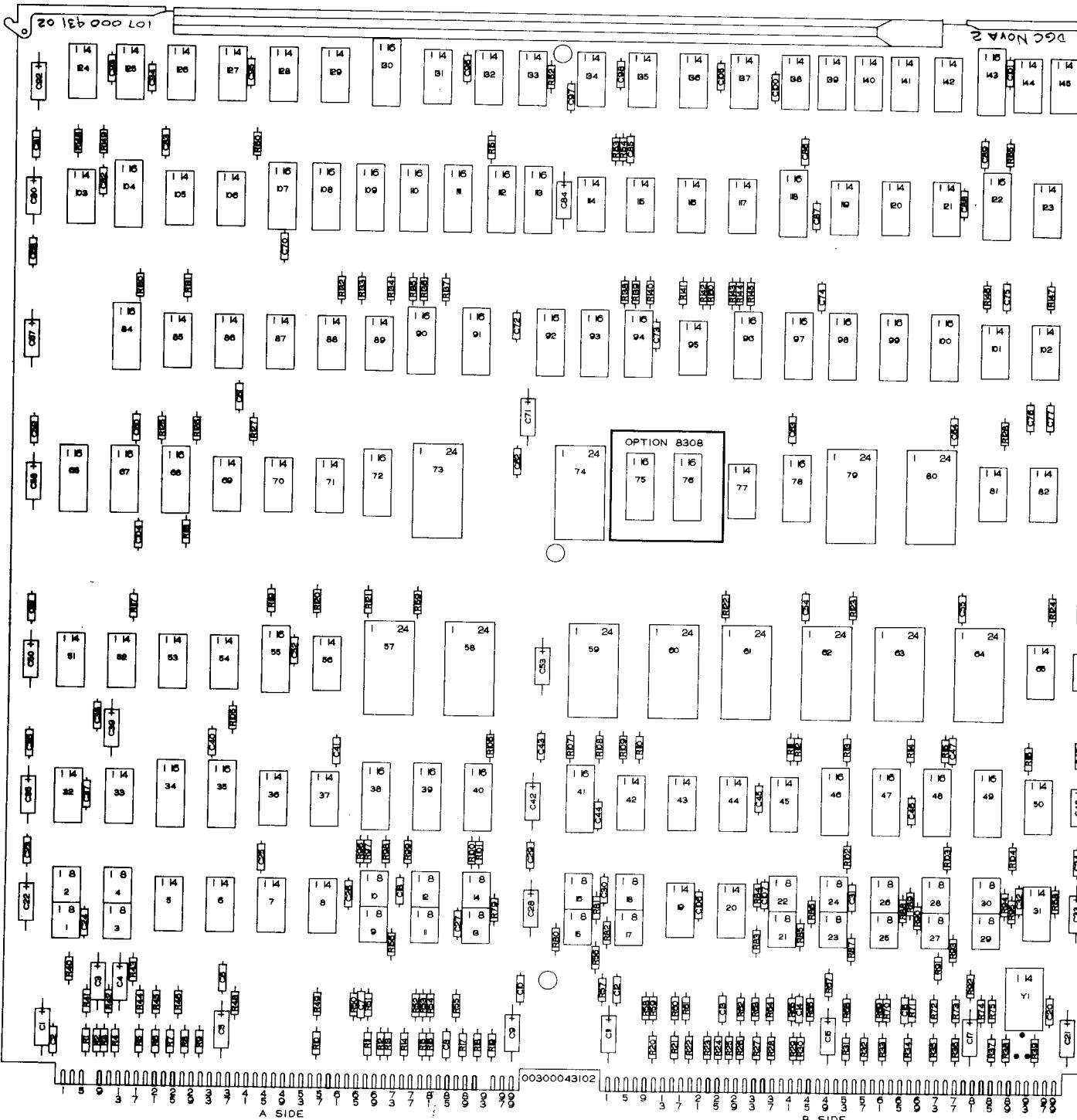
UNLESS OTHERWISE SPECIFIED USE D G C SPECIFICATION 10B 000028 FOR TOLER ANCES AND WORKMAN SHIP REQUIREMENTS	DRAWN BY <b>WFB</b>	DATA GENERAL CORPORATION BEDFORD, MASSACHUSETTS 01730 TITLE
MATERIAL	CHECKED <i>qph</i> BY DATE APR 1976	3/19/76
FINISH	FAST LOGIC N2 CPU	NOVA 2 CPU
	2153	DRAWN NO. MB- <b>016000306</b> REV D 00



BRUNNEN 40-526 2041

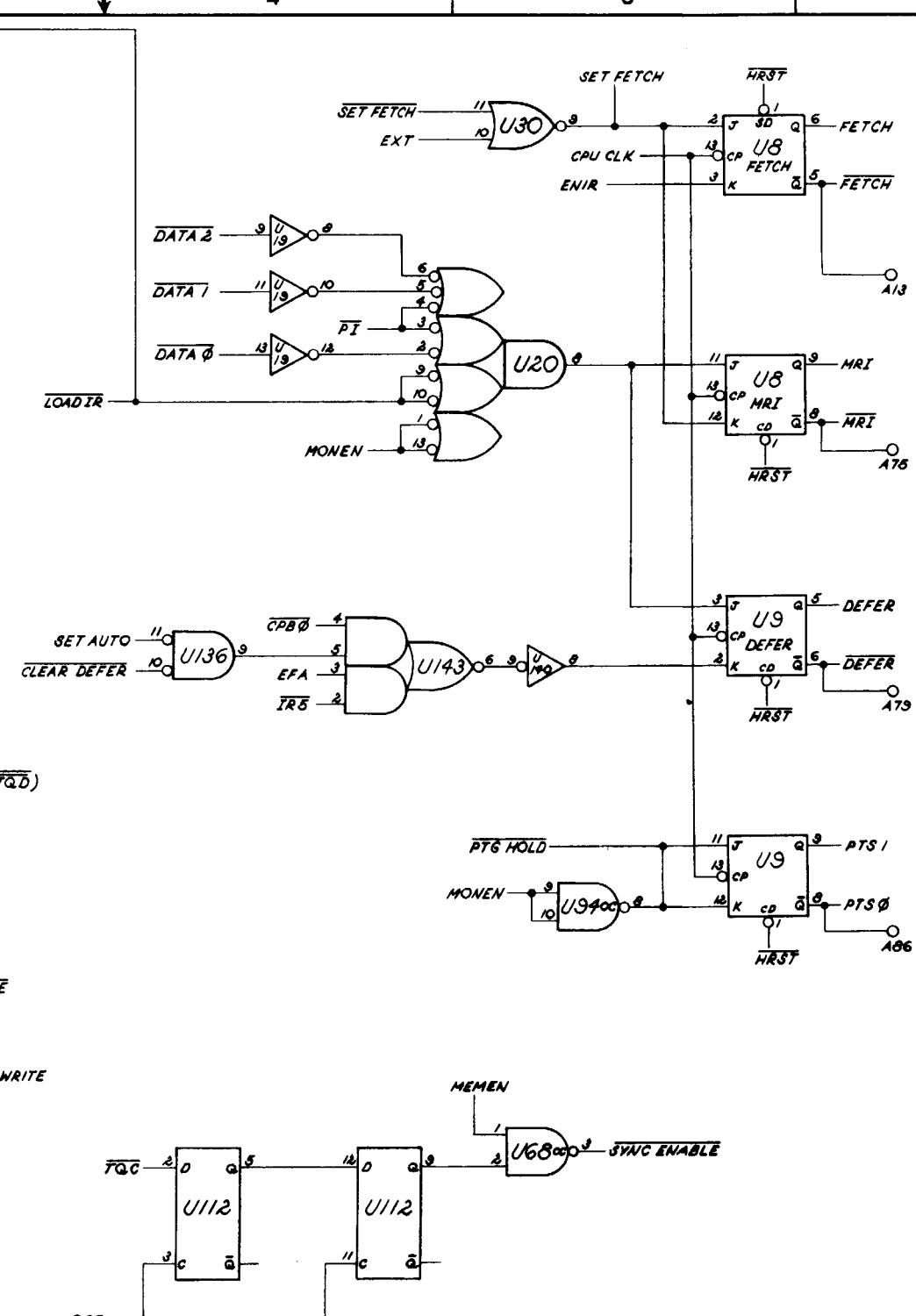
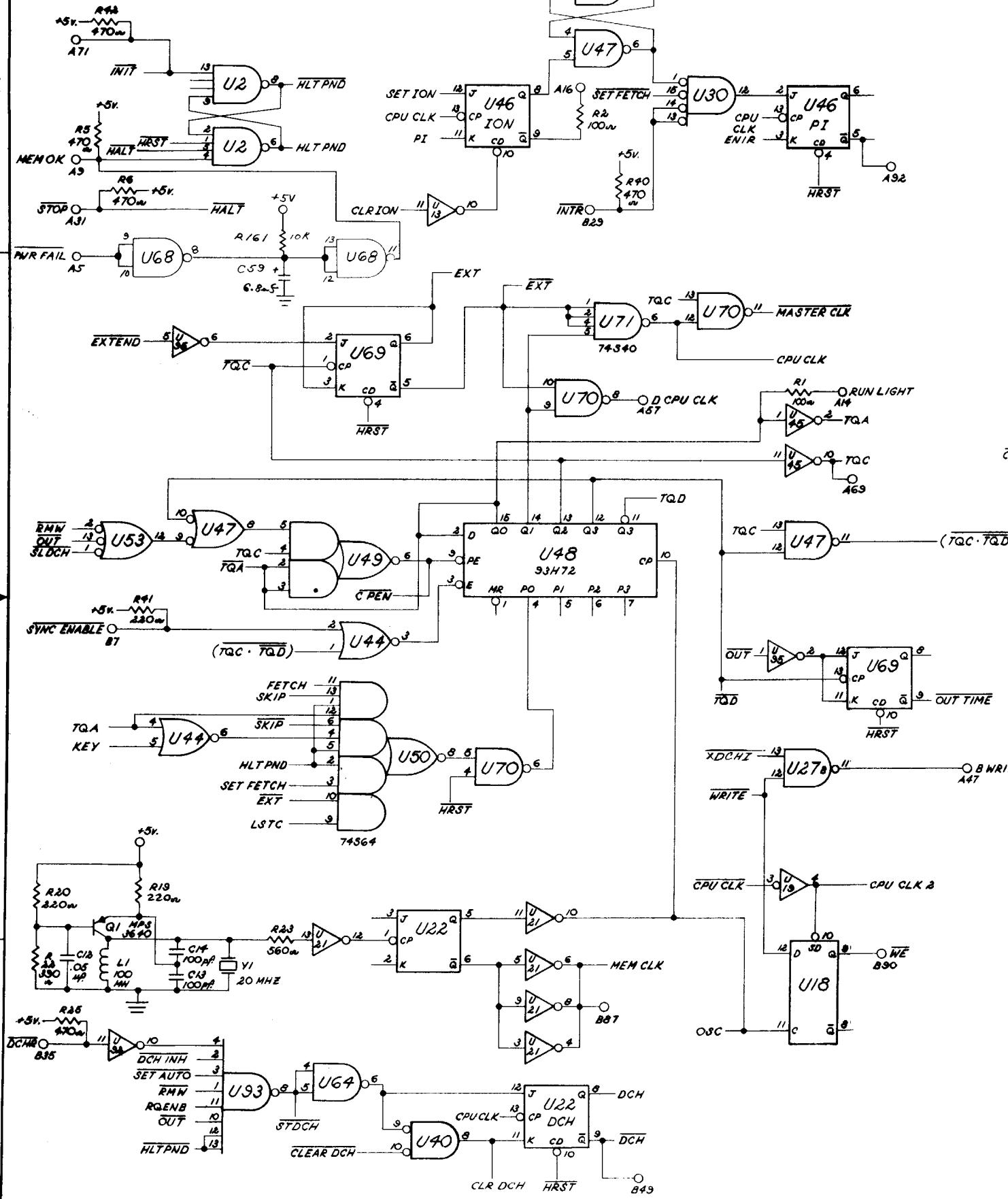
L/TN		DEPARTMENT		FACILITY	
UNLESS OTHERWISE SPECIFIED USE D G C SPECIFICATION 108 000028 FOR TOLER- ANCES AND WORKMAN- SHIP REQUIREMENTS		<b>DRAWN</b> <b>WFB</b> <b>CHECKED</b> <i>yrb</i> <b>ENGR</b> <b>D.L.W.</b> <b>APPROVED</b>		<b>DATA GENERAL CORPORATION</b> WOOLSTHORPE, MASSACHUSETTS 01772 <b>TITLE</b> <b>NOVA 2 CPU</b> <b>W/ POWER MONITOR</b>	
MATERIAL		FIRST USED ON N2 CPU		PAGE CODE DRAWING NUMBER REV	
FINISH		<b>2133, 6306</b>		<b>D</b>	<b>016000307</b>
		SCALE		00	

REVISIONS			
REV	DESCRIPTION	DRFTG	APP BY
00	-FIRST RELEASE- ECO 4430	WFB	D.L.W. 2/19/76



L/T	DESCR PT OF	R.A.T NO
DRAWN	WFB	
CHECKED	gms	2/19/76
ENGINEER	D.L.W.	
APPROVED		
FIRST USED ON	N2 CPU	
FINISH	2153, 8308	
SCALE	D	DRAWING NUMBER
		016000308
REV	00	

THIS DRAWING AND SPECIFICATIONS HEREIN ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHOULD NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.



REVISIONS			
REV.	DESCRIPTION	DRFTG.	APP. BY
O1	ECO # 1736	R.N.C.	1-19 73
O2	ECO # 1888	R.N.C.	1-19 73
O3	ECO # 1901	R.N.C.	1-19 73
O4	ECO # 1913	R.N.C.	1-19 73
O5	ECO # 2090	R.N.C.	1-19 73
O6	ECO # 2156	R.N.C.	1-19 73
O7	ECO # 2201	R.N.C.	1-19 73
O8	ECO # 2253	R.N.C.	1-19 73
O9	ECO # 2289	R.N.C.	1-19 73
O10	ECO # 2360	R.N.C.	1-19 73
O11	ECO # 2368	R.N.C.	1-19 73
O12	ECO # 2802	R.N.C.	1-19 73
O13	ECO # 2775	G.W.M.	1-19 73
O14	ECO # 2752	G.W.M.	1-19 73
O15	ECO # 3164	R.N.C.	1-19 73
O16	ECO # 8574	R.N.C.	1-19 73

ARTWORK: 107000295	
ARTWORK VS SCHEMATIC	ARTWORK REV(S)
REV	SCHEMATIC REV(S)
02	01 & 02
03	03 & 04
04	03 & 04
05	05
06	05
07	06
08	07, 08 & 09
09	10 & 11
10	12
11	12
12	13
13	14 & 15

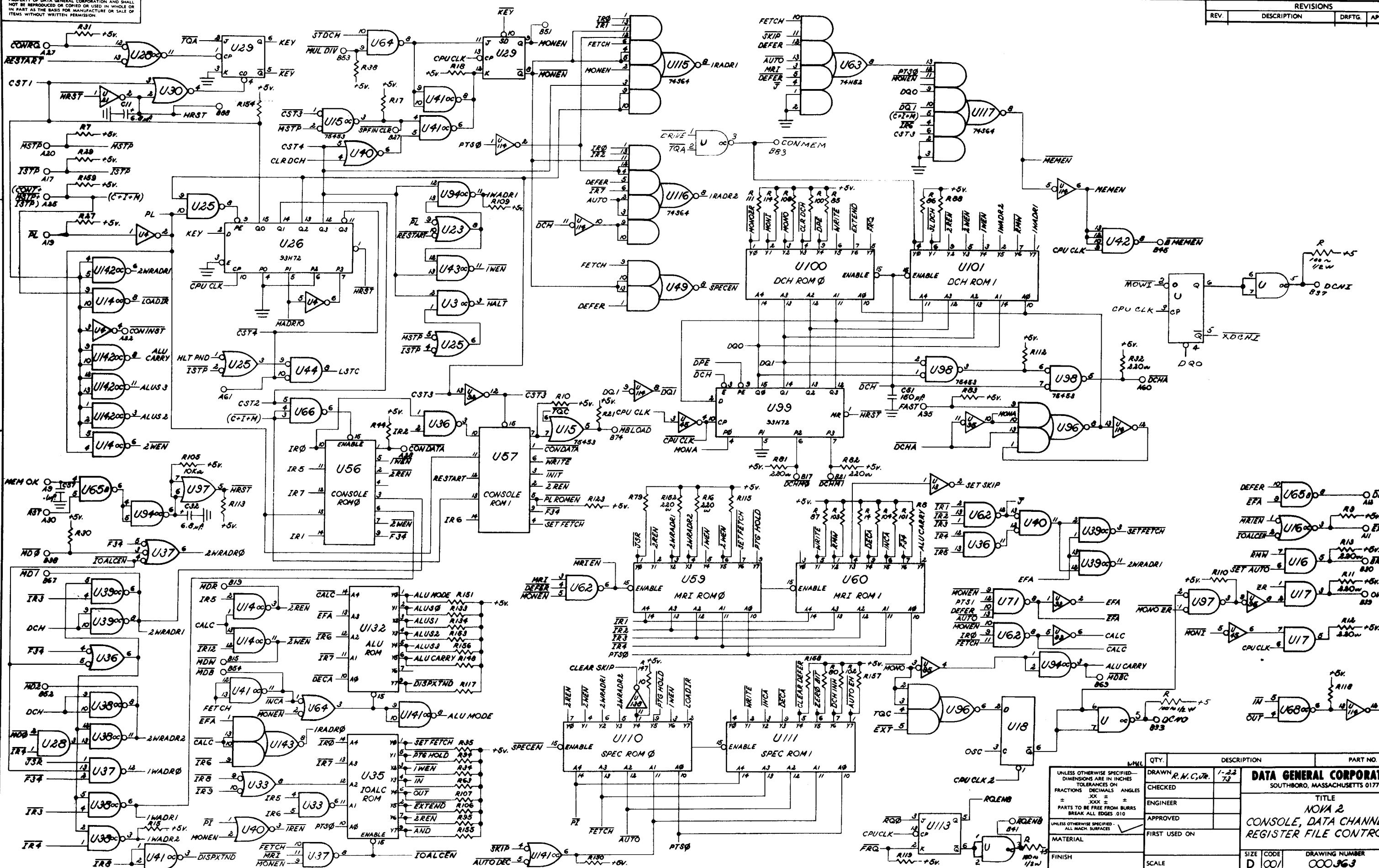
ARTWORK: 107000481	
ARTWORK VS SCHEMATIC	ARTWORK REV(S)
REV	SCHEMATIC REV(S)
00	16
01	
02	
03	
04	
05	
06	
07	
08	
09	
10	
11	
12	
13	
14	
15	

NOTES:  
1) ALL RESISTORS ARE  $470\ \Omega$ ,  $1\% 5\%$  UNLESS OTHERWISE SPECIFIED.

ITEM NO.	QTY	DESCRIPTION	PART NO.
1	DRAWN R.N.C., JR.	1-19 73	DATA GENERAL CORPORATION
2	CHECKED		SOUTHBORO, MASSACHUSETTS 01772
3	ENGINEER	12-6 73	TITLE
4	APPROVED		NOVA 2
5	FIRST USED ON	N2/1 2153 N2/10	MAJOR STATES
6	MATERIAL		
7	FINISH		
8	SCALE	D 001	DRAWING NUMBER 000-363
9	SIZE		REV.

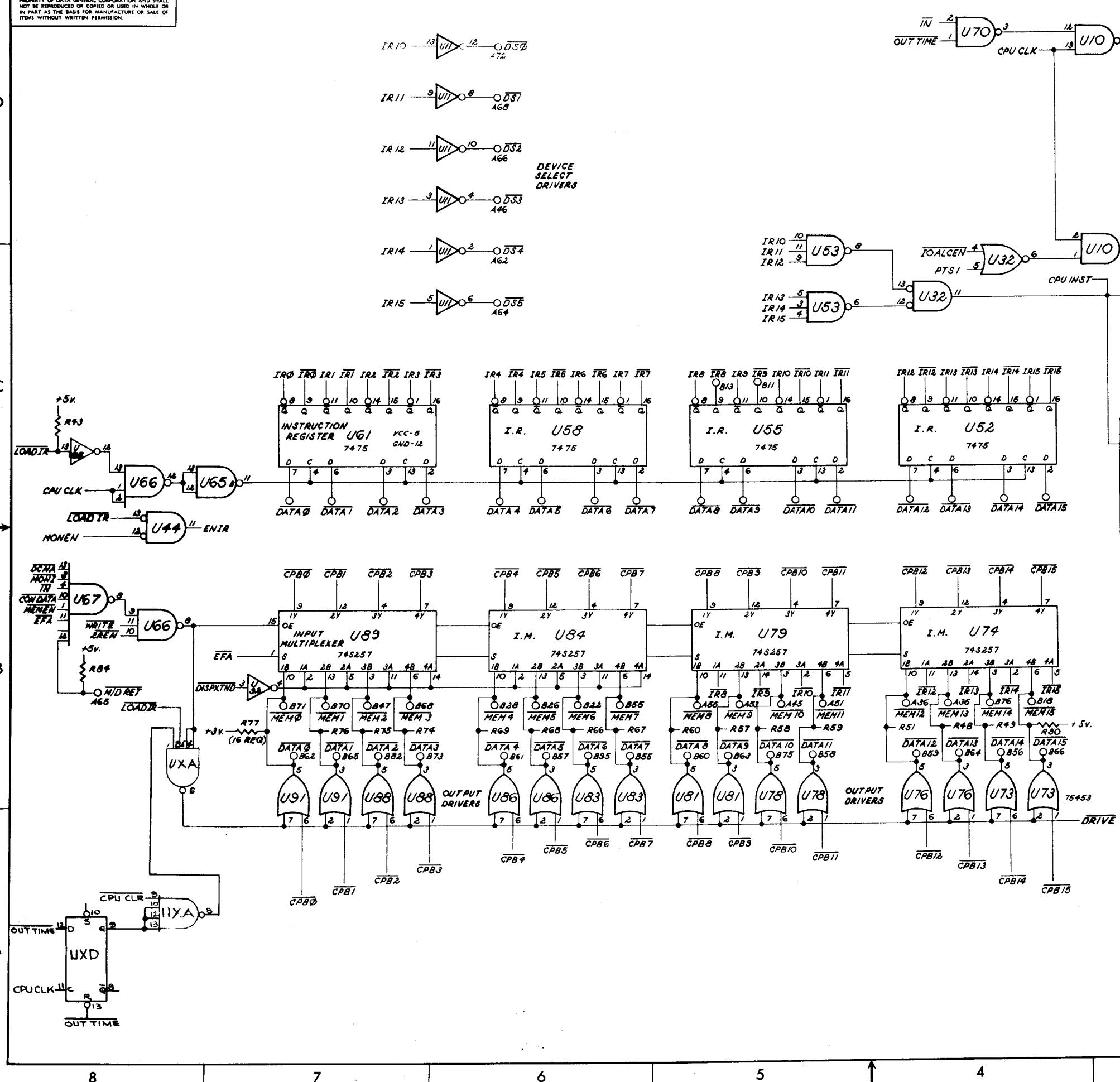
THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

REVISIONS				
REV.	DESCRIPTION	DRFTG.	APP. BY	DATE

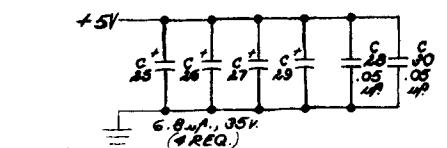


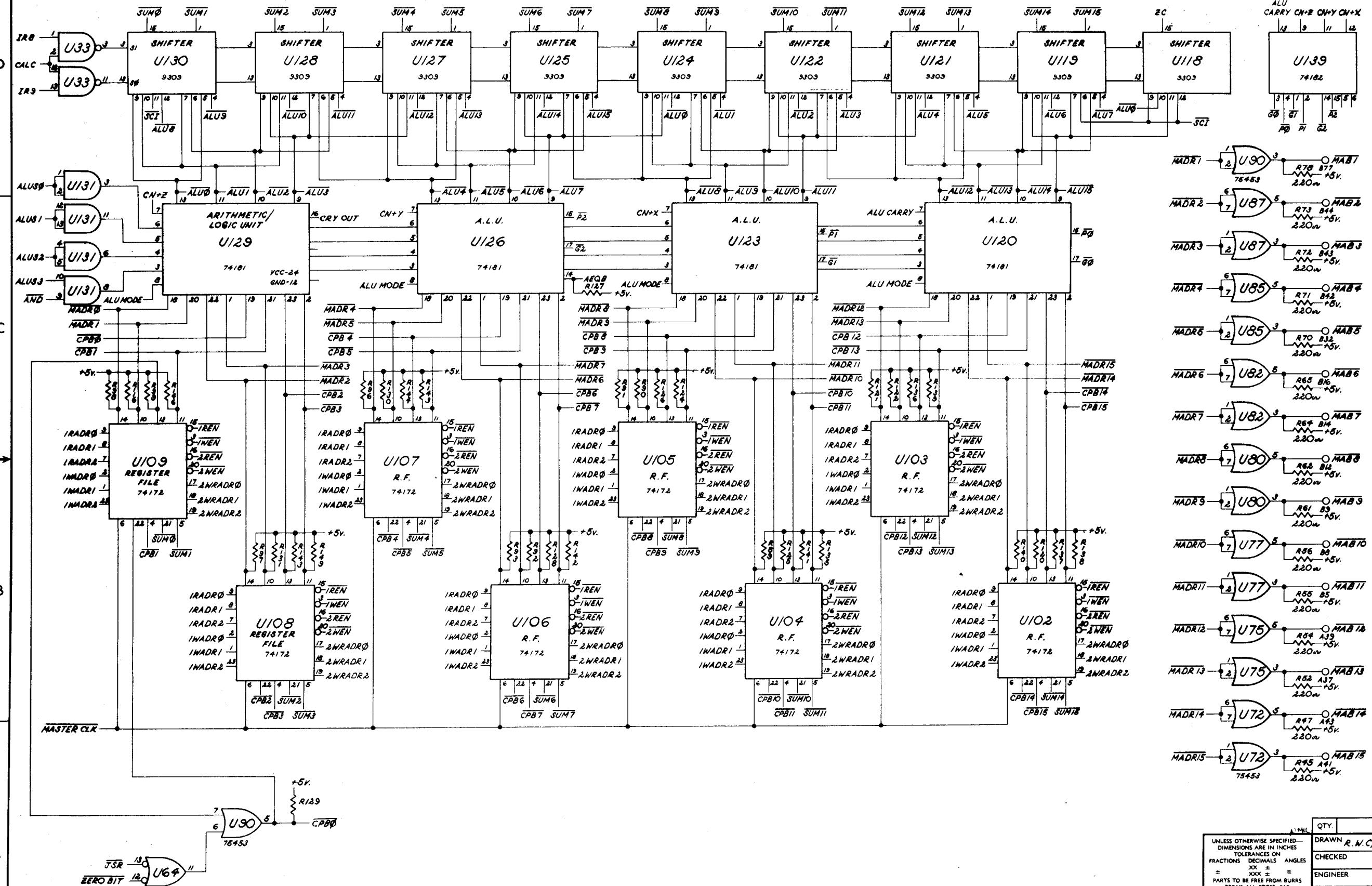
THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION, AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

REVIEWS			
REV	DESCRIPTION	DRFTG	APP. BY

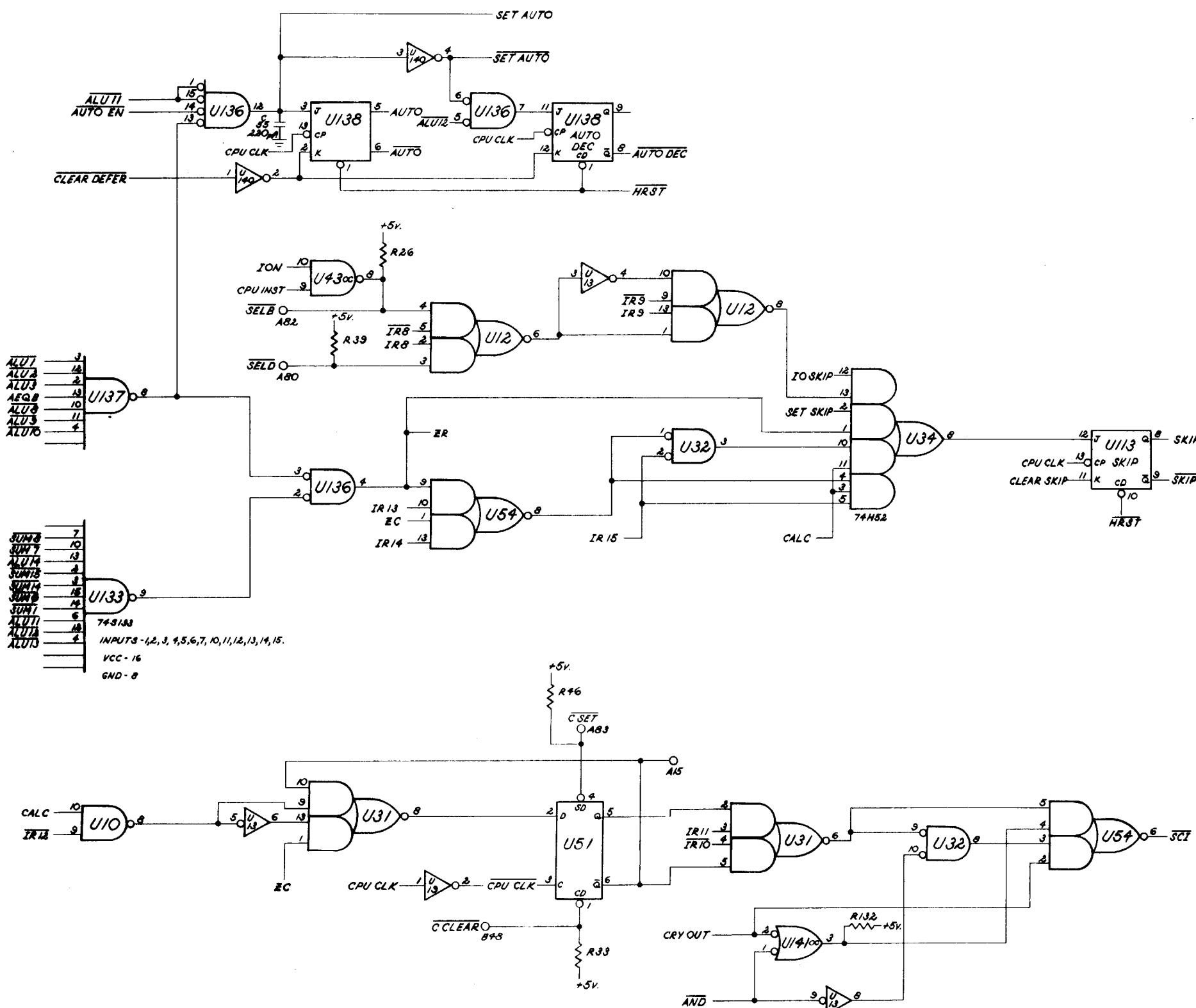


QTY	DESCRIPTION	PART NO.
1	DRAWN R. W. C. JR. 1-24 70	DATA GENERAL CORPORATION
1	CHECKED	SOUTHBORO, MASSACHUSETTS 01772
1	ENGINEER	TITLE
1	APPROVED	NOVA 2
1	FIRST USED ON	INSTRUCTION REGISTERS,
1	FINISH	INPUT MUX, OUTPUT DRIVERS,
1	SCALE	PROGRAM I/O.
1	SIZE D	DRAWING NUMBER 000363
1	CODE 001	REV. 16





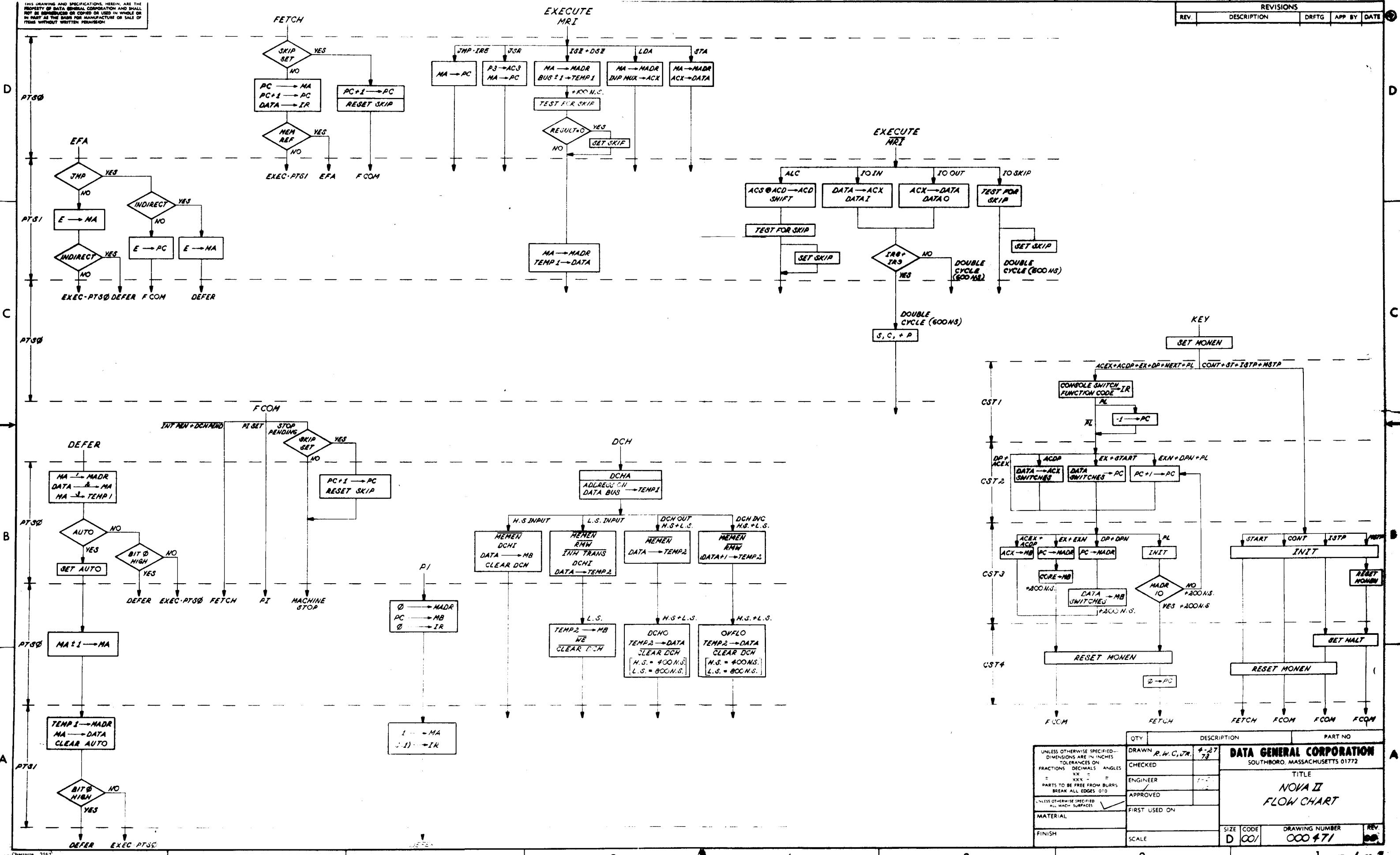
QTY.		DESCRIPTION		PART NO.
DRAWN	R. W. C. JR.	1-15	78	DATA GENERAL CORPORATION
CHECKED				SOUTH BORO, MASSACHUSETTS 01772
= XX				TITLE
± XXX ±				NOVA 2
PARTS TO BE FREE FROM BURRS				ALU SHIFTER &
BREAK ALL EDGES 0.10				ADDRESS BUFFERS
UNLESS OTHERWISE SPECIFIED -				
ALL MACH. SURFACES ✓				
MATERIAL				
FINISH				
SCALE				

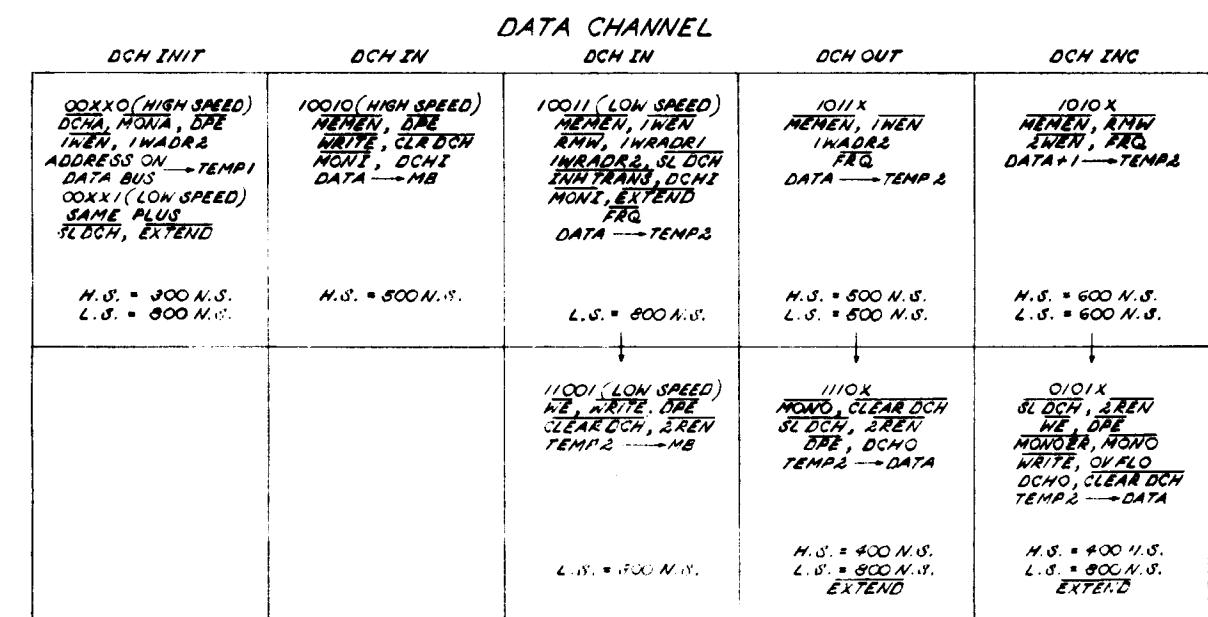
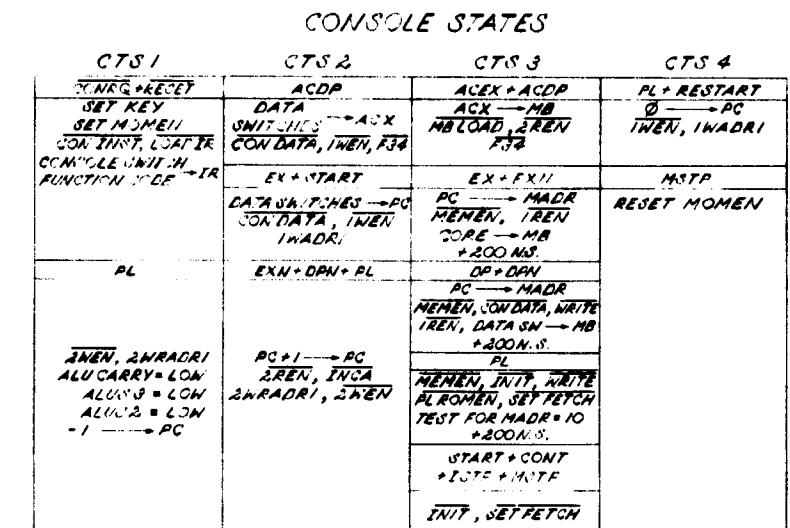
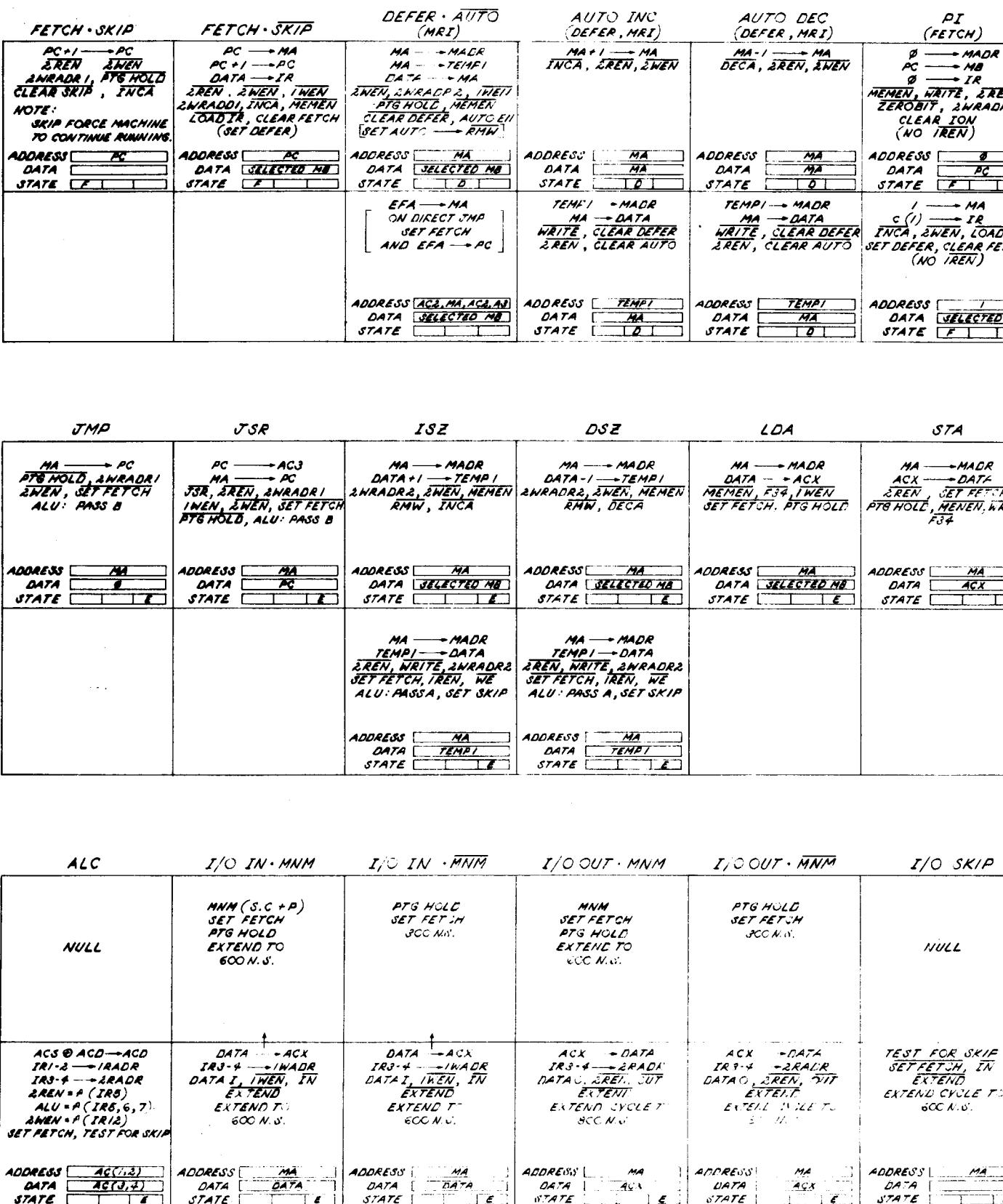


QTY.		DESCRIPTION		PART NO.
DRAWN	R. W. C. Jr.	1-25 78	DATA GENERAL CORPORATION	SOUTHBORO, MASSACHUSETTS 01772
CHECKED			TITLE	NOVA 2
ENGINEER			SKIP AUTO CARRY LOGIC	
APPROVED				
MATERIAL		FIRST USED ON		
FINISH		SCALE	SIZE	D 001
			CODE	000363
			DRAWING NUMBER	REV. 1

UNLESS OTHERWISE SPECIFIED -  
DIMENSIONS ARE IN INCHES  
TOLERANCES ON  
FRACTIONS DECIMALS ANGLES  
 $\pm$  XX  $\pm$  XX  $\pm$  XX  
= EXACT = EXACT = EXACT  
PARTS TO BE FREE FROM BURRS  
BREAK ALL EDGES .010

UNLESS OTHERWISE SPECIFIED -  
ALL MACH. SURFACES ✓





QTY	DESCRIPTION	PART NO
UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES	DRAWN <i>R. W. C. T. H.</i> 5-3: 7-3	<b>DATA GENERAL CORPORATION</b> SOUTHBORO, MASSACHUSETTS 01772 <b>TITLE</b> <i>NOVA II</i> <i>STATE CHART</i>
= XX = = = XXX = = PARTS TO BE FREE FROM BURRS BREAK ALL EDGES 010	CHECKED	
	ENGINEER /	
	APPROVED	
	FIRST USED ON	
MATERIAL		
FINISH	SCALE	SIZE CODE DRAWING NUMBER REV. D 661 200471 00

PIN #	14	DQ0	13	DQ1	12	DQ2	11	DQ3	FST	MNCZR	1	MCI	2	MNO	3	CLRDCH	4	FRQ	5	WRTE	6	EXTEND	7	NOTE	9	OCTAL CONTENTS																	
OCTAL ADDRESS	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	Y <sub>0</sub>	Y <sub>1</sub>	Y <sub>2</sub>	Y <sub>3</sub>	Y <sub>4</sub>	Y <sub>5</sub>	Y <sub>6</sub>	Y <sub>7</sub>	Y <sub>8</sub>	Y <sub>9</sub>	Y <sub>10</sub>	Y <sub>11</sub>	Y <sub>12</sub>	Y <sub>13</sub>	Y <sub>14</sub>	Y <sub>15</sub>	Y <sub>16</sub>	Y <sub>17</sub>	Y <sub>18</sub>	Y <sub>19</sub>	Y <sub>20</sub>	Y <sub>21</sub>	Y <sub>22</sub>	Y <sub>23</sub>	Y <sub>24</sub>	Y <sub>25</sub>	Y <sub>26</sub>	Y <sub>27</sub>	Y <sub>28</sub>	Y <sub>29</sub>	Y <sub>30</sub>	Y <sub>31</sub>	Y <sub>32</sub>	Y <sub>33</sub>	Y <sub>34</sub>	Y <sub>35</sub>	Y <sub>36</sub>	Y <sub>37</sub>
00	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	376								
01	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	374								
02	0	0	0	0	1	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	376								
03	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	374								
04	0	0	0	1	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	376								
05	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	374								
06	0	0	0	1	1	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	376								
07	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	374								
10	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
11	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
12	0	1	0	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	112								
13	0	1	0	1	1	0	0	1	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	110								
14	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
15	0	1	1	0	1	1	1	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	356								
16	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
17	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
20	1	0	0	0	0	0	1	1	1	1	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	356								
21	1	0	0	0	0	1	1	1	1	1	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	356								
22	1	0	0	1	0	0	1	1	0	1	0	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	252								
23	1	0	0	1	1	1	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	277								
24	1	0	1	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	367								
25	1	0	1	0	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	367								
26	1	0	1	1	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	367								
27	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	367								
30	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
31	1	1	0	0	1	1	0	1	0	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	273								
32	1	1	0	1	0	0	1	1	1	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	316								
33	1	1	0	1	1	1	1	1	1	0	0	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	314								
34	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
35	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
36	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
37	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								

- NOTES:
- 1) With chip disabled ( $C_s=1$ ), outputs are high level.
  - 2) The map is specified with pos. logic output (i.e. 1=High)
  - 3) Least significant bit of contents is  $Y_7$ .

DRAWN	DATA GENERAL CORPORATION		
CHECKED	AJMC		
<i>2/18/84</i>	TITLE DCHO		
APPROVED	32 X 8 READ ONLY MEMORY MAP		
FILED AND INDEXED	W2		
SHEET / 1 / 1	1	2	3
REV A	100	000499	100

REVISIONS	
REV	DESCRIPTION
M	

PIN #	DQ0	DQ1	DQ2	DQ3	FST	1WADR1	1WADR2	1WEN	CONNECT	2WEN	SIDCH	R/W	2REN	OCTAL CONTENTS
00	0	0	0	0	0	1	0	0	1	1	1	1	1	237
01	0	0	0	0	1	1	0	0	1	1	0	1	1	233
02	0	0	0	1	0	1	0	0	1	1	1	1	1	237
03	0	0	0	1	1	1	0	0	1	1	0	1	1	233
04	0	0	1	0	0	1	0	0	1	1	1	1	1	233
05	0	0	1	0	1	1	0	0	1	1	1	1	1	237
06	0	0	1	1	0	1	0	0	1	1	0	1	1	233
07	0	0	1	1	1	1	0	0	1	1	0	1	1	237
10	0	1	0	0	0									
11	0	1	0	0	1									
12	0	1	0	1	0	1	1	1	1	1	0	1	0	372
13	0	1	0	1	1	1	1	1	1	1	0	1	0	372
14	0	1	1	0	0									
15	0	1	1	0	1	1	1	1	1	1	1	1	1	377
16	0	1	1	1	0									
17	0	1	1	1	1									
20	1	0	0	0	0	1	1	1	1	1	1	1	1	377
21	1	0	0	0	1	1	1	1	1	1	1	1	1	377
22	1	0	0	1	0	1	1	1	1	1	1	1	1	377
23	1	0	0	1	1	0	0	0	1	1	0	0	1	031
24	1	0	1	0	0	1	1	1	1	0	1	0	1	365
25	1	0	1	0	1	1	1	1	1	0	1	0	1	365
26	1	0	1	1	0	0	0	0	0	1	1	1	1	017
27	1	0	1	1	1	0	0	0	0	1	1	1	1	017
30	1	1	0	0	0									
31	1	1	0	0	1	1	1	1	1	1	1	1	1	377
32	1	1	0	1	0	1	1	1	0	1	0	1	1	353
33	1	1	0	1	1	1	1	1	0	1	0	1	1	353
34	1	1	1	0	0									
35	1	1	1	0	1									
36	1	1	1	1	0									
37	1	1	1	1	1									

- NOTES:
- 1) With chip disabled ( $C_S=1$ ), outputs are high level.
  - 2) The map is specified with pos. logic output (i.e. 1=High)
  - 3) Least significant bit of contents is  $Y_7$ .

DRAWN		DATA GENERAL CORPORATION	
CHECKED		UNCC	
<i>[Signature]</i>		DCH1	
APPROVED		32 X 8 READ ONLY	
		MEMORY MAP	
FIRST USED IN	N12	DRAWING NUMBER	
SHEET /	/	A100	000500

4

3

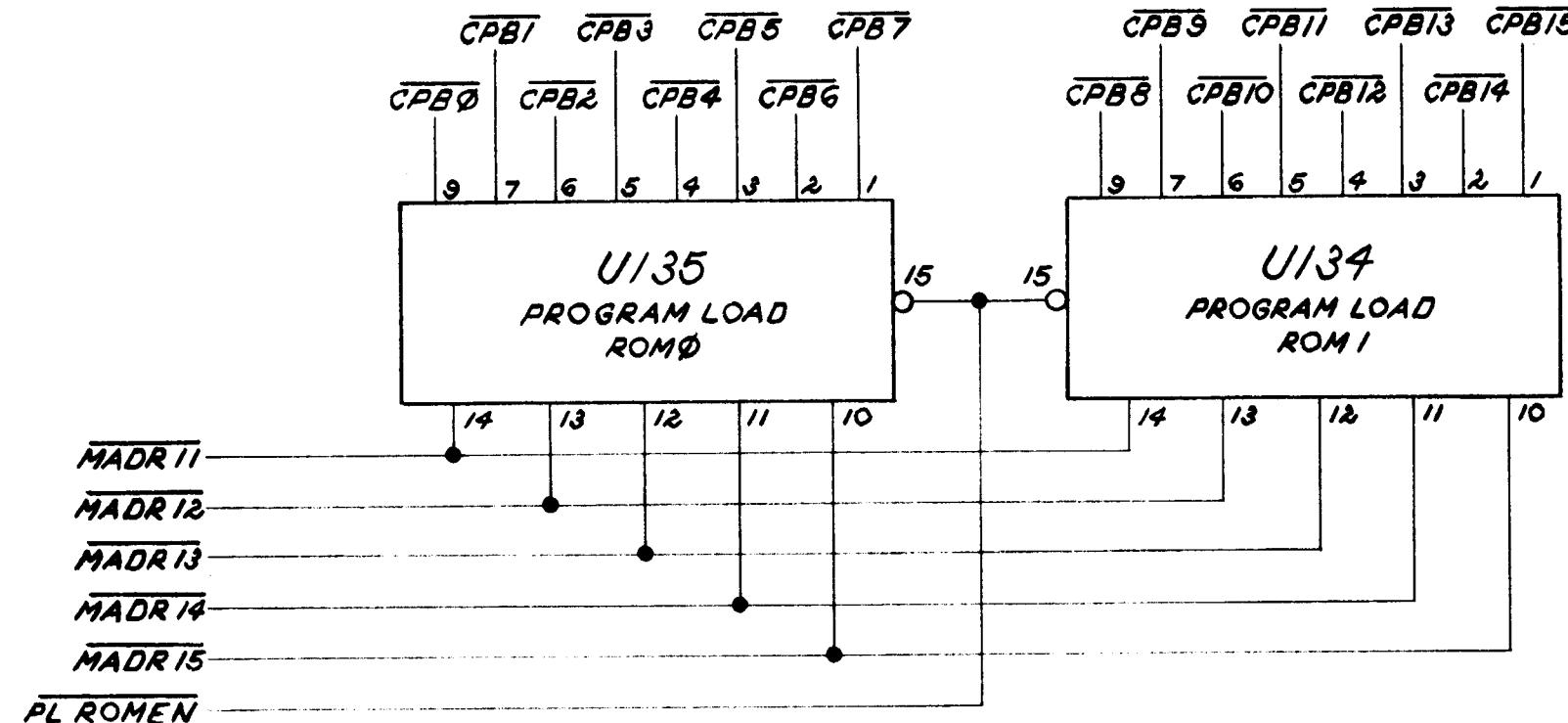
2

1

THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

## REVISIONS

REV.	DESCRIPTION	DRFTG.	APP. BY	DATE
O1	ECO # 1949	R.W.C.		9-27 73



QTY.	DESCRIPTION		PART NO.
UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES $\pm .XX \pm .XX \pm .XX$ PARTS TO BE FREE FROM BURRS BREAK ALL EDGES .010	DRAWN R.W.C.JR.	9-10 73	<b>DATA GENERAL CORPORATION</b> SOUTHBORO, MASSACHUSETTS 01772
CHECKED			
ENGINEER		9-10 73	
APPROVED			
FIRST USED ON	8308		TITLE <b>NOVA 2 PROGRAM LOAD</b>
MATERIAL	SIZE B	CODE 001	DRAWING NUMBER 000512
FINISH	SCALE	REV. O1	

4

3

2

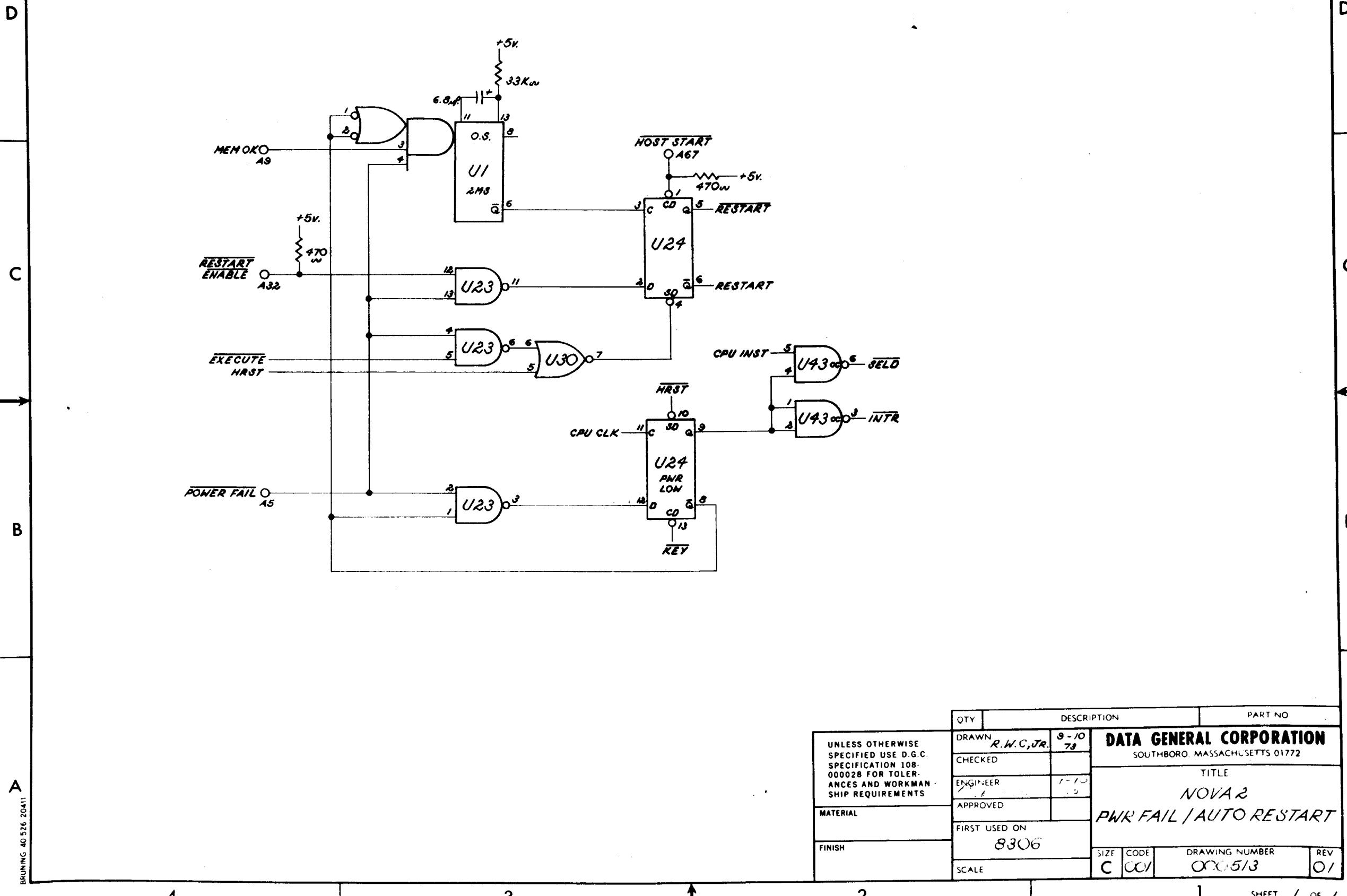
1 SHEET / OF 1

B

A

THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION

REVISIONS				
REV	DESCRIPTION	DRFTG	APP BY	DATE
O/	ECO # 1949	R.W.C.		9-27

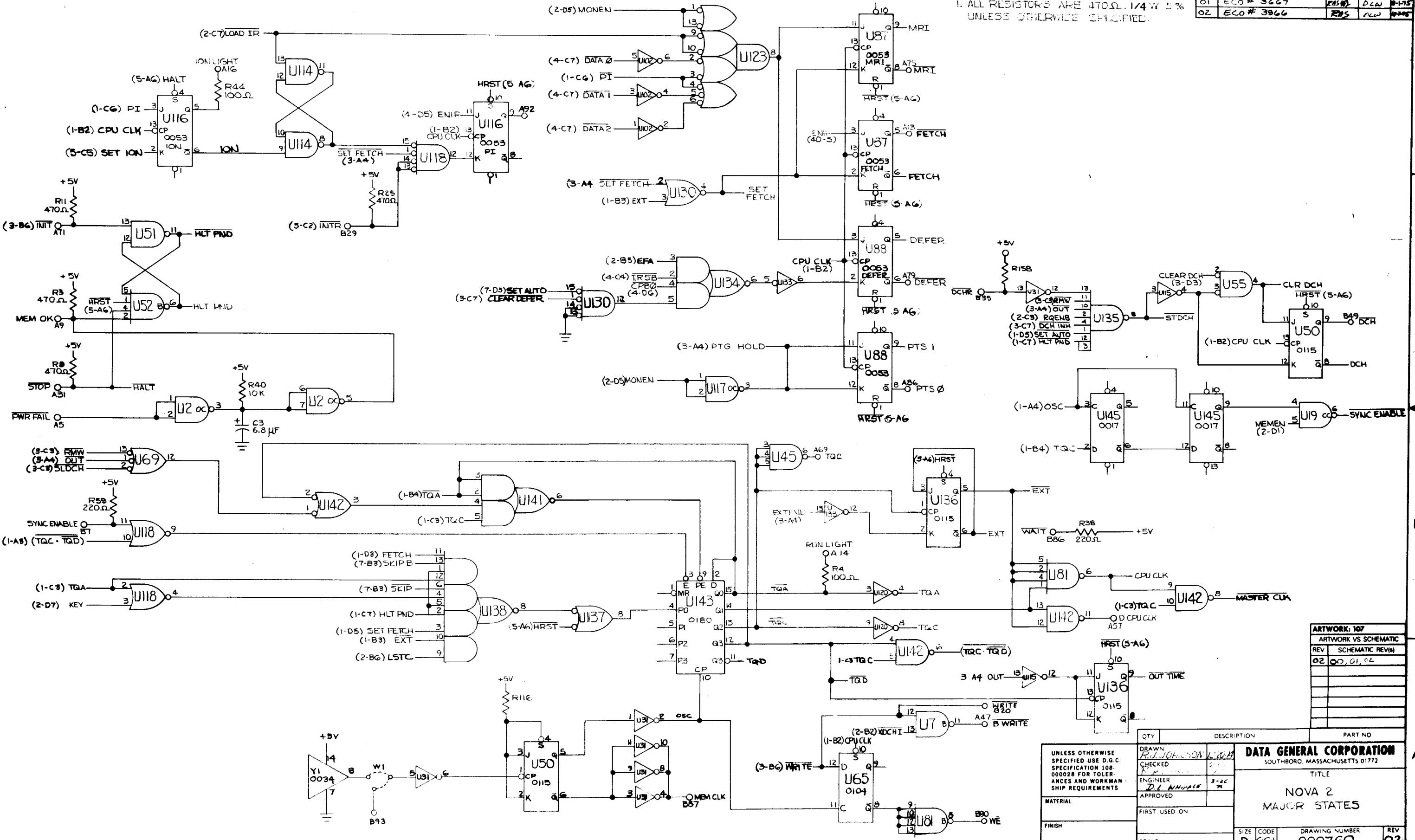


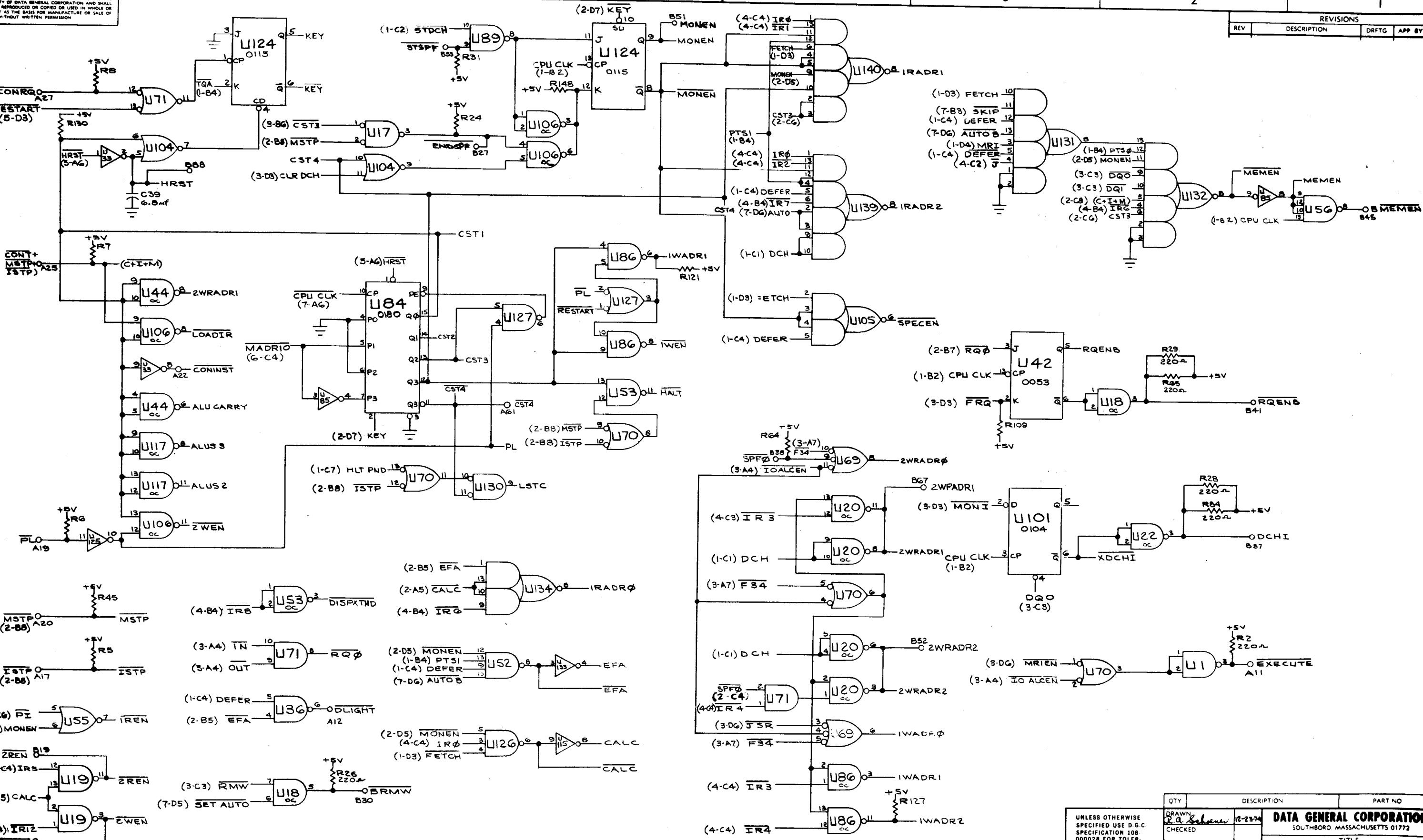
QTY	DESCRIPTION		PART NO
UNLESS OTHERWISE SPECIFIED USE D.G.C. SPECIFICATION 108-000028 FOR TOLERANCES AND WORKMANSHIP REQUIREMENTS	DRAWN	R.W.C., JR.	9-10 78
MATERIAL	CHECKED		
FIRST USED ON	ENGINEER		7-10 78
FINISH	APPROVED		
SCALE	SHEET / OF /	DRAWING NUMBER	REV
		000-513	O/

DATA GENERAL CORPORATION  
SOUTHBURY, MASSACHUSETTS 01772

TITLE  
NOVA 2  
PWR FAIL/AUTO RESTART

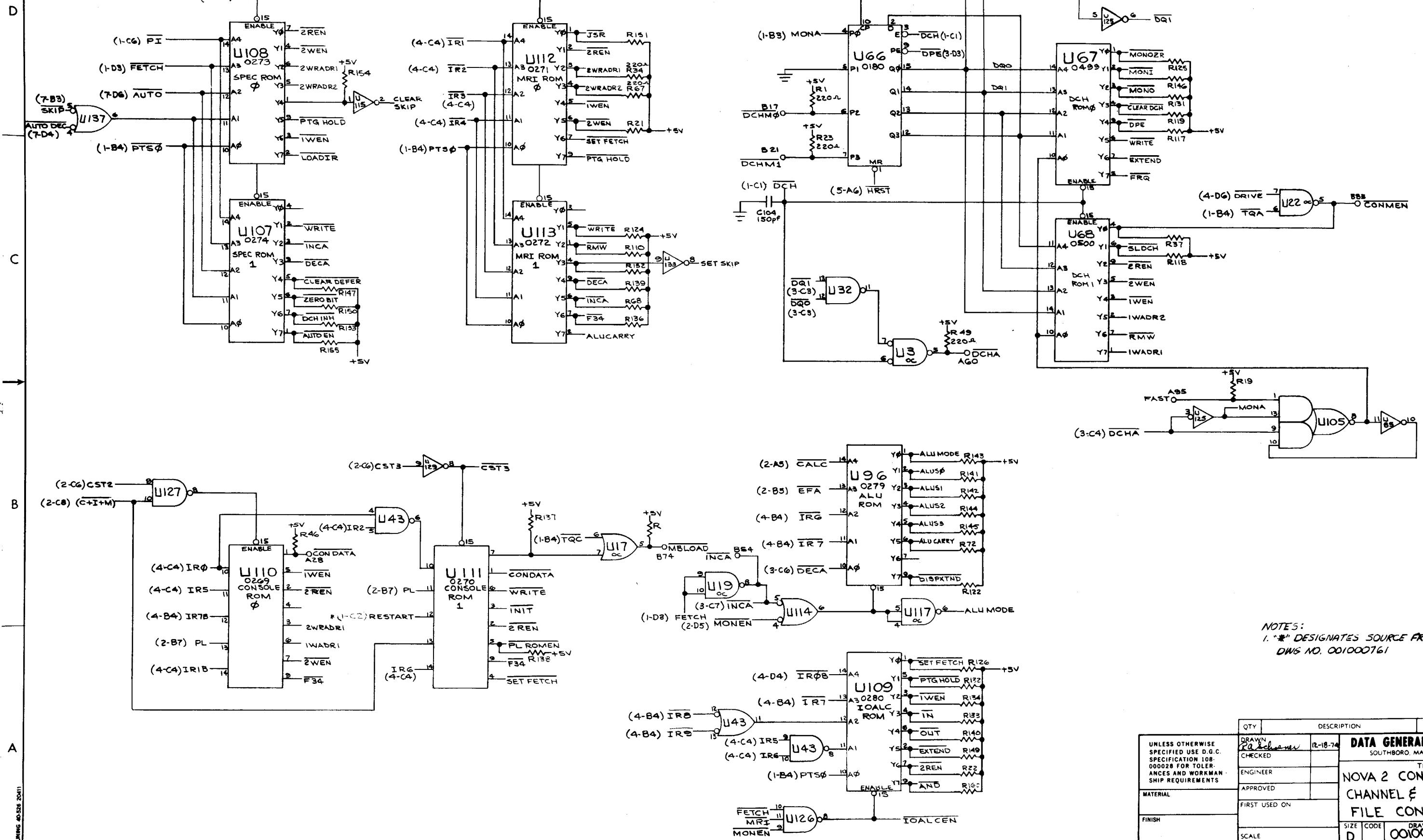
REVISIONS				
REV.	DESCRIPTION	DRFTG	APP BY	DATE
00	ORIGINATED			
01	ECO # 3667	EAS	DCLW	8-17-75
02	ECO # 3966	EAS	DCLW	10-20-75





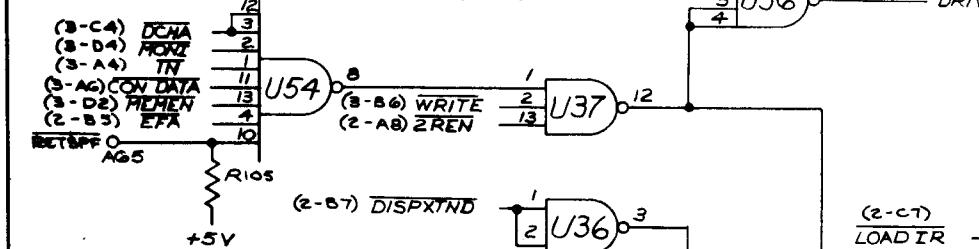
D

D

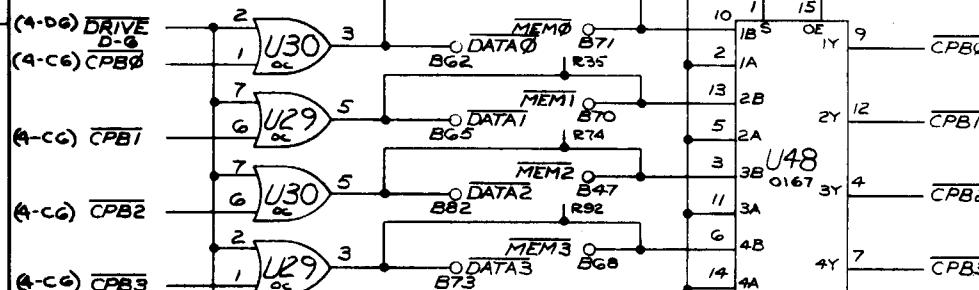


QTY	DESCRIPTION	PART NO
DRAWN checked	12-18-74	DATA GENERAL CORPORATION
CHECKED		SOUTHBORO, MASSACHUSETTS 01772
ENGINEER		TITLE
APPROVED		NOVA 2 CONSOLE, DATA
MATERIAL		CHANNEL & REGISTER
FIRST USED ON		FILE CONTROL
FINISH		
SCALE		SIZE CODE DRAWING NUMBER REV
D	001000760	02

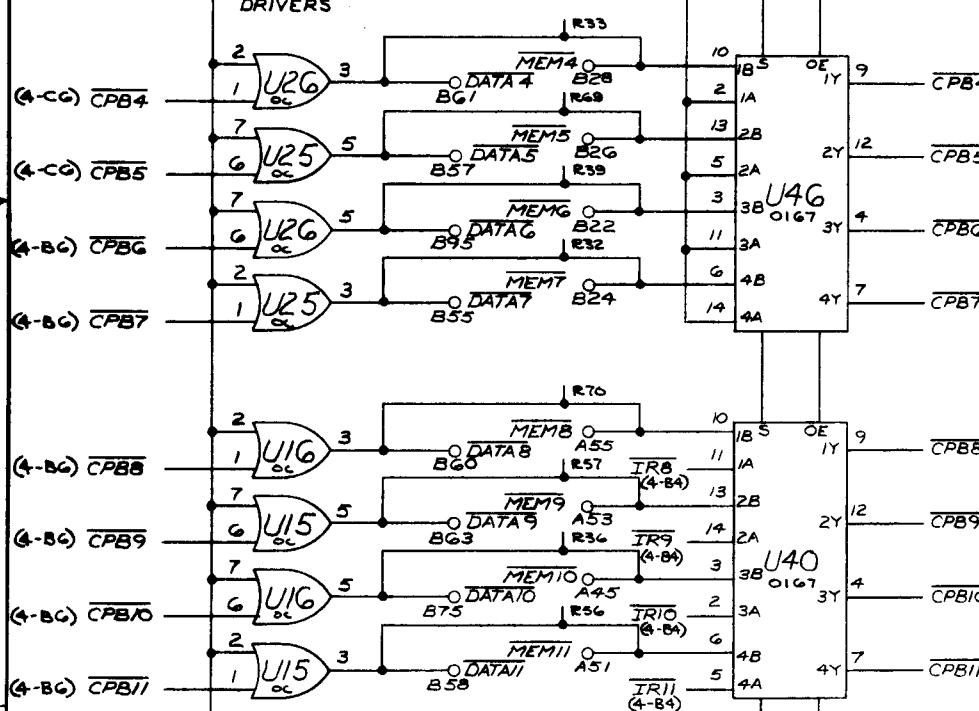
D



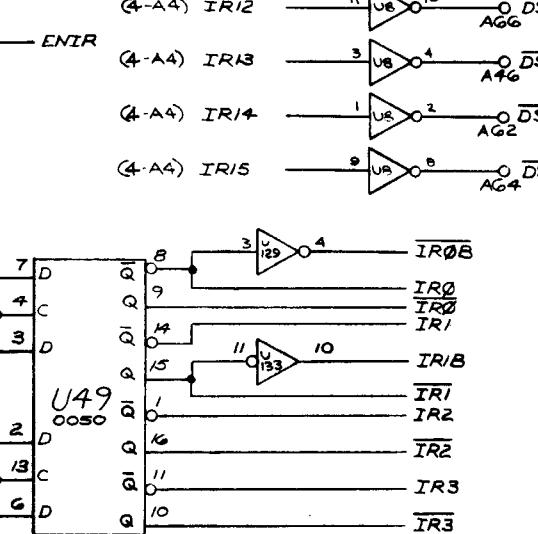
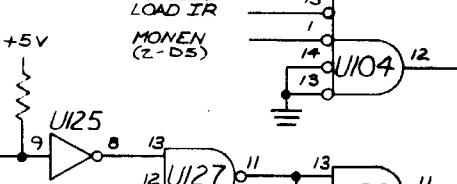
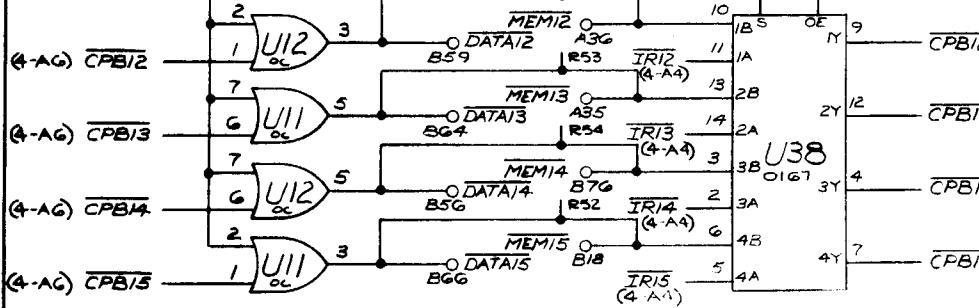
C



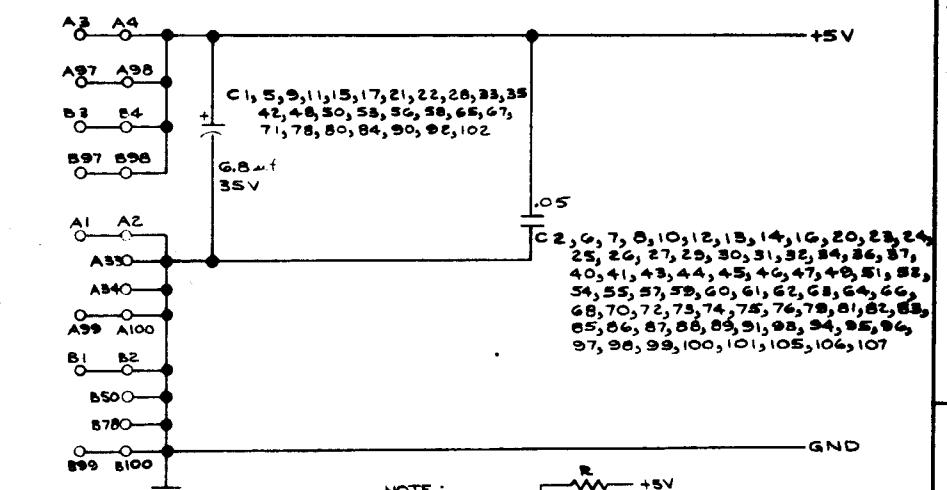
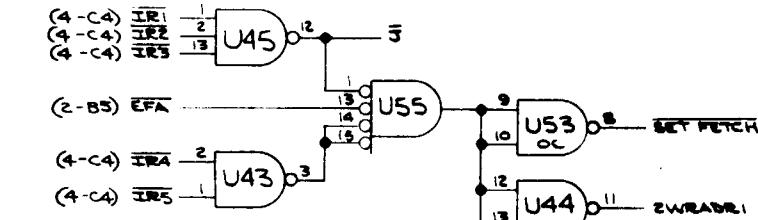
B



A



DEVICE  
SELECT  
DRIVERS

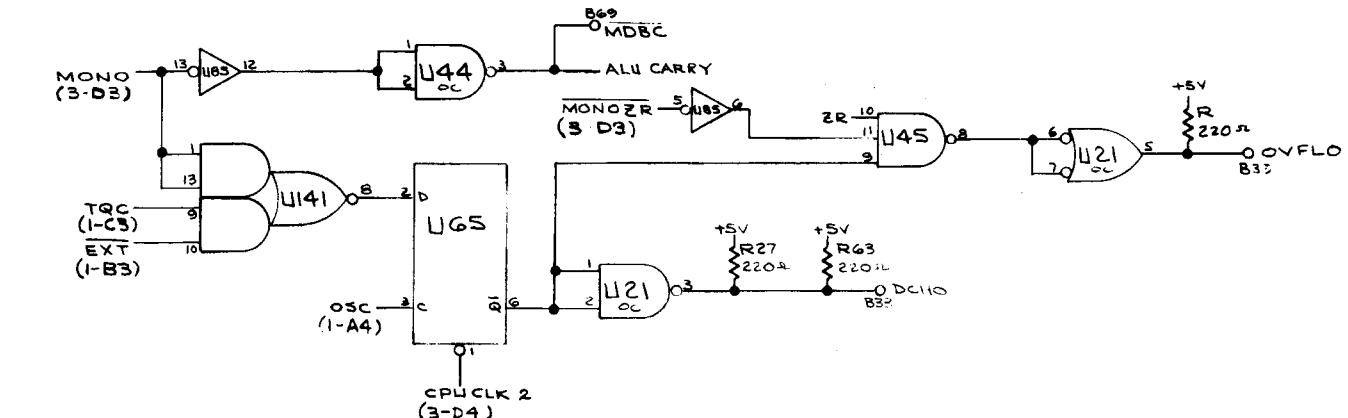
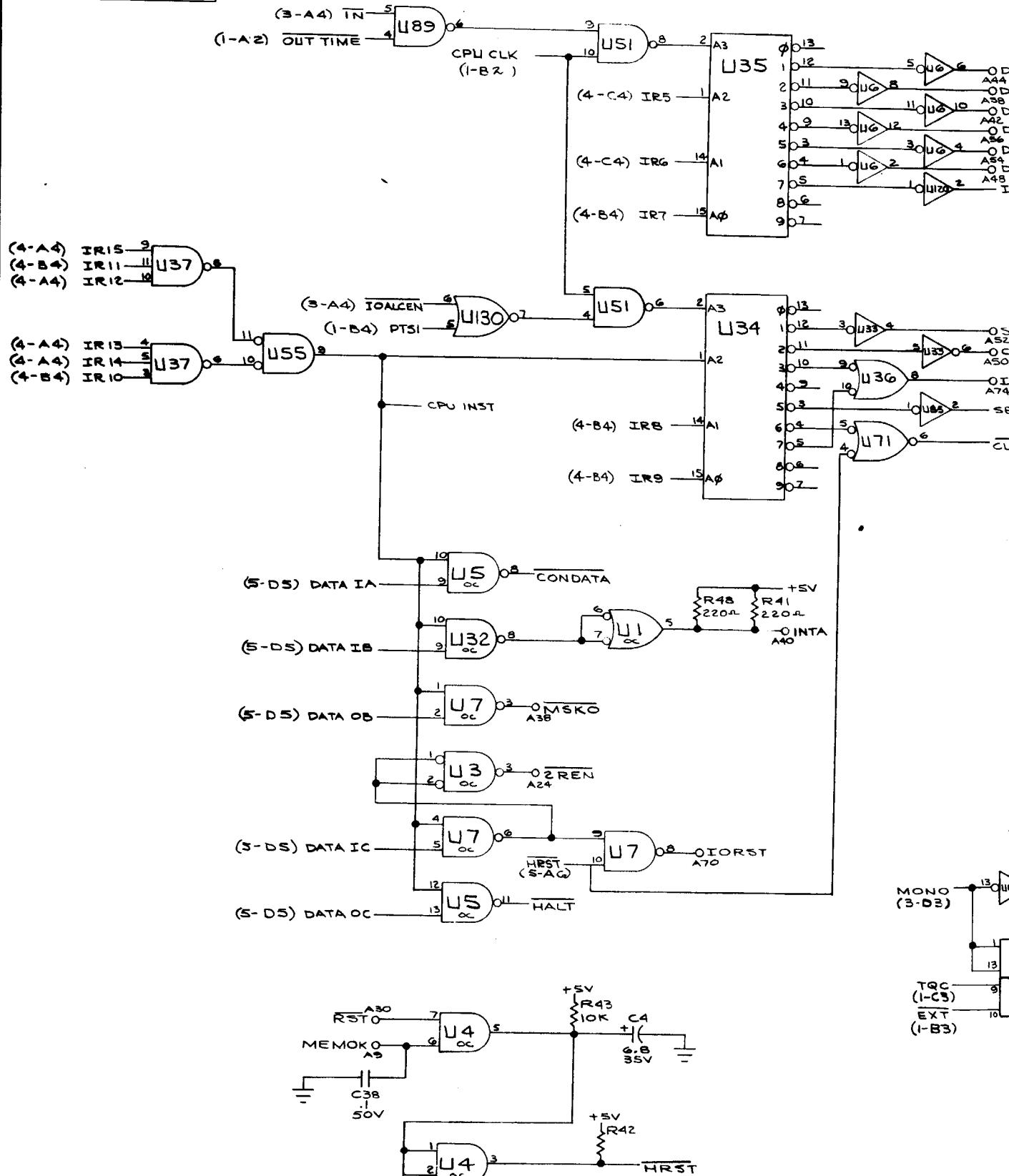


QTY	DESCR PTN	PART NO
1	DRAW 02 Johnsen 12-23-74	DATA GENERAL CORPORATION
1	CHECKED	SOUTHBORO MASSACHUSETTS 01772
1	ENGINEER	
1	APPROVED	
1	FIRST USED ON	
1	SCALE	SIZE CODE DRAWING NUMBER REV
D	001 000 760	02

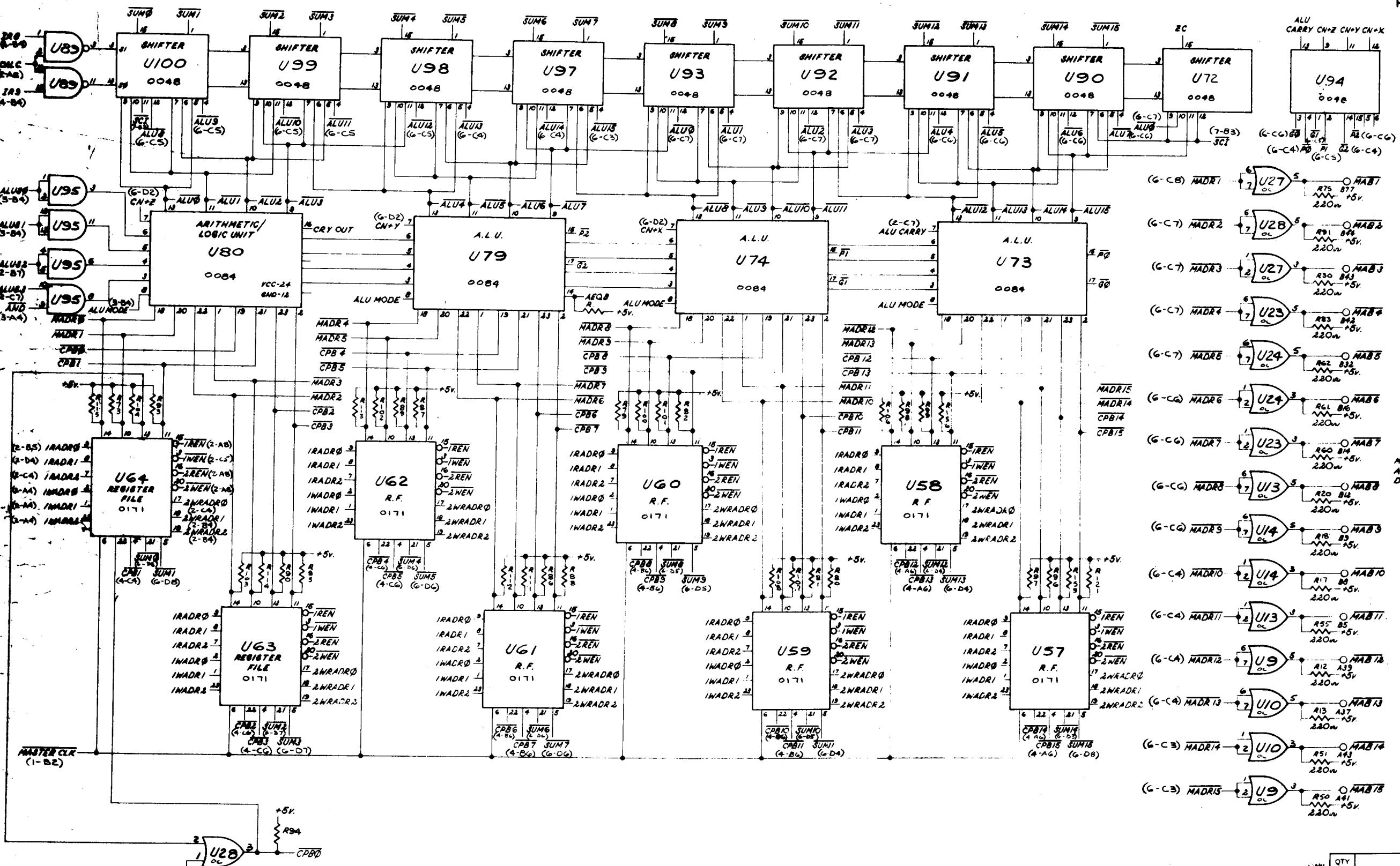
UNLESS OTHERWISE SPECIFIED USE D.G.C. SPECIFICATION 108-000028 FOR TOLERANCES AND WORKMANSHIP REQUIREMENTS

NOTE: 1.  $R = 300\Omega$

TITLE  
NOVA 2  
INSTRUCTION REGISTERS & INPUT MUX



QTY	DESCRIPTION	PART NO
UNLESS OTHERWISE SPECIFIED USE D.G.C. SPECIFICATION 108: 000028 FOR TOLER- ANCES AND WORKMAN- SHIP REQUIREMENTS	DRAWN <i>DA 108 Rev 1</i>	DATA GENERAL CORPORATION WALTHAM MASSACHUSETTS 01772
MATERIAL	CHECKED	TITLE
FINISH	ENG-NER	NOVA 2
	APPROVED	PROGRAM I/O & OUTPUT DRIVERS
	FRST USED ON	
SCALE	DATE CODE	DRAW. NO. NUMBER
	D	001 000 760 02

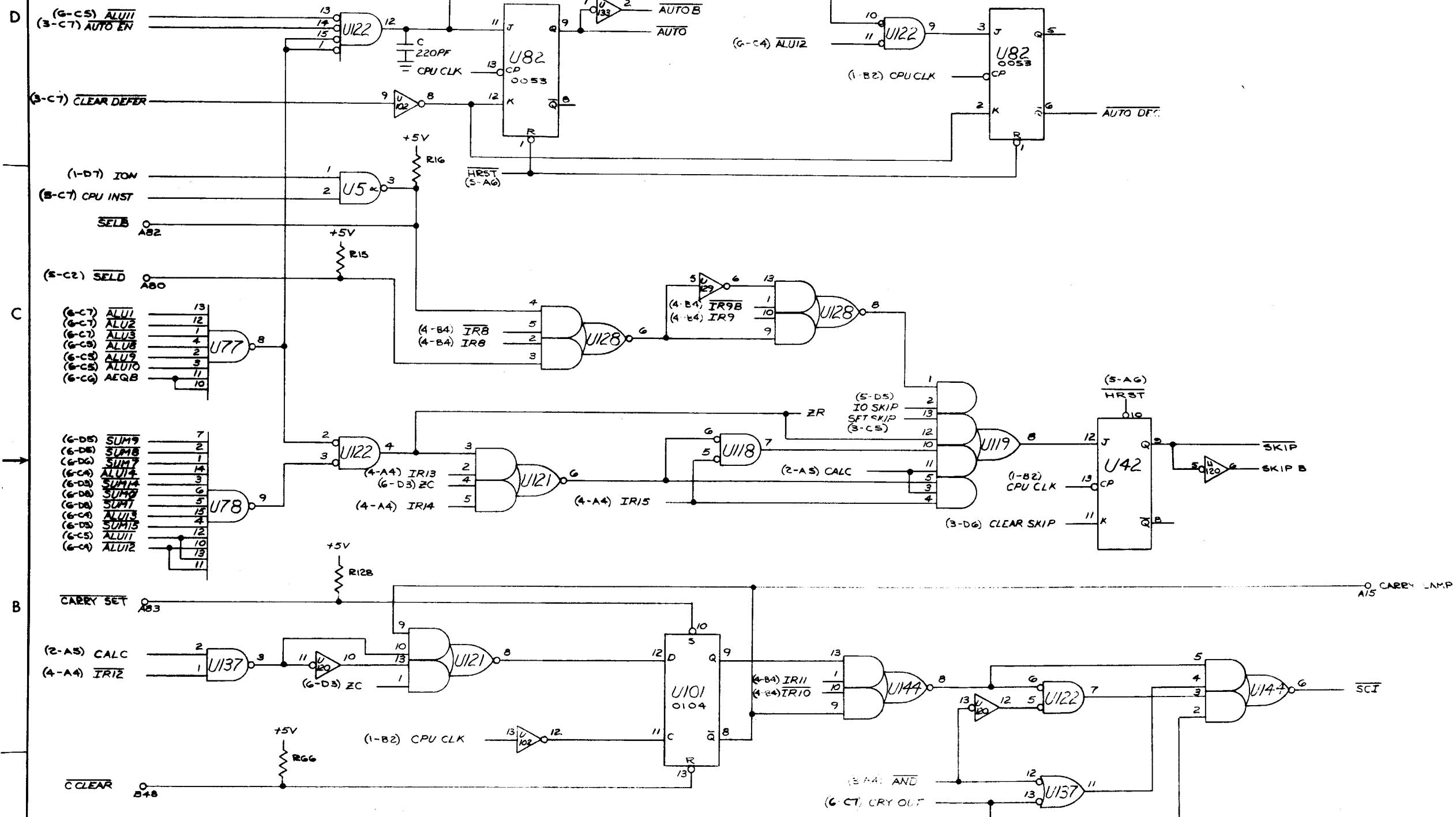


QTY	DESCRIPTION	PART NO.
1	DRAWN R.G.Schoenew 12-29-74 CHECKED	
1	FRACTIONS DECIMALS ANGLES = XXX = PARTS TO BE FREE FROM BURRS BREAK ALL EDGES 0.10	
1	UNLESS OTHERWISE SPECIFIED ALL MACH SURFACES ✓	
1	ENGINEER	
1	APPROVED	
1	MATERIAL	
1	FIRST USED ON	
1	FINISH	
1	SCALE	

**DATA GENERAL CORPORATION**  
SOUTHBROOK, MASSACHUSETTS 01772

**TITLE**  
**NOVA 2**  
**ALU SHIFTER &**  
**ADDRESS BUFFERS**

**SIZE** CODE DRAWING NUMBER  
D 001 000 760

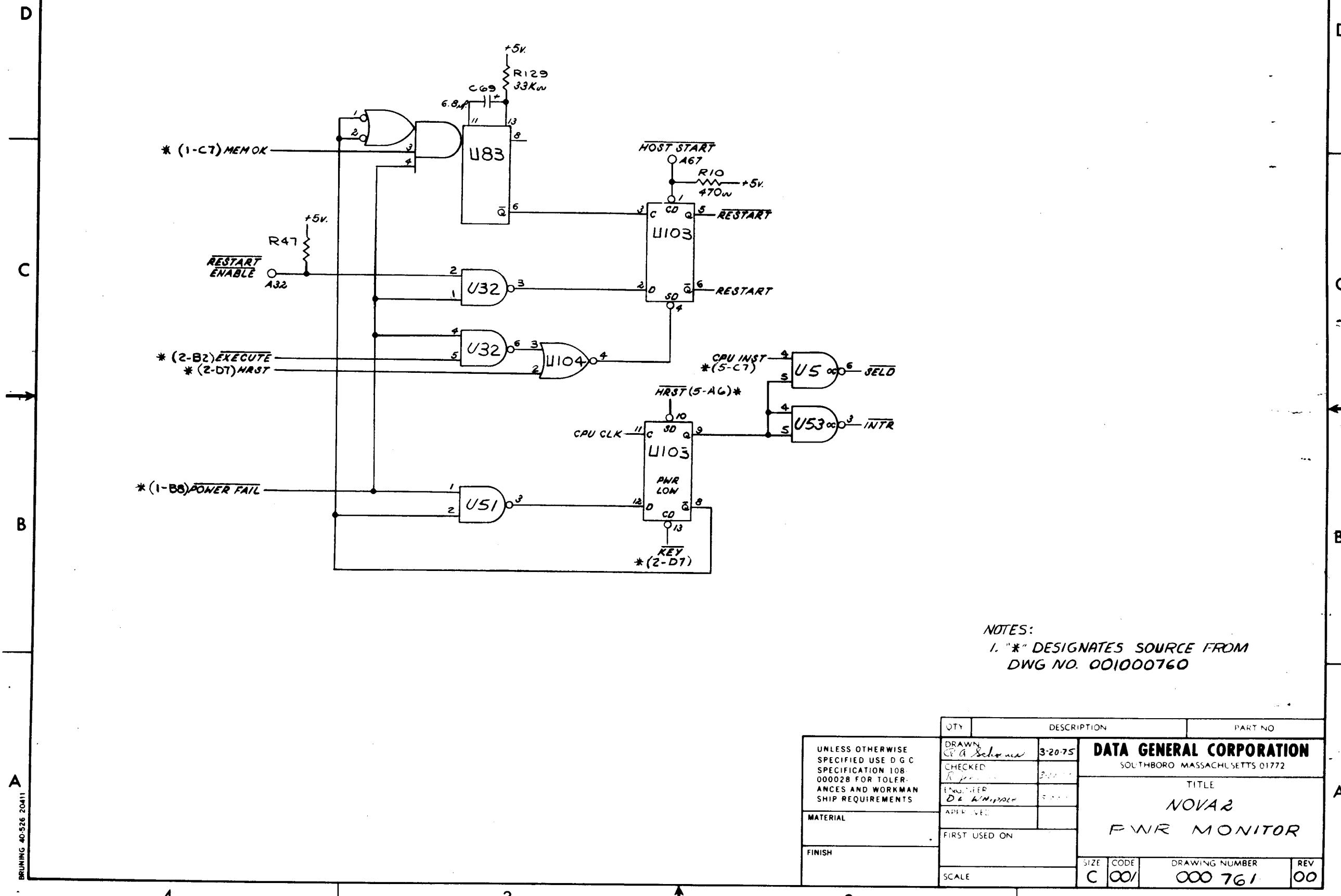


QTY	DESCRIPTION	PART NO
DATA GENERAL CORPORATION SOUTHBORO MASSACHUSETTS 01772		
UNLESS OTHERWISE SPECIFIED USE D.G.C. SPECIFICATION 108-000028 FOR TOLERANCES AND WORKMANSHIP REQUIREMENTS		
MATERIAL		
FIRST USED ON		
FINISH		
SCALE		
SIZE	CODE	DRAWING NUMBER
D		GCI 000 760 02

THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION, AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION

REVISIONS

REV	DESCRIPTION	DRFTG	APP BY	DATE
-----	-------------	-------	--------	------

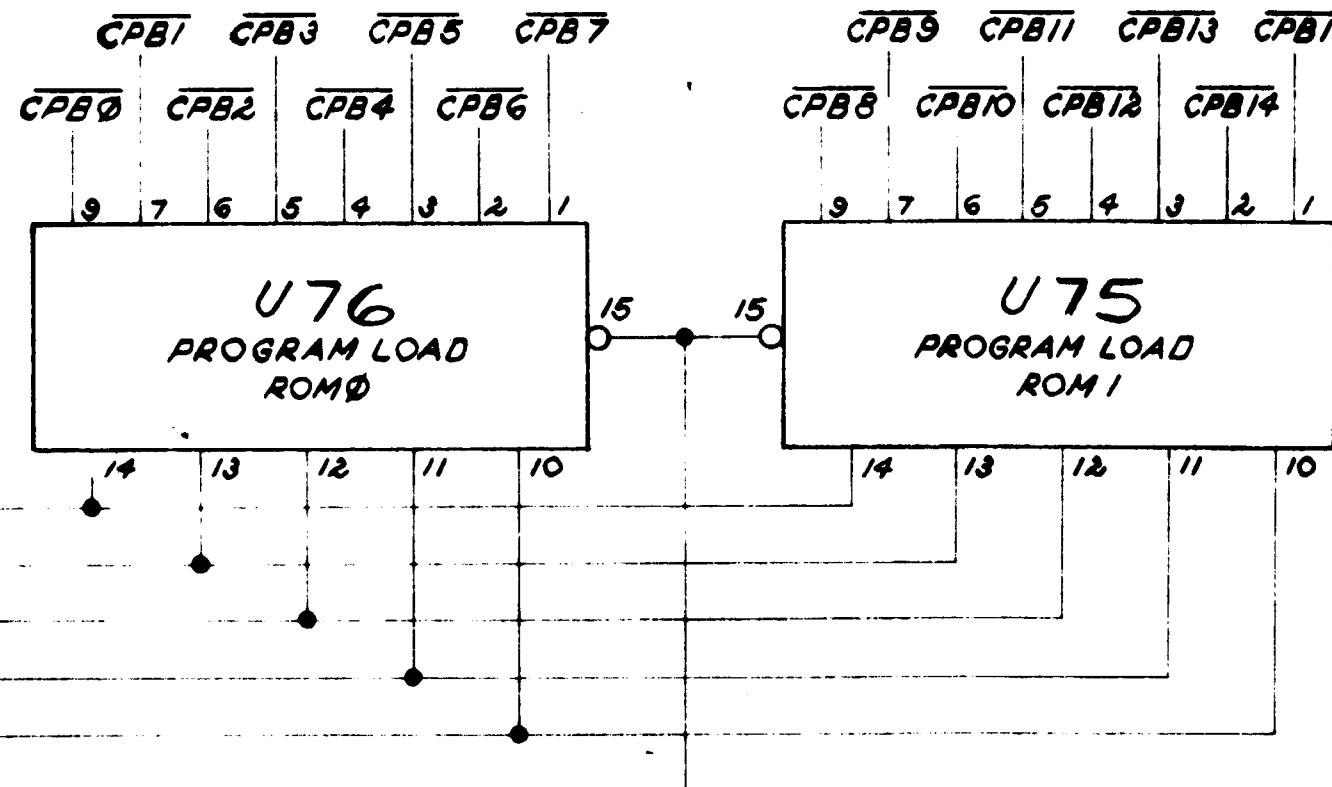


THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION

REVISIONS

REV.	DESCRIPTION	DRFTG.	APP. BY	DATE
01	ECO # 4027	RAS	DW	11/1/75

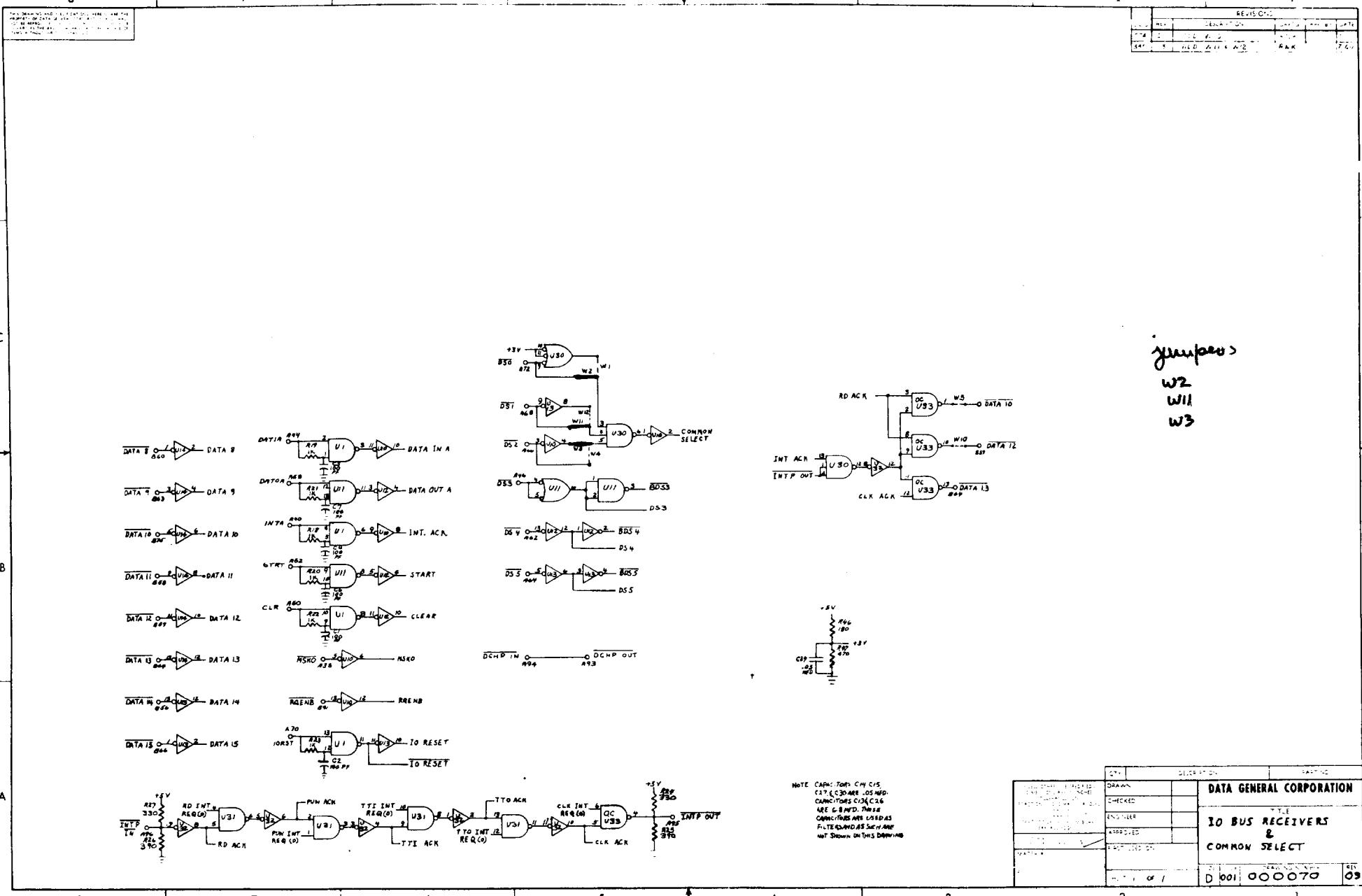
\* (6-C3 THRU C7)

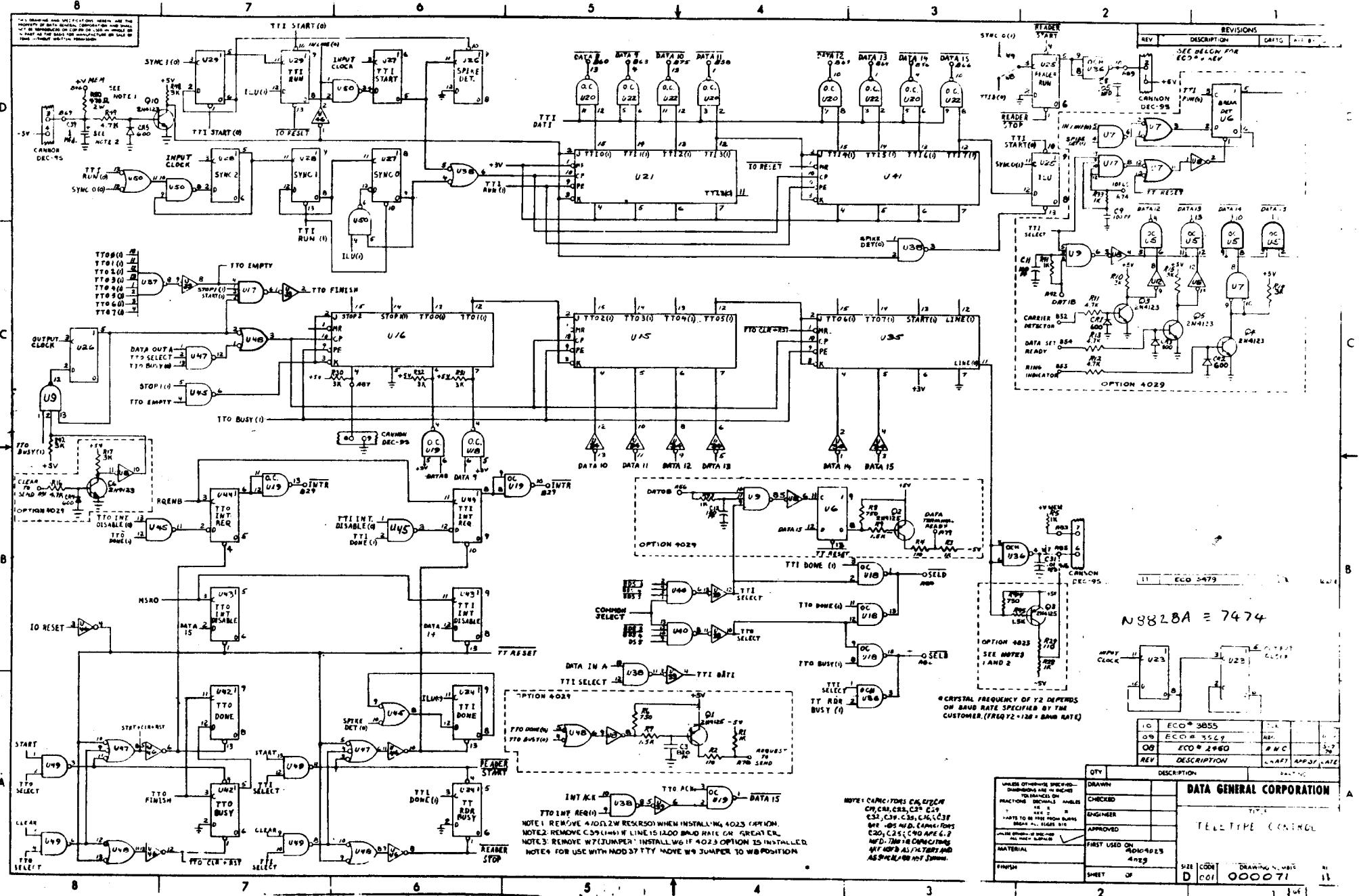


NOTES:

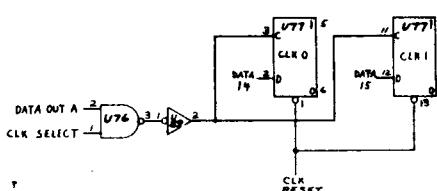
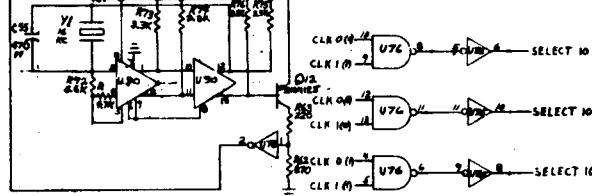
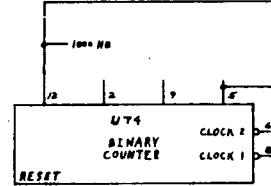
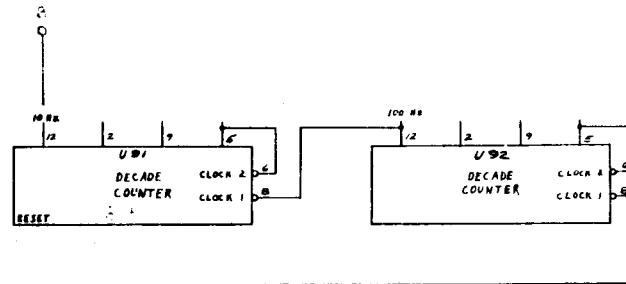
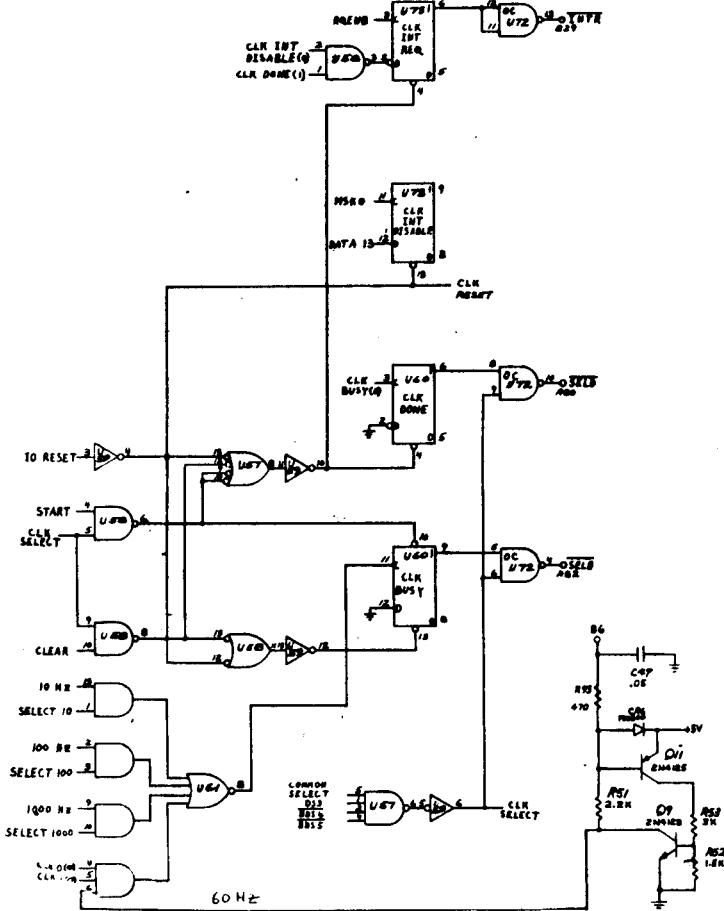
1. \* DESIGNATES SOURCE FROM DWG NO. 001000760

QTY	DESCRIPTION		PART NO.
UNLESS OTHERWISE SPECIFIED -- DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONAL DECIMALS ANGLES XX.XXXXXX XXX.XXXXXX PARTS TO BE FREE FROM BURRS. BREAK ALL EDGES 0.00	DRAWN 3-27-75	CHECKED 3-27-75	<b>DATA GENERAL CORPORATION</b> SOUTHBORO, MASSACHUSETTS 01772
UNLESS OTHERWISE SPECIFIED -- ALL MACHINED REAR	ENGINEER D.L. WHIPPLE 3-27-75	APPROVED ✓	
MATERIAL	FIRST USED ON		TITLE  <b>NOVA 2</b> <b>PROGRAM LOAD</b>
FINISH	SCALE	SIZE B	
	DRAWING NUMBER 000762		REV. 01





THIS DRAWING AND INFORMATION CONTAINED HEREIN ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE COPIED OR DISCLOSED EXCEPT AS AUTHORIZED IN WRITING BY DATA GENERAL CORPORATION OR AS REQUIRED IN THE COURSE OF MANUFACTURE OR SALE OF THIS EQUIPMENT.



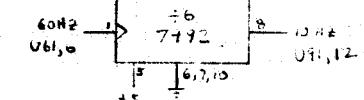
NOTE: CAPACITORS C33, C39, C45, C46, C48, C52, C58, C70, AND C71 ARE 10PF.  
C55, C57 AND C69 ARE 100PF.  
TIME CAPACITORS ARE USED AS FILTERS AND AS SUCH ARE NOT SHOWN ON THIS DRAWING.

REVISONS			
REV	DESCRIPTION	DRTG	APP BY DATE
02	ECO 028	RNC	1/27/68
03	ECO 033	RNC	1/27/68
04			
05	ECO #1595	RNC	5/1/68
06	ECO #2460	RNC	5/1/68

### MODIFICATIONS

29 JAN 68 K CHIN

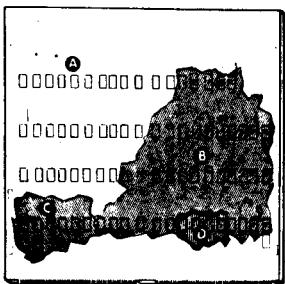
REMOVED U71



QTY	DESCRIPTION	PART NO
1	DATA GENERAL CORPORATION	
1	DRAWN	
1	CHECKED	
1	ENGINEER	
1	APPROVED	
1	FIRST USED ON	
1	FINISH	
SHEET 1 OF 1		
D 001	DRAWING NUMBER	REV 06

SIZE 11x17 D 001 DRAWING NUMBER 000074 REV 06

## SUBSYSTEM COMPONENT BREAKDOWN



DS-03094



### MAJOR COMPONENT

Item	Component	Mounting Location	Notes
A	BASIC I/O INTERFACE	COMPUTER CHASSIS	
B	ASYNC LINE CONTROLLER	BASIC I/O INTERFACE	FOR ZONE CURRENT LOOP APPLICATIONS AT 110 BAUD
C	EIA INTERFACE	ASYNC LINE CONTROLLER	FOR EIA APPLICATION AT 110 BAUD OR HIGHER
D	PRECISION CRYSTAL OSCILLATOR	ASYNC LINE CONTROLLER	FOR 75-9600 BAUD

### CABLE

Item	Cable	Connecting	Max Allowed Length ft/m	Notes
E	INTERNAL	ASYNC LINE CONTROLLER and DEVICE CABLE	N/A	NEEDED FOR SECOND CONTROLLER OR WHEN PREFERRED SLOT IS NOT USED. PADDLEBOARD STYLE SAME AS ABOVE SOCKET.
F	INTERNAL	ASYNC LINE CONTROLLER " DEVICE CABLE	N/A	(STYLE)

## SPECIFICATIONS OF CHASSIS MOUNTED COMPONENTS

Item	Component	Chassis	Slots Required	Max Allowable Data Channel Latency ( $\mu$ sec)	Type of Data Channel Service Desired High Speed, Standard	Max Allowable Programmed I/O Latency *	Controller's +5 Volt Current Draw (Amps)
A	BASIC I/O AND ASYNC LINE CONTROLLER	COMPUTER	1	N/A	N/A	21.6ms @ 110 BAUD TO 0.24ms @ 9600 BAUD	0.25 FOR INTERFACE PLUS 0.7 FOR ASYNC LINE CONT

\*DATA WILL BE LOST IN INPUT IF THESE LATENCIES ARE EXCEEDED

### CONFIGURATION RULES

- If a system has one or more TTYs, one (device code 10) must be assigned the primary slot (3, 4, or 5, depending on computer type).
- If a system has one or more 6012s, Infotrons, or other CRT displays, and no TTY, one (device code 10) must be assigned the primary slot.
- If a system has one or more Low Cost Displays and none of the above, then one (device code 10) must be assigned the primary slot.
- If a system has one or more Sprints (with keyboard) and none of the above, then one (device code 10) must be assigned the primary slot.
- If a system has none of the above, then it is assumed to have no console device, and any other constraints and rules can be followed without diagnostic impact.

The above assumes that the Interface boards required are standard DG types (ALM and Data Channel Line Printer Interfaces excluded). In these cases, No. 5 applies.

013 COOMO  
DRAWING NO. DS-03094

THIS DRAWING AND SPECIFICATIONS HEREIN ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHOULD NOT BE REPRODUCED OR COPIED OR DISCLOSED WITHOUT THE WRITTEN PERMISSION OF DATA GENERAL CORPORATION. IT IS PART OF THE BASIS FOR MANUFACTURE OR SALE OF ITEMS THEREIN.

REV 000 01

ECO 5372605Y

APP - 1000

DATE 11/12/74

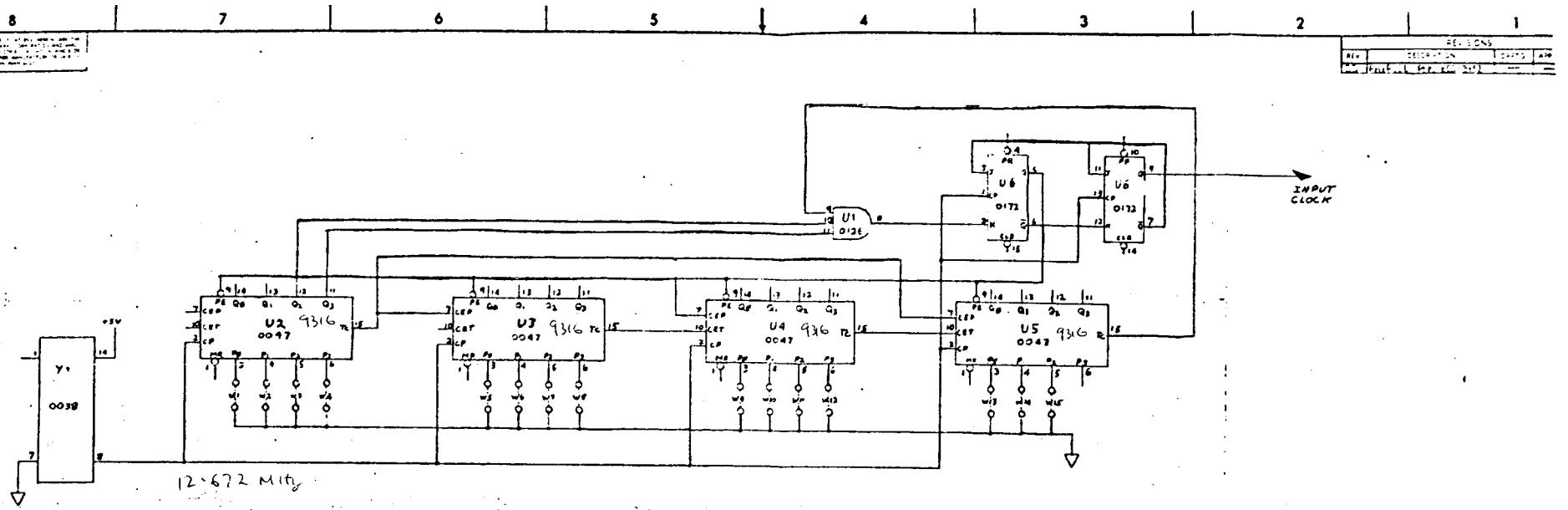
DRAFTER [Signature] APPROVED  
CHECKED [Signature] FIRST USED ON  
ENGINEER [Signature] CODE IDENT 34984

TITLE  
ASYNCHRONOUS  
CONTROLLER

DATA GENERAL CORPORATION  
SOUTHBORO MASSACHUSETTS 01772

SIZE CODE DRAWING NUMBER  
C 010 000115 REV 01

SHEET 1 OF 2



12.672 Mity

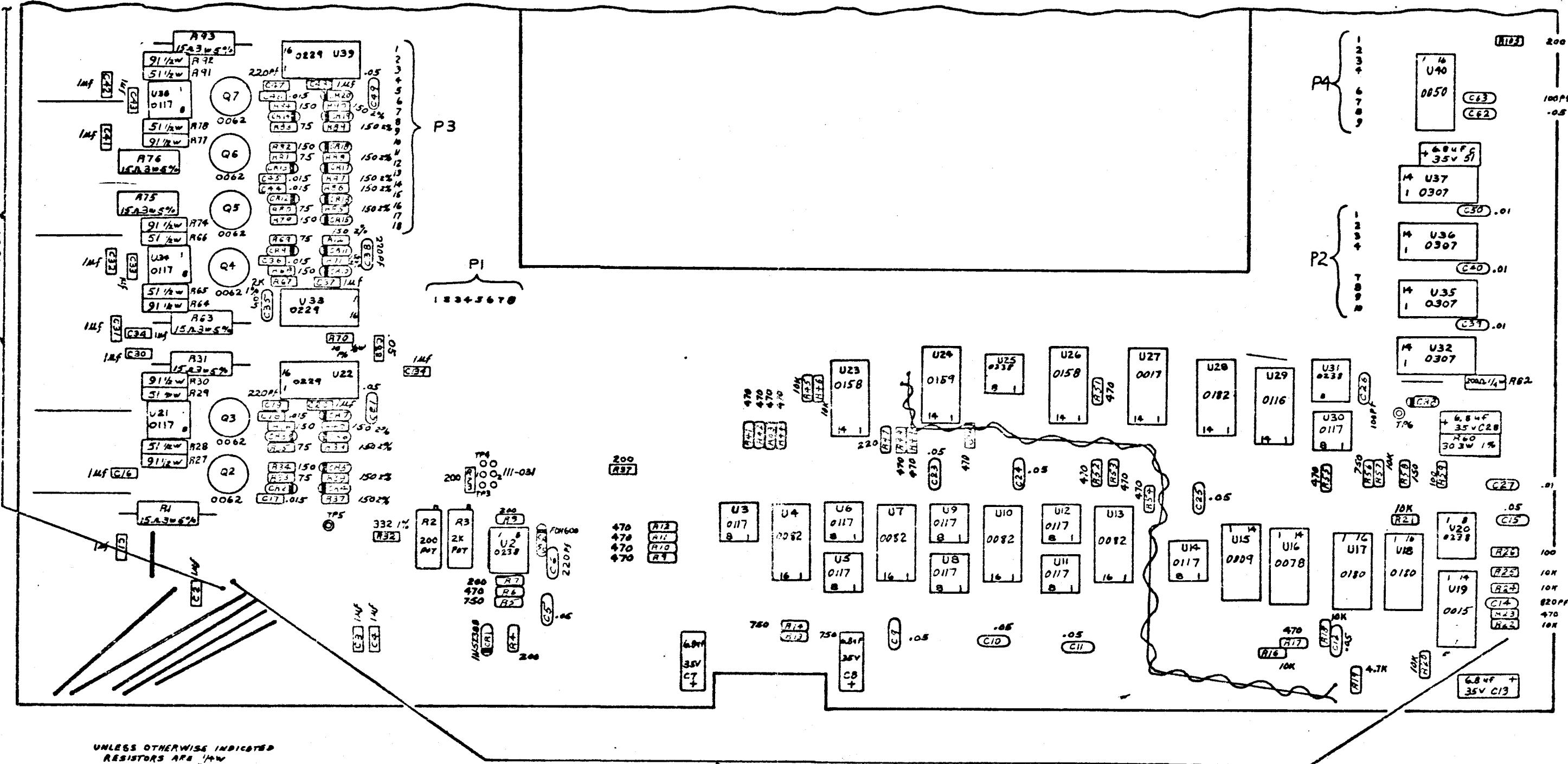
.9316 ≈ 74.161

BAUD RATE	INSTALL JUMPERS
75	W9, W10, W12, W17
110	W7, W12, W13, W14
134.5	W6, W4, W7, W8, W9, W11, W12
150	W7, W9, W12, W14
300	W6, W8, W11, W13
600	W5, W7, W8, W12
1200	W4, W6, W8, W11
1800	W8, W6, W7, W9, W10
2400	W3, W5, W9, W10
4800	W2, W4, W7, W9
7200	W3, W4, W5, W7, W8
9600	W1, W3, W6, W8

ARTWORK 1029-0000072		UNLESS OTHERWISE SPECIFIED LOC C SPECIFIED LOC COSCO FOR TEL ANDERSON STRIPES ALL OTHERS	DATA GENERAL CORPORATION 100 FEDERAL STREET, BOSTON, MASS.
FILE	SEARCHED	SEARCHED	FILED
FILE	INDEXED	INDEXED	INDEXED
	MAILED	MAILED	MAILED
	RECORDED	RECORDED	RECORDED
	FILED	FILED	FILED
	402	402	402
	SAC	SAC	SAC
	D	D	D
	10290890	10290890	10290890

003-000282-13  
107-000282-06

REV		DESCRIPTION	CHGTS	APP BY	DATE
01		PEN ECO 2122		W-212	10-1-73
02		PEN ECO 3873		W-387	10-1-73
03		PEN ECO 4137		W-413	9-6-73



QTY	DESCRIPTION	PART NO
1	DATA GENERAL CORPORATION SOUTH BORO MASSACHUSETTS 01772	
1	CHECKED	10-1-73
1	EX-CHANGER	10-1-73
1	APPROVED	10-1-73
1	FIRST USED ON	
1	FINISH	
1	SCALE	
1	SIZE CODE	D 016
1	DRAWING NUMBER	000096
1	REV	03

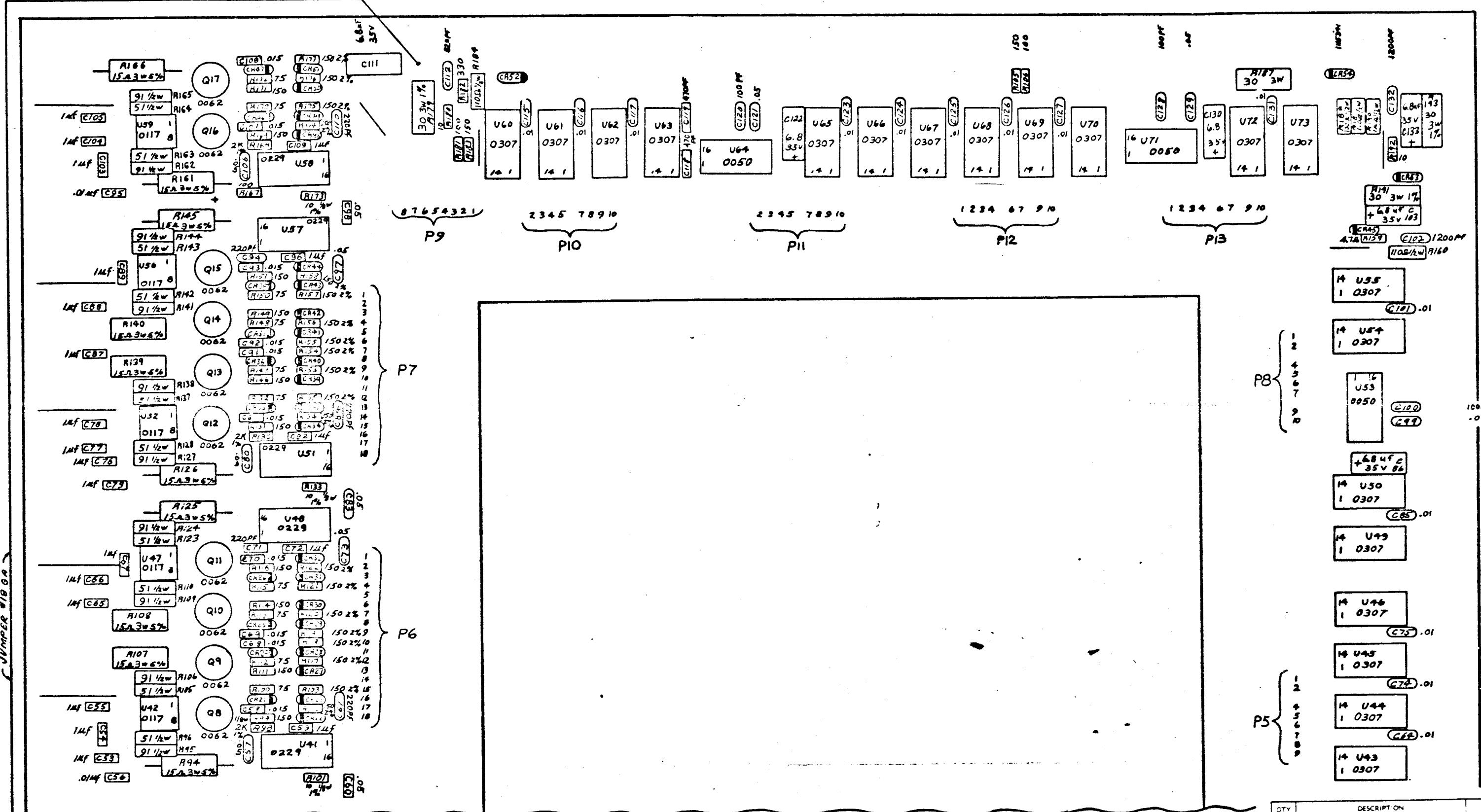
UNLESS OTHERWISE SPECIFIED -  
DIMENSIONS ARE IN INCHES  
TOLERANCES ON  
FRACTIONS: .001-.031 ANGLES:  
0° = 0° ± 1°  
90° = 90° ± 1°  
PARTS TO BE FREE FROM BURRS  
BREAK ALL EDGES D10

UNLESS OTHERWISE SPECIFIED -  
ALL MACH SURFACES ✓

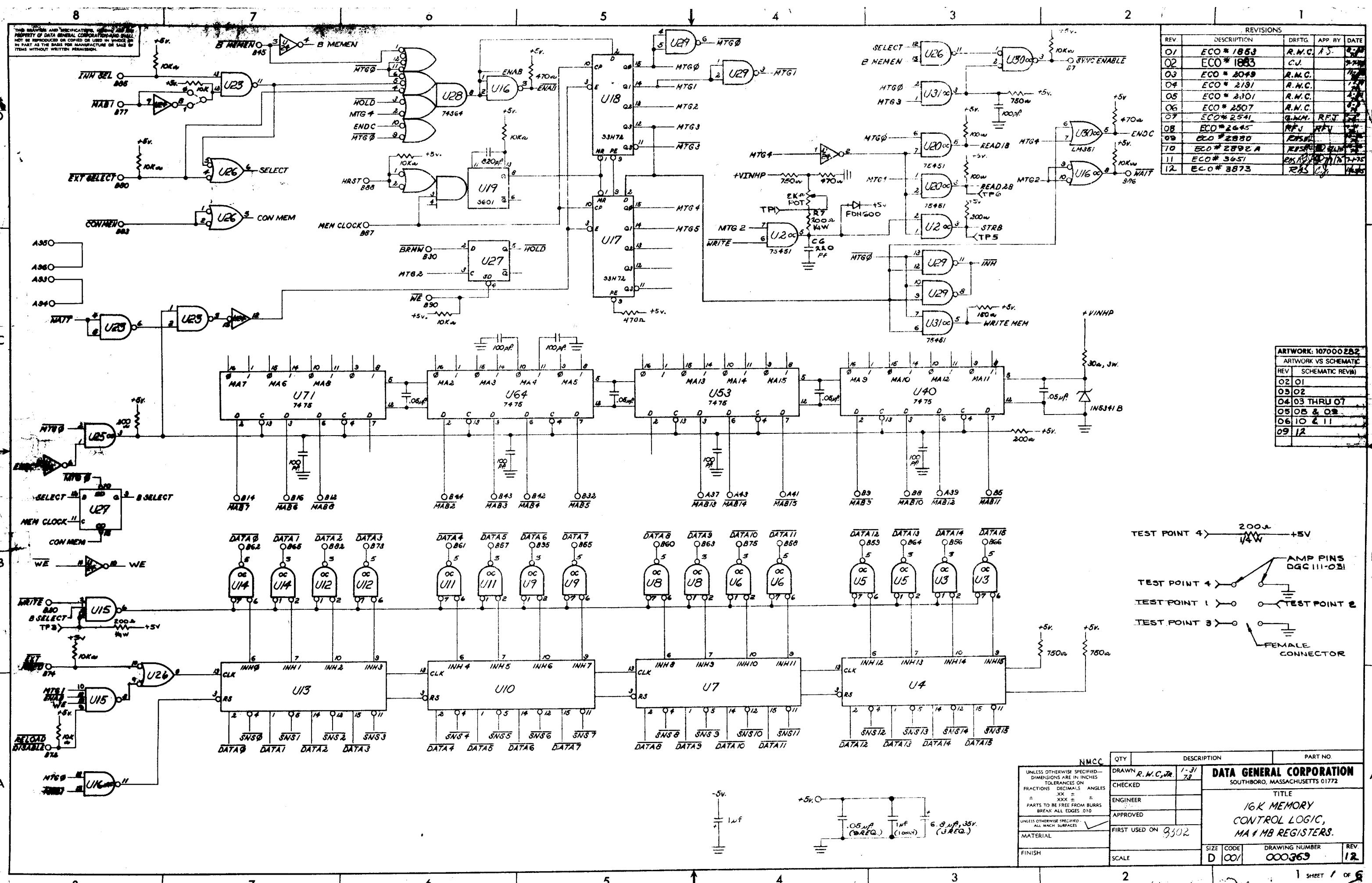
MATERIAL

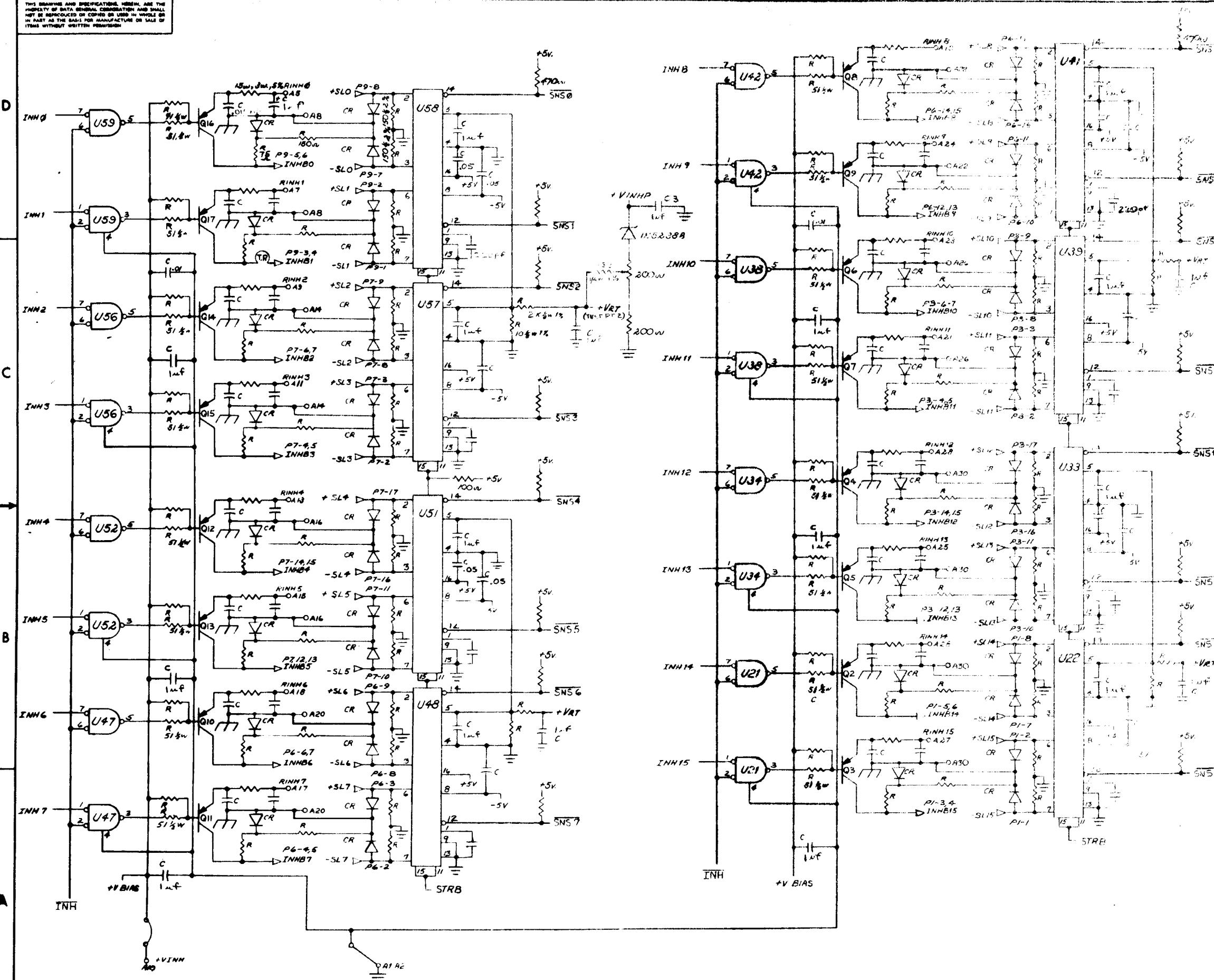
003-000282-13  
107-000282-06

DESCRIPTION	CRITG	APP BY	DATE
PER ECO 2892	N VOL		5-6-74
PER ECO 3873	S KLM		5-6-74
PER ECO 4437	S KLM		5-6-74



UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS & DECIMALS		DRAWN			DATA GENERAL CORPORATION SOUTHBROOK MASSACHUSETTS 01772		
ANGLES XX ± IE XXX ± PARTS TO BE FREE FROM BURRS BREAK ALL EDGES 0.010		CHECKED <i>(initials)</i>					
		ENGINEER	12-13		TITLE		
		<i>(initials)</i>	72				
		APPROVED <i>(initials)</i>			NOVA 2 16 K MEMORY MODEL 8302		
		FIRST USED ON					
UNLESS OTHERWISE SPECIFIED. ALL MACH SURFACES ✓					SIZE	CODE	DRAWING NUMBER
MATERIAL		SCALE			D	016	000096
FINISH							O





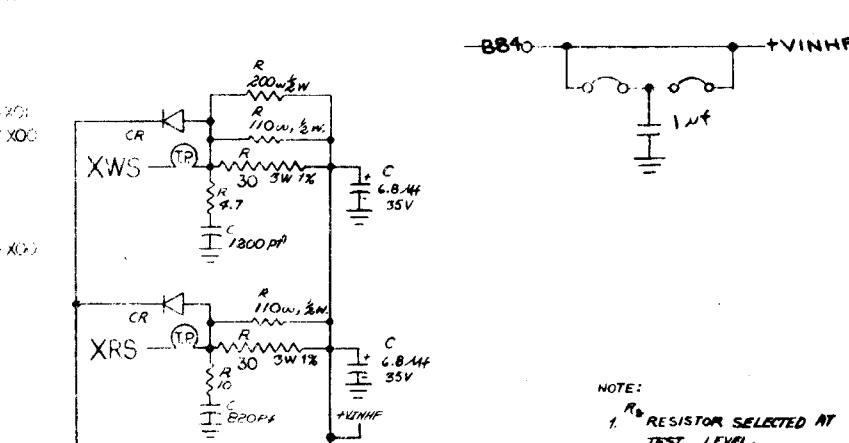
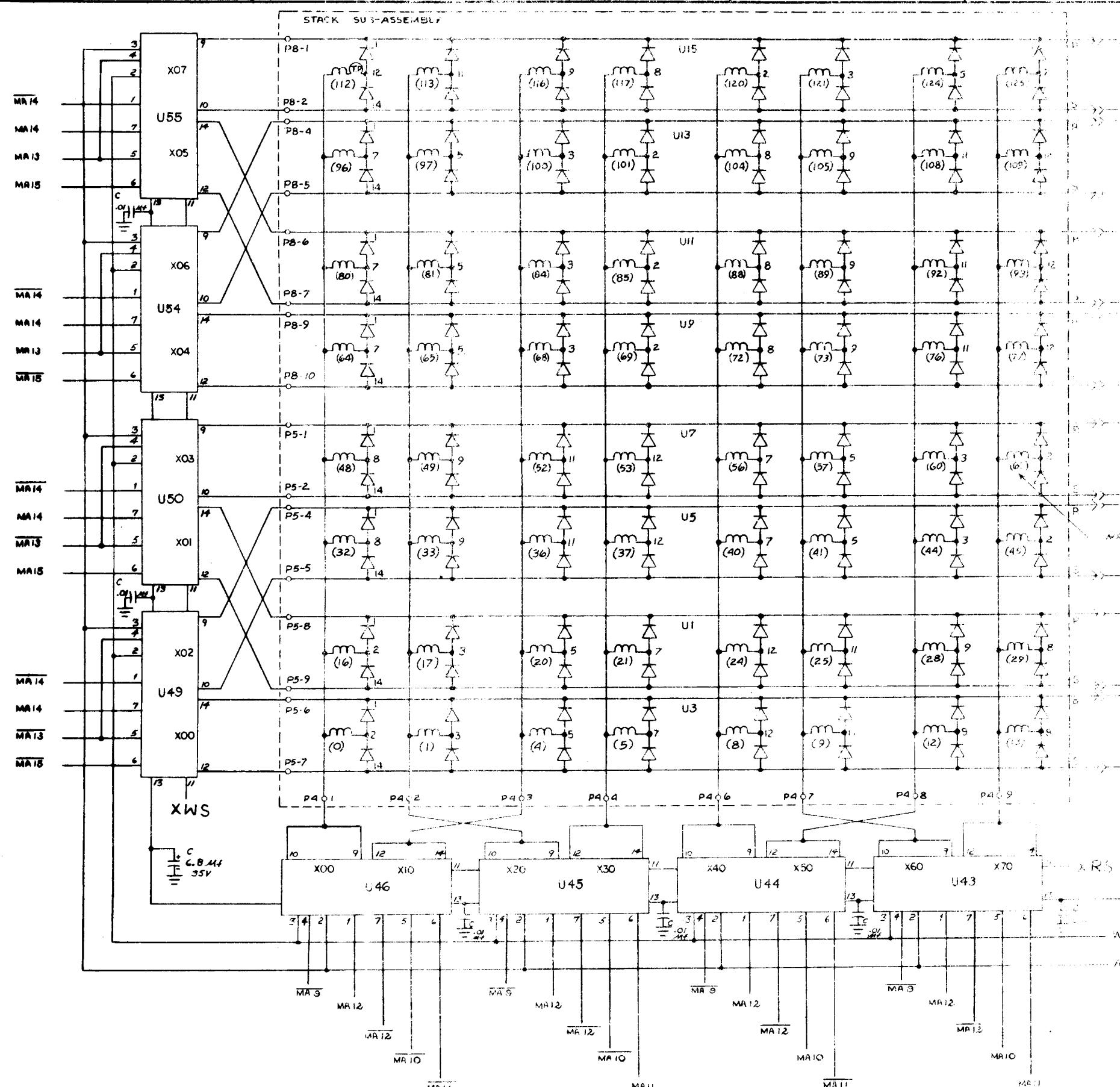
NOTE: 1. ALL CAPACITORS ARE IN MICROFARADS, AND ALL RESISTORS ARE IN OHMS.  
2. TRANSISTORS ARE 2N1022 WITH HEAT SINKS.

QTY	DESCRIPTION	PART NO.
DRAWN CHEKED	CHEKED	1/5/73
CHECKED		
ENGINEER		
APPROVED		
FIRST USED ON		
MATERIAL		
FINISH		

DATA GENERAL CORPORATION  
SOUTHBROOK, MASSACHUSETTS 01772  
TITLE  
16K MEMORY  
SENSE & INHIBIT

THIS DRAWING AND SPECIFICATIONS, HERIN, ARE THE PROPERTY OF DATA GENERAL CORPORATION. THEY ARE NOT TO BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.

REV	DESCRIPTION	DRFTG	APP BY	DATE
-----	-------------	-------	--------	------



NOTE:  
1. RESISTOR SELECTED AT TEST LEVEL.  
2. FOR STACK VENDOR USE ONLY.

NOTE: ALL CAPACITORS ARE IN MICROPARAS. AND ALL RESISTORS ARE IN OHMS. 1/4 WATT. WITH 5% TOLERANCE UNLESS OTHERWISE SPECIFIED.

QTY	DESCRIPTION	PART NO.
DRAWN CHET JOHNSON	11-177	<b>DATA GENERAL CORPORATION</b>
CHECKED		SOUTHBURY, MASSACHUSETTS 01772
ENGINEER		TITLE
APPROVED		16K MEMORY
FIRST USED ON	107-000	X DRIVERS
FINISH	SCALE	SIZE D CODE 001 DRAWING NUMBER 000369 REV 12

8

7

6

5

4

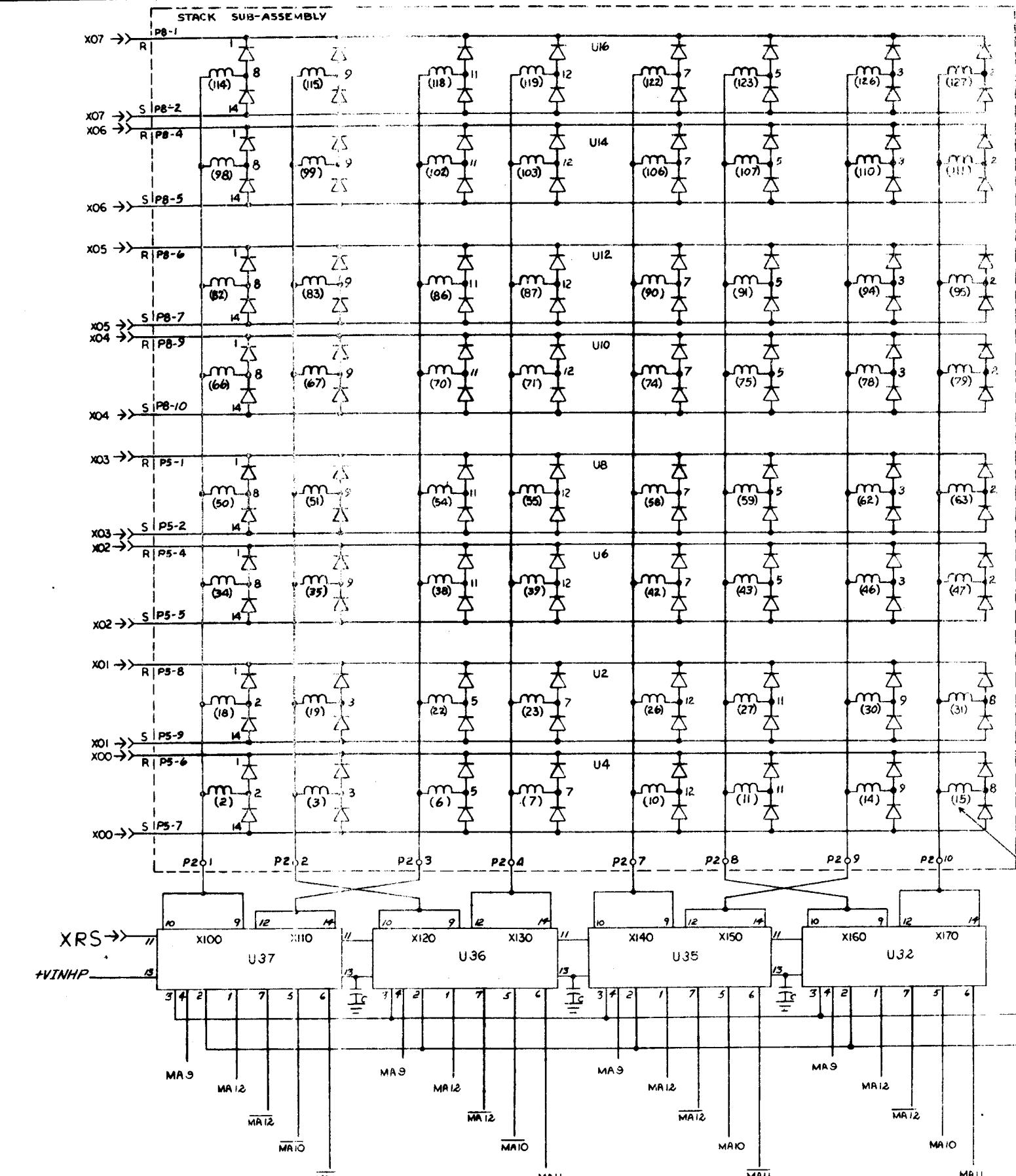
3

2

1

THIS DRAWING AND INFORMATION CONTAINED HEREIN ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE COPIED OR DISCLOSED EXCEPT IN ACCORDANCE WITH THE TERMS OF THE AGREEMENT OR LICENSE AGREEMENT OR AS OTHERWISE APPROVED BY DATA GENERAL.

REVISIONS  
REV. DESCRIPTION DATE APP BY



QTY	DESCRIPTION	PART NO.
DRAWN CHET JOHNSON 1/15/72	<b>DATA GENERAL CORPORATION</b>	
CHECKED	SOUTHBORO, MASSACHUSETTS 01772	
ENGINEER	TITLE	
APPROVED	16K MEMORY	
FIRST USED ON	X DRIVERS	
MAATERIAL		
FINISH		
SCALE	SIZE D	CODE 001
	DRAWING NUMBER 000369	

NOTE: ALL CAPACITORS ARE IN MICROPARAS. AND ALL RESISTORS ARE IN OHMS. 1/4 WATT, WITH 5% TOLERANCE UNLESS OTHERWISE SPECIFIED.

NOTE:-

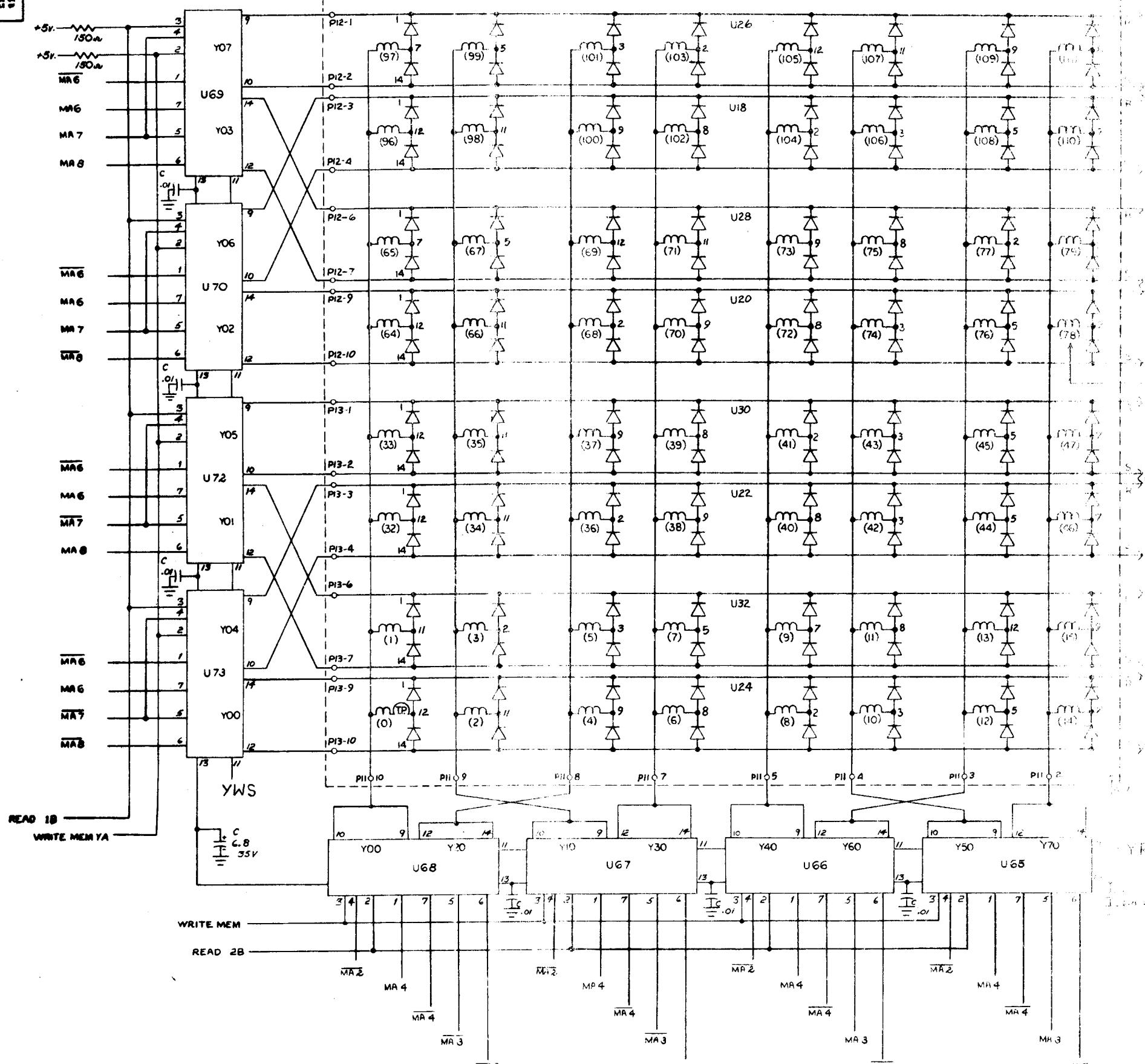
7. FOR SYNTH VENDOR USE ONLY

WRITE MEM

READ 1B

THIS DRAWING AND INFORMATION HEREIN ARE THE PROPERTY OF DATA GENERAL CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF EQUIPMENT WITHOUT WRITTEN PERMISSION.

REVISIONS  
REV DESCRIPTION DRFTG APP BY DATE



8

7

6

5

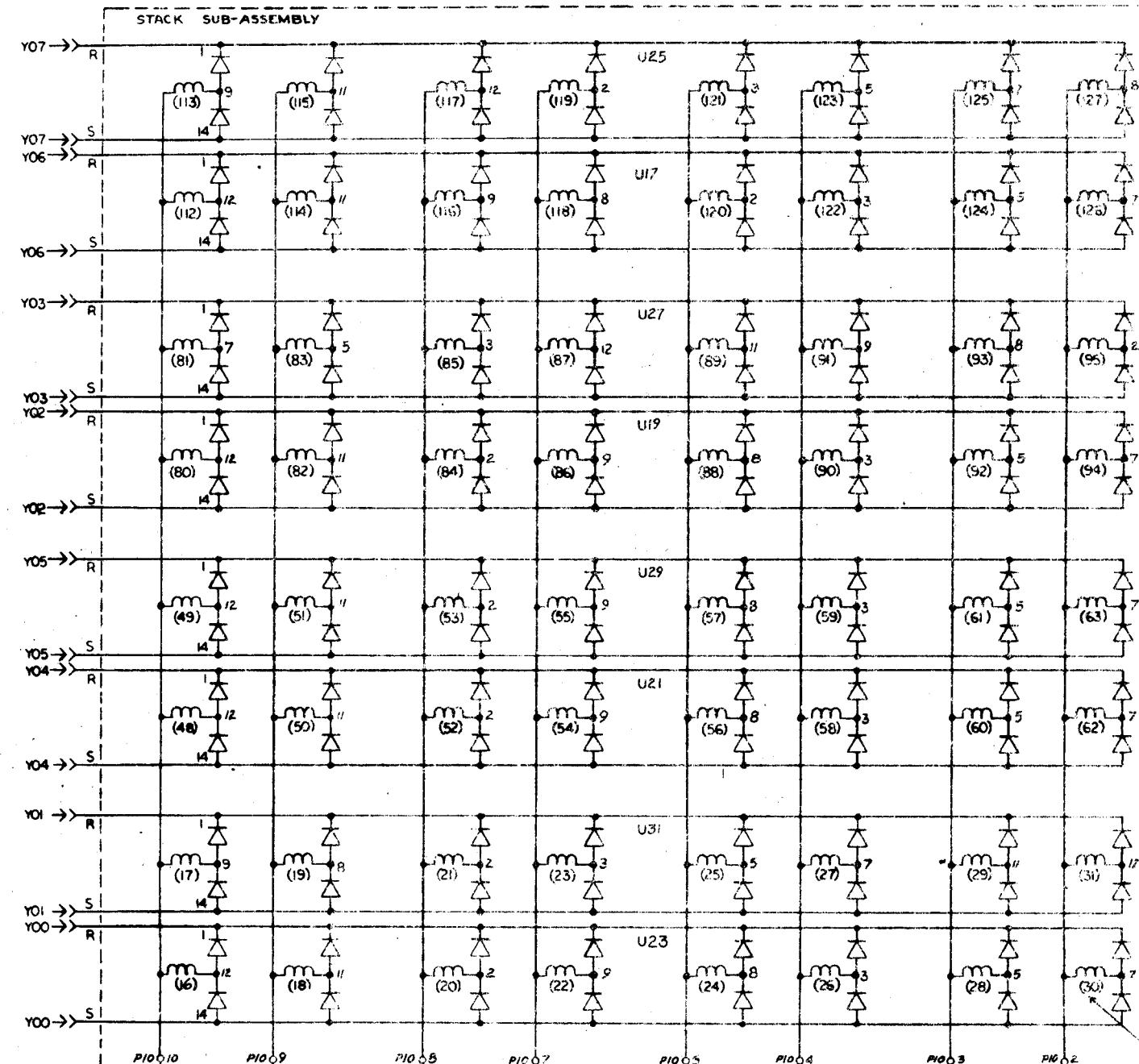
4

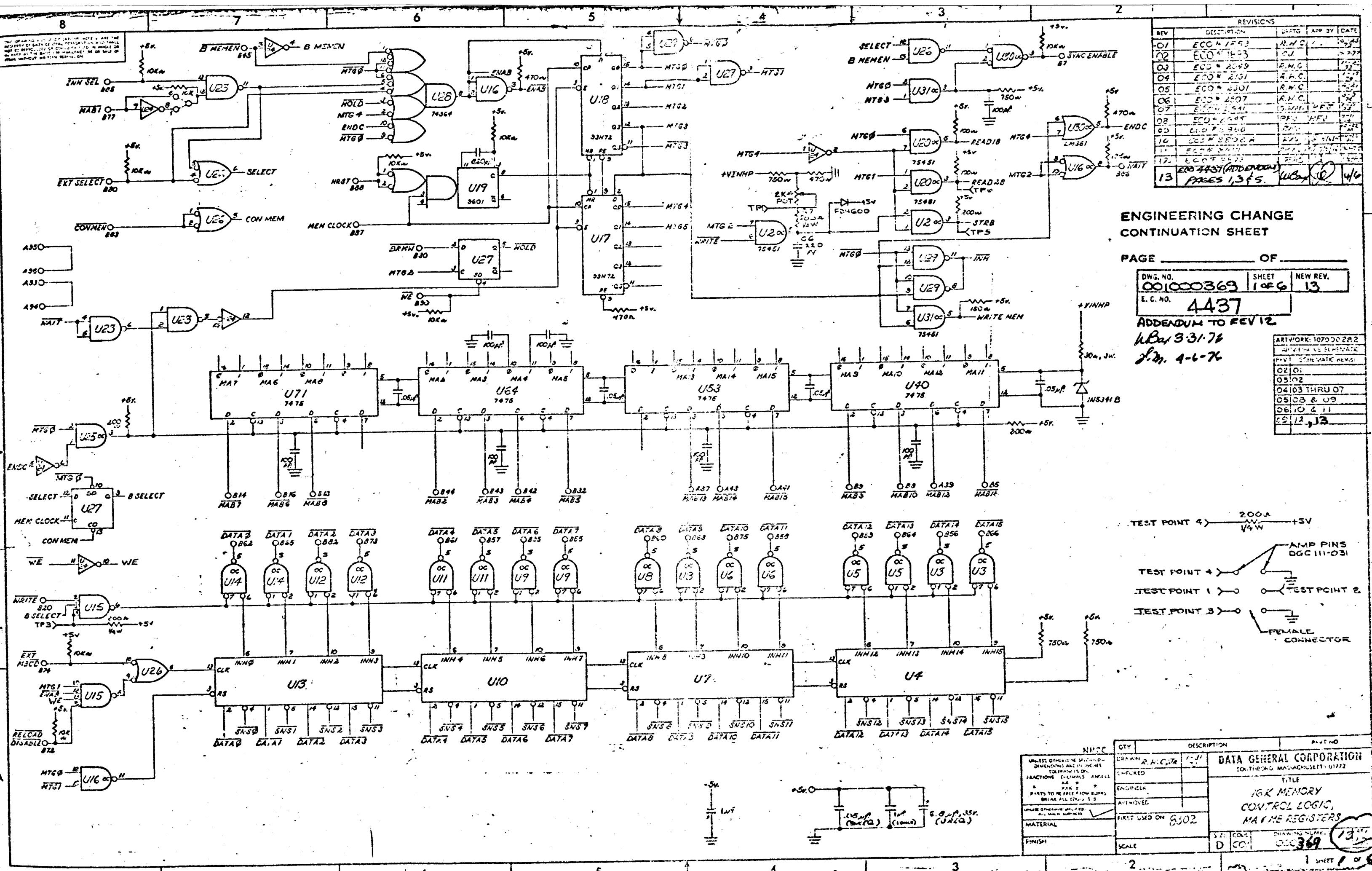
3

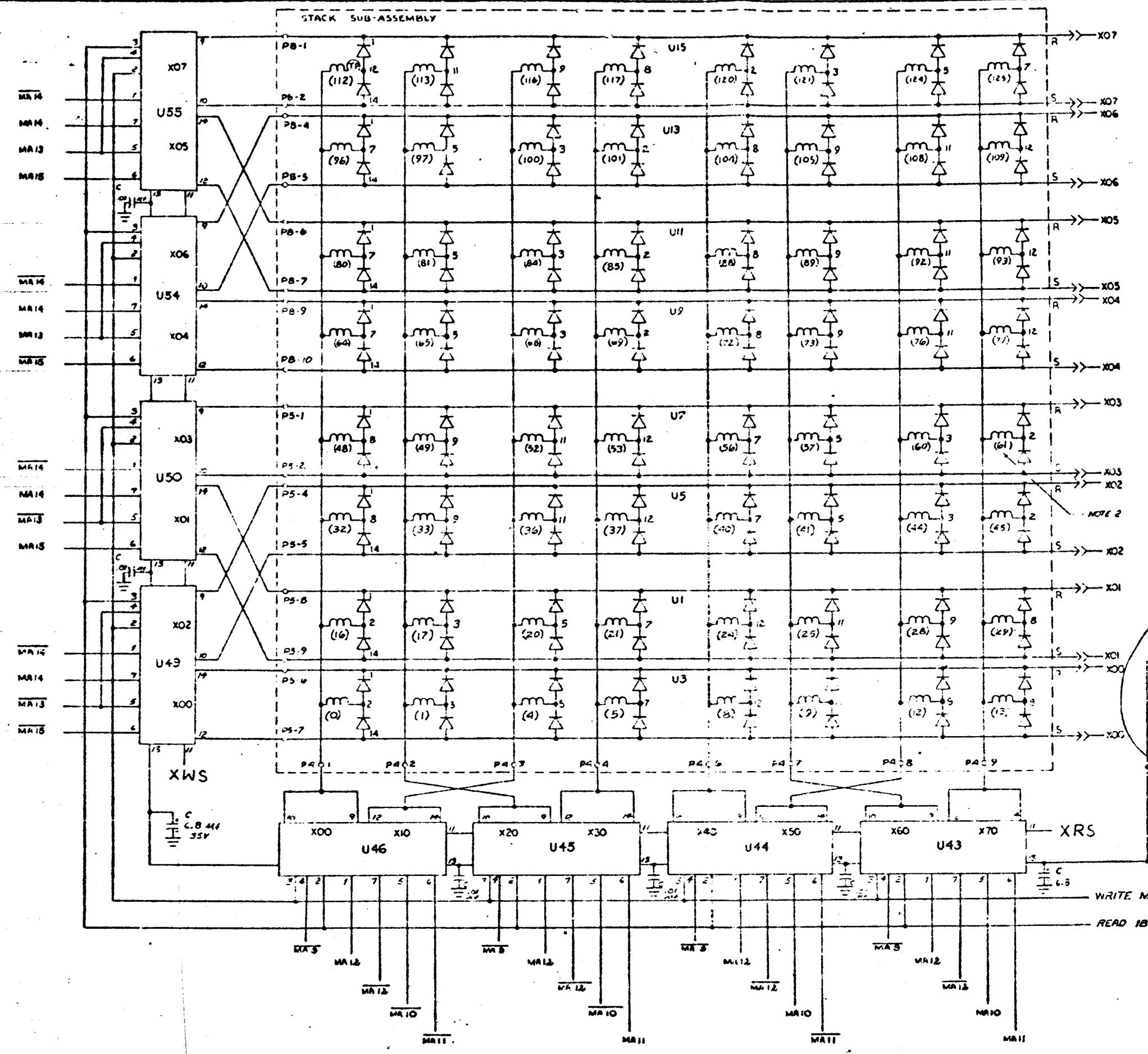
2

ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED  
DATE 10-10-01 BY SP/SP

REV	DESCRIPTION
-----	-------------





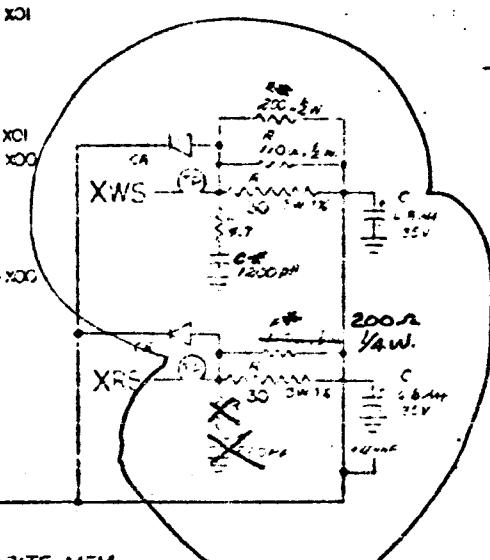


**ENGINEERING CHANGE  
CONTINUATION SHEET**

PAGE \_\_\_\_\_ OF \_\_\_\_\_

DWG. NO. 001000369 SHEET 3 OF 6 NEW REV. 13

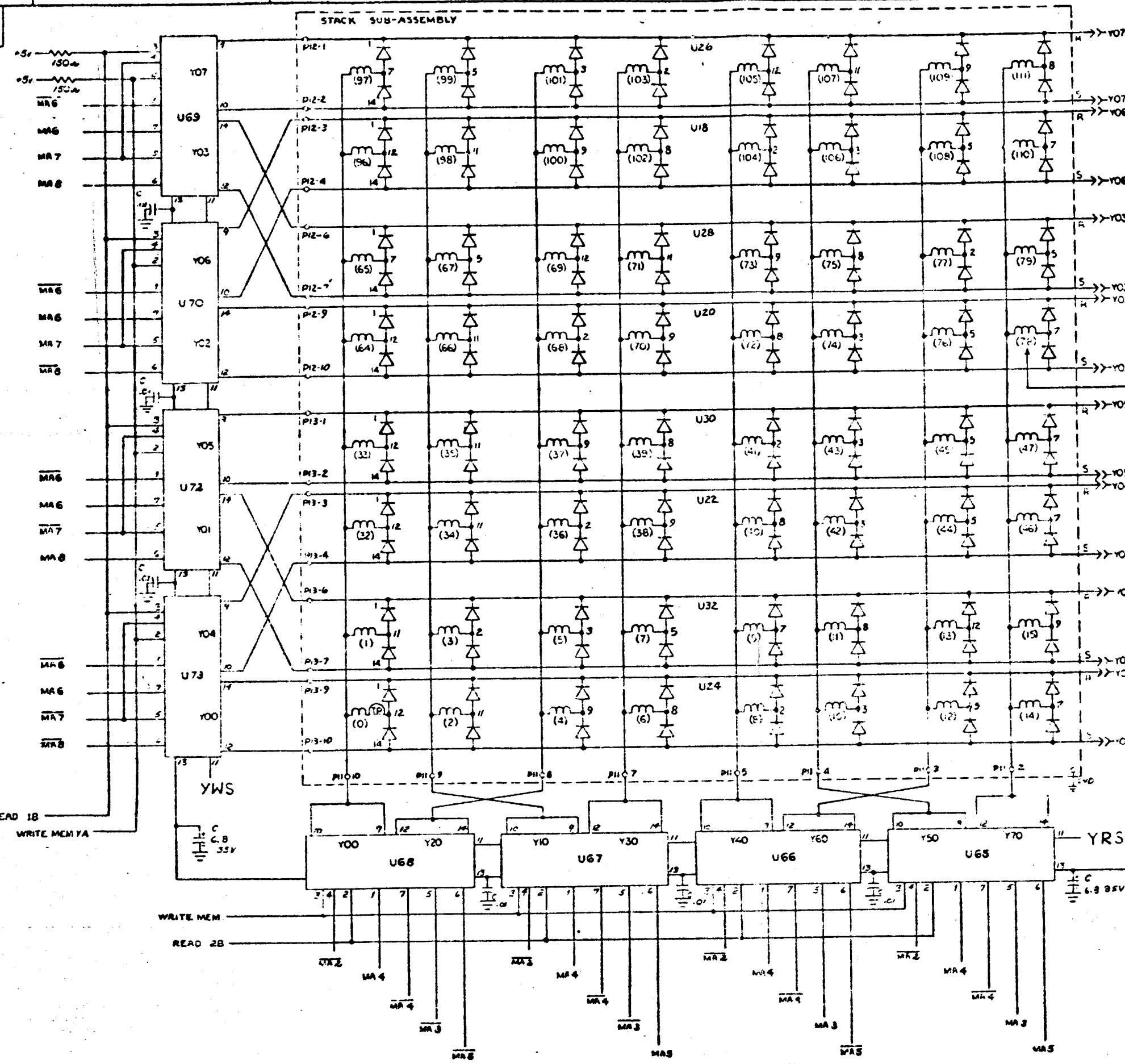
ADDENDUM TO REVIZ  
W&G 3/31/76  
J.M. 4-6-76



NOTE: AND CAP MAY BE  
CHANGED AT  
TEST LEVEL.  
FOR STACK VERSION USE C4X

ALL CAPACITORS ARE IN  
CUTTER PARALLEL AND ALL  
RESISTORS ARE IN SERIES.  
1/20 WATT. WITHIN THE  
FRANCE UNLESS OTHERWISE

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES.	DATE ISSUED	EXPIRATION
FRACTIONS E.G. 1/2, ANGLES DEGREES E.G. 90°	JOHNSON	DATA GENERAL CORPORATION
PARTS TO BE POLISHED, PLATES BREAK ALL EDGES E.G. 0	CHECKED	SOUTHBURY, CONNECTICUT 06488
UNLESS OTHERWISE SPECIFIED, ALL WORK IS UNPAINTED	ENGINEERED	TITLE
A	APPROVED	16K MEMORY
	LAST USED ON	X DRIVERS
MATERIAL	107-000	DRAWING NO. 13
FINISH	SCALE	DATE 3/6/89

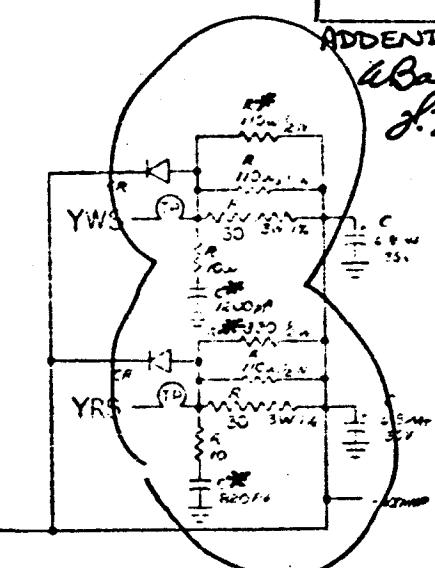


**ENGINEERING CHANGE  
CONTINUATION SHEET**

PAGE \_\_\_\_\_ OF \_\_\_\_\_

DWG. NO.	001000369	SHEET 5 OF 6	NEW REV. 13
E. G. NO.	4437		

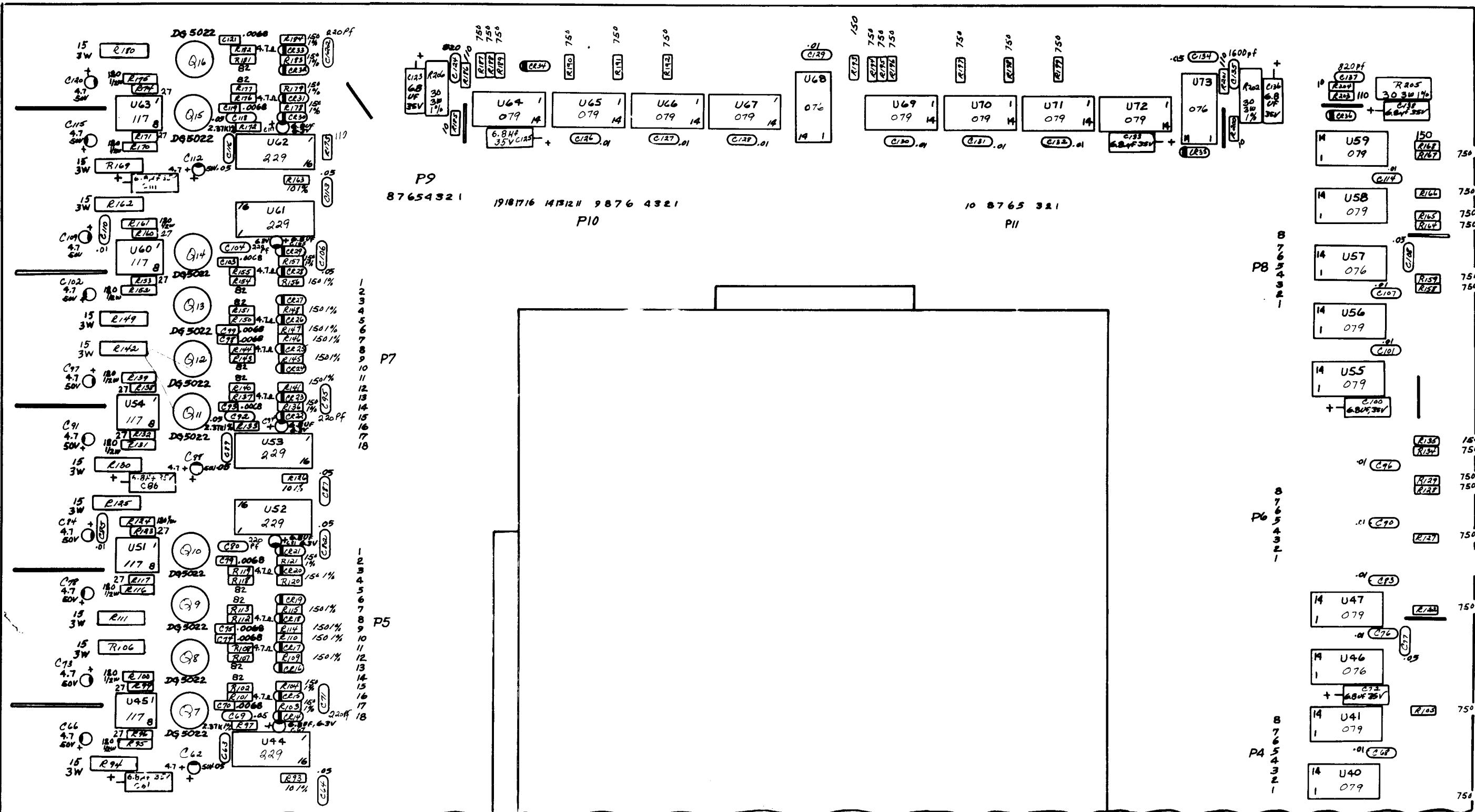
ADDENDUM TO REV 12  
ABF 3/31/76  
J.M. 4-6-76



**NOTE:** ALL CAPACITORS ARE IN  
MICROPARAS, AND ALL  
BENTONICS ARE D. C. V. 450  
1/4 WATT, WITH 5% TOL.  
FRANCE UNLESS OTHERWISE  
WITH SPECIFIED.

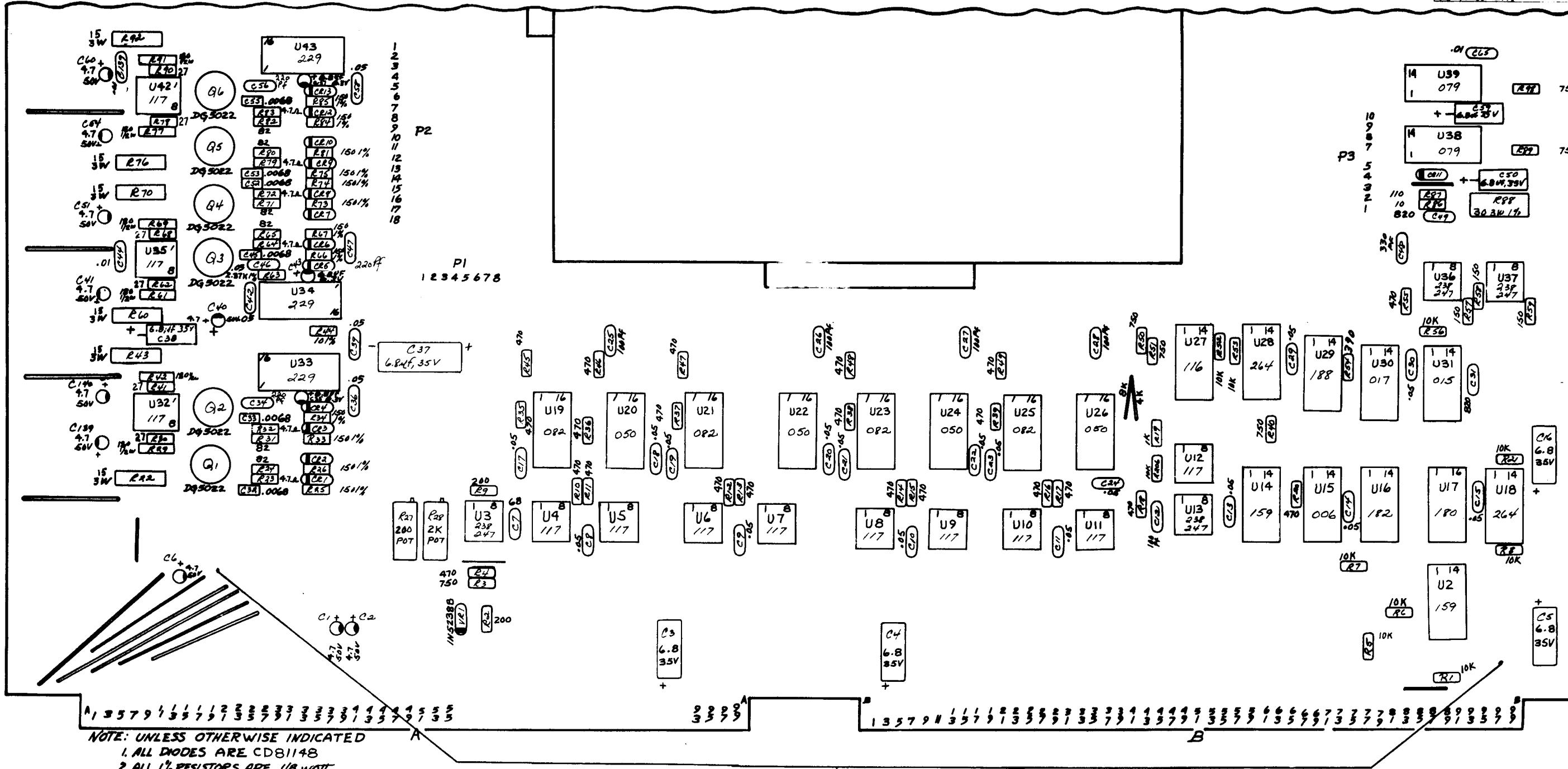
UNLESS OTHERWISE SPECIFIED - DIMENSIONS ARE IN INCHES FOR DECIMAL ON FRACTIONS DECIMAL ANGLES ARE IN DEGREES TOLERANCE .010 PARTS TO BE FOLDED FROM ZORS BUT NOT ALL EDGES	CTN	DESCRIPTION	PART NO.
	DRAWN ... JOHNSON		DATA GENERAL CORPORATION SOFTWORLD, WATERTON, MASSACHUSETTS 01723 TITLE 16K MEMORY Y DRIVERS
	CHECKED		
	ENGINEER		
	APPROVED		
	FIRST USED ON 107-000		
MATERIAL	SIZE D 1001	DRAWING NUMBER 00369	
FINISH	SCALE	(13)	

REVISIONS			
REV	DESCRIPTION	DRFTG	APP BY
0.1	EX-C 6/27	W. 1/16	4/1/73
0.2	EX-C 7/17	E. 1/16	4/2/73
0.3	EX-C 11/12	W. 1/16	4/16/73

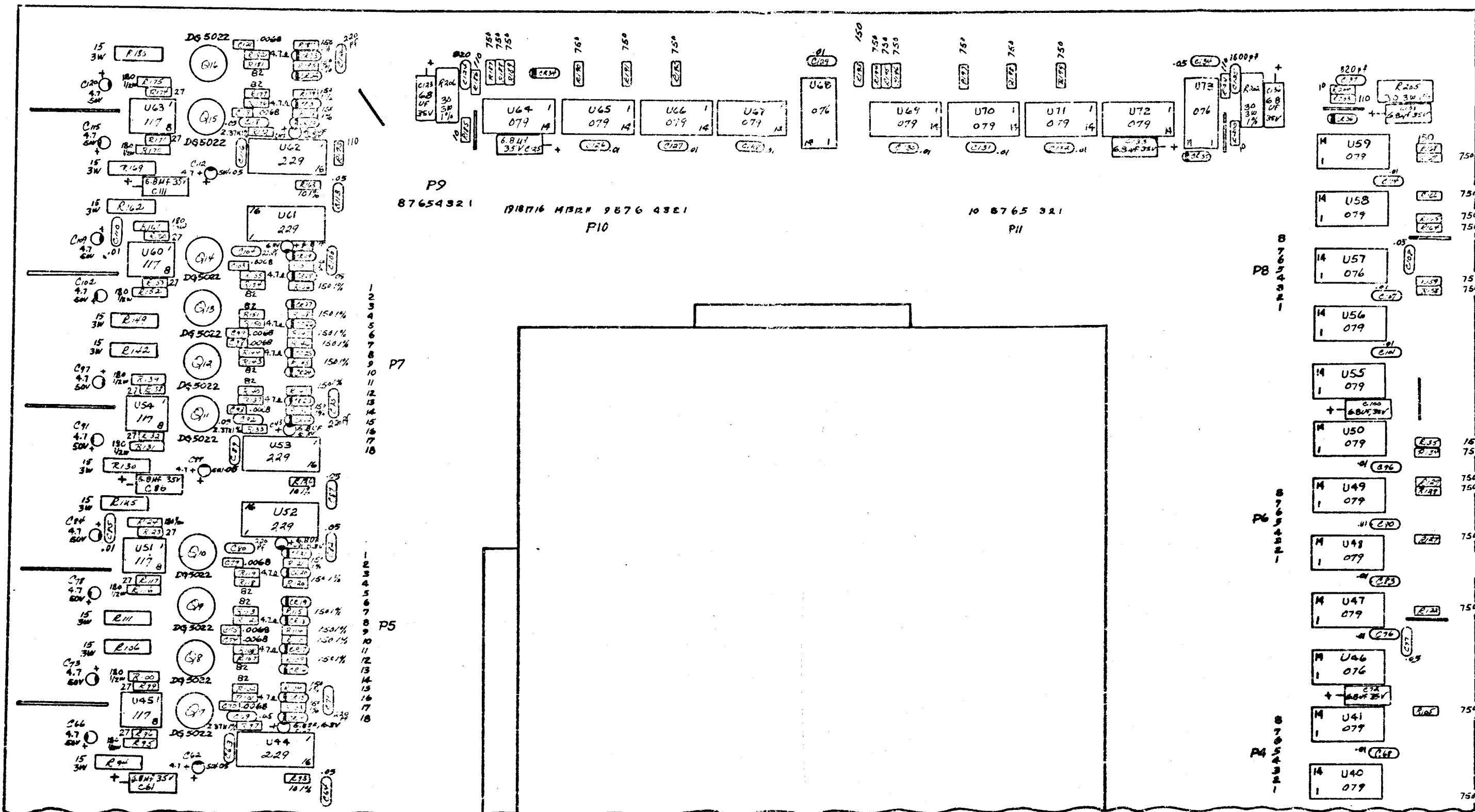


STN	DESCRIPTION	PART NO
DRAWN	DATA GENERAL CORPORATION	
CHECKED	SOUTHBROOK MASSACHUSETTS 01772	
ENGINEER	TITLE	
APPROVED	NOVA 2	
MATERIAL	4K MEMORY	
F/N SH	MODEL 8300	
SCALE	DRAWING NUMBER	
D 016	005 001360	
REV	03	

REVISIONS				
REV	DESCRIPTION	DRTG	APP BY	DATE
C1	ECC 5.7V	6/1968	J.F.	10/1968
C2	ECC 5.7V	6/1968	J.F.	10/1968

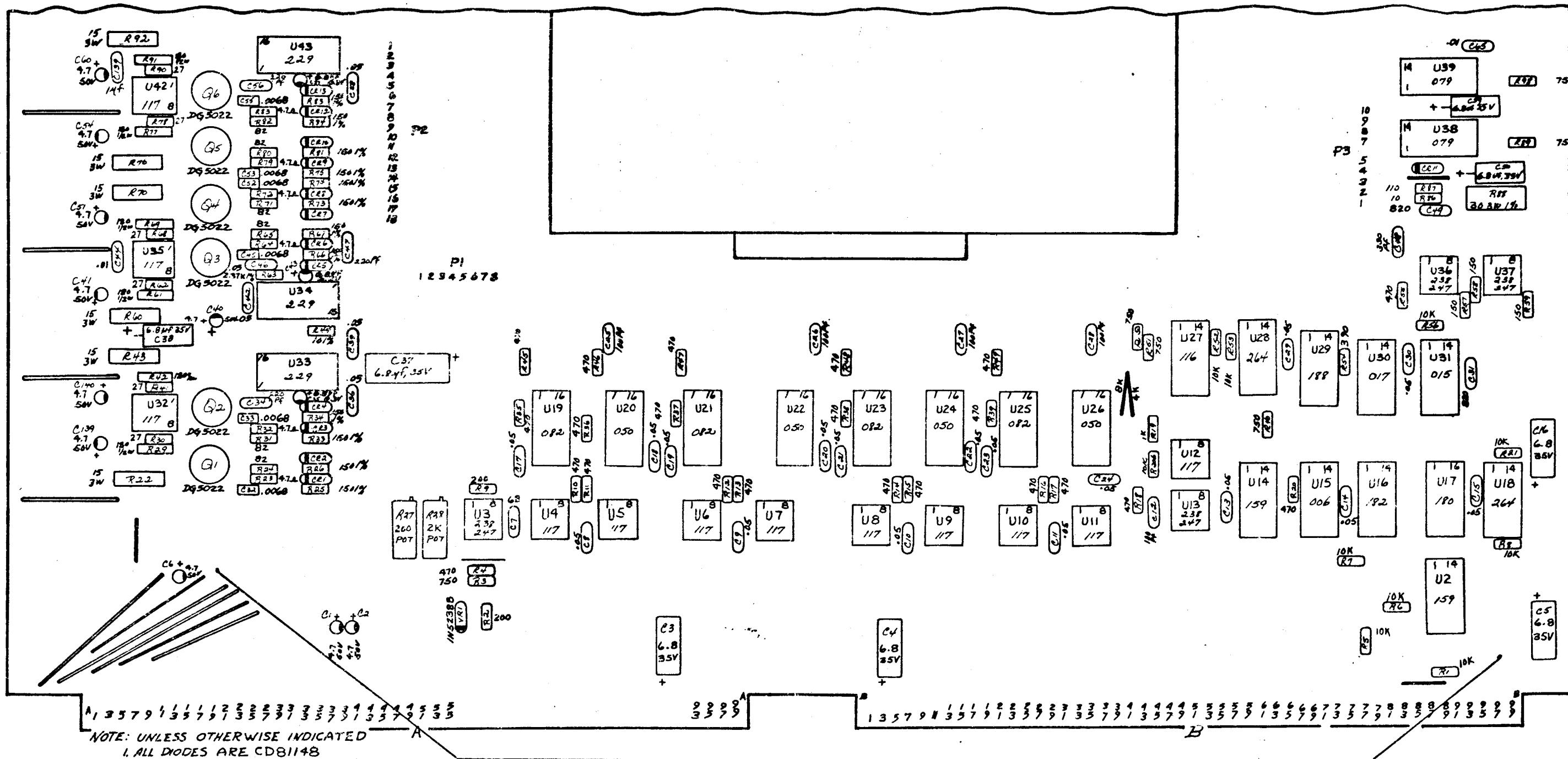


QTY	DESCRIPTION	PART NO
DRAWN	DATA GENERAL CORPORATION	
CHECKED	/ SOUTHBORO MASSACHUSETTS 01772	
XX	TITLE	
XX	NOVA 2	
XX	4K MEMORY	
XX	MODEL 8300	
FRACTIONS DECIMALS ANGLES		
UNLESS OTHERWISE SPECIFIED ALL MACH SURFACES		
XX	APPROVED	
XX	ENGINEER	
XX	CHECKED	
XX	DRAWN	
MATERIAL	FIRST USED ON	
FINISH	005 001360	
SCALE	SIZE	CODE
	D	016
	DRAWING NUMBER	
	000094	
	REV	03



3/1/71		QTY	DESCRIPTION		PART NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON		DRAWN			
FRACTION: DECIMALS ANGLES		CHECKED			
S. E. X. T. F.		DESIGNED	X-3		TITLE
PARTS TO BE FREE FROM BURRS, BREAK ALL EDGES D10		APPROVED	✓		
UNLESS OTHERWISE SPECIFIED - ALL MACHINING SURFACES ✓		FIRST USED ON	005 001360		
MATERIAL		SCALE	SIZE	DATE	DRAWING NUMBER
FINISH			D	016	000095
					REV. 03

REV		DESCRIPTION		DRWLG	APP BY	DATE
P1	ECD	2771				
P2	ECD	2772				
P3	ECD	2773				



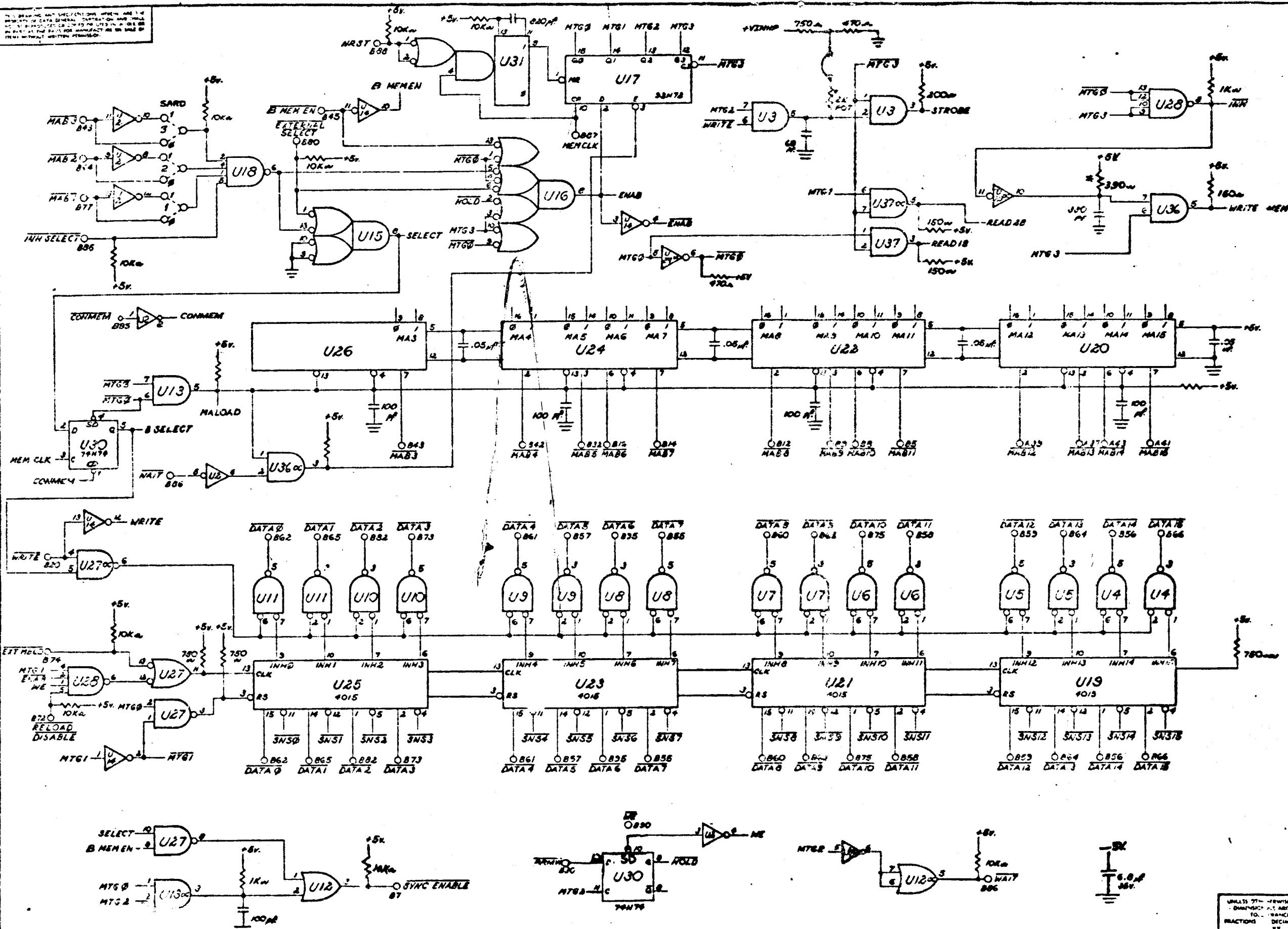
QTY	DESCRIPTION	PART NO
1	DATA GENERAL CORPORATION SOUTHBORO MASSACHUSETTS 01772	
1	DRAWN	
1	CHECKED	
1	ENGINEER	1/13
1	APPROVED	2/2
1	FIRST USED ON	
1	005 001360	
1	FINISH	
1	SCALE	
1	SIZE D 016	
1	DRAWING NUMBER 000095	
1	REV 03	

UNLESS OTHERWISE SPECIFIED:  
 DIMENSIONS ARE IN INCHES  
 TOLERANCES ON FRACTIONS: .001  
 DECIMALS: .001  
 ANGLES: 1°  
 PARTS TO BE FREE FROM BURRS  
 BREAK ALL EDGES 0.05  
 UNLESS OTHERWISE SPECIFIED  
 ALL SURFACES

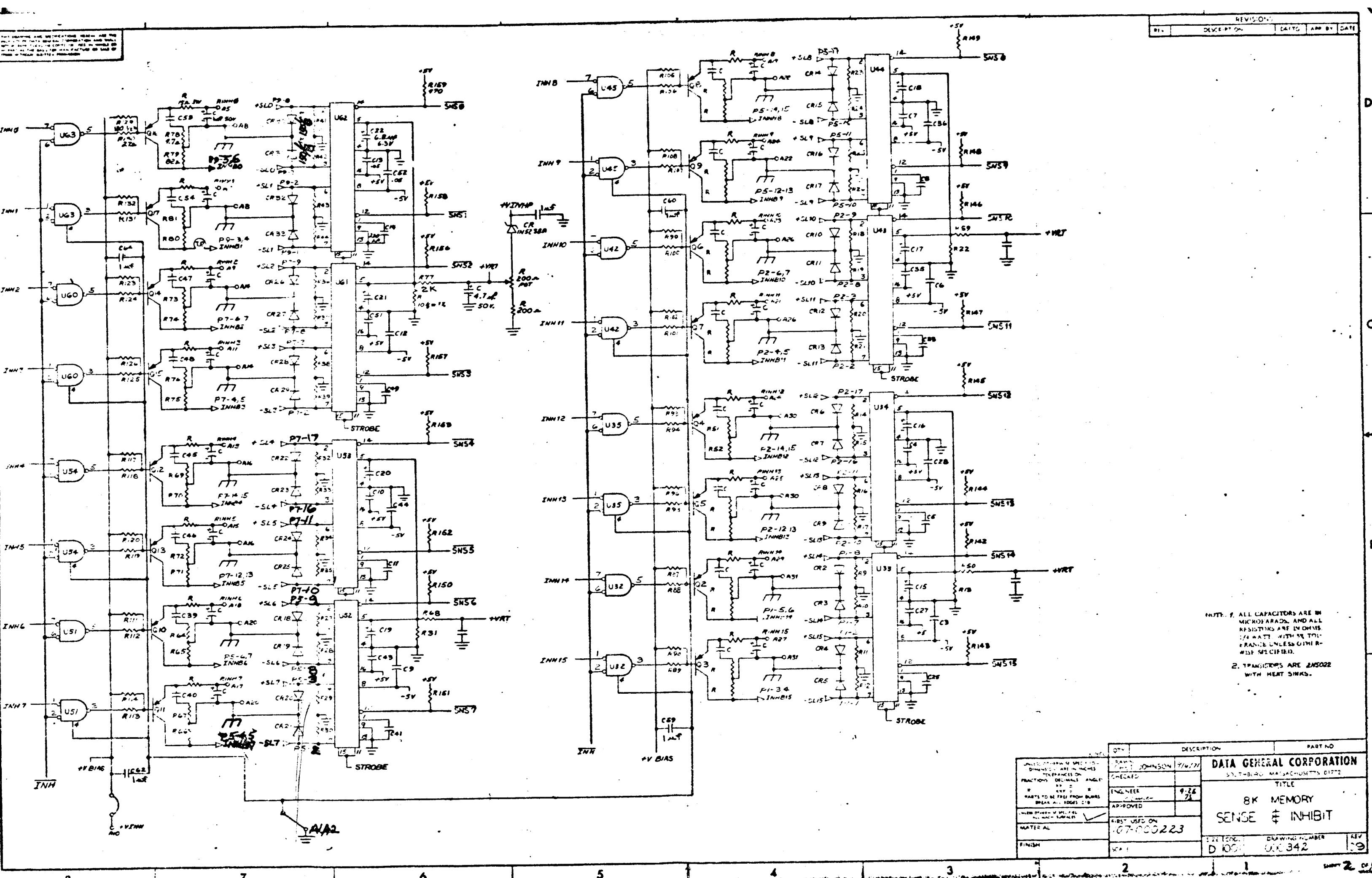
MATERIAL:

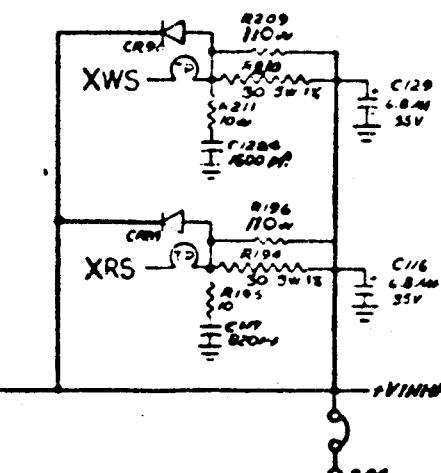
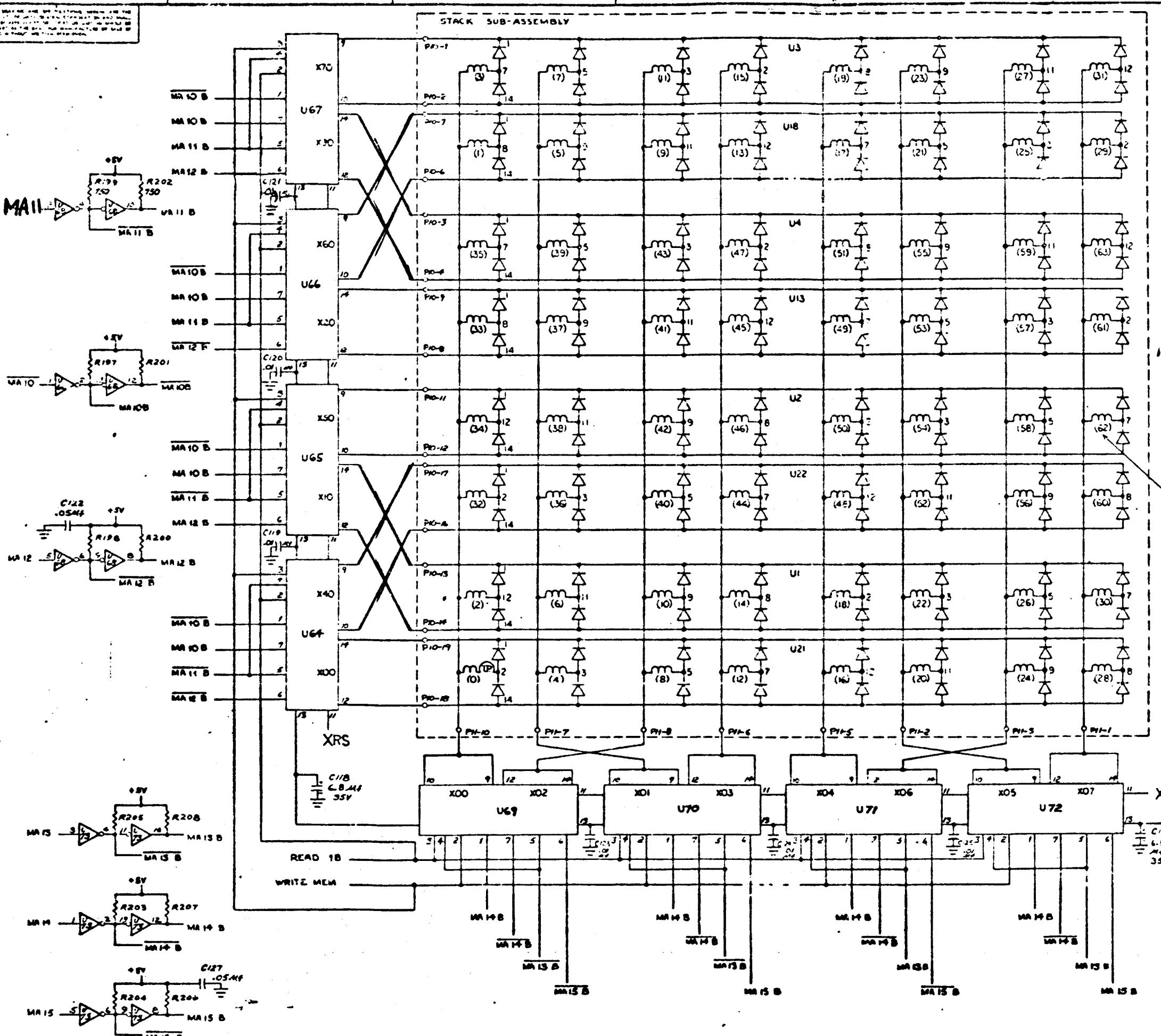
NOVA 2  
8 K MEMORY  
MODEL 8301

REVISI			
REV	DESCRIPTION	DRWGS	APP BY DATE
01	ECO # 1523 (A)	1.A.C.	1-24
02	ECO # 2207	R.W.C.	1-25
03	ECO # 2237	R.W.C.	1-26
04	ECO # 2259	R.W.C.	1-26
05	ECO # 2430	R.W.C.	1-26
06	ECO # 2507	R.W.C.	1-26
07	ECO # 2541	P.W.C.	1-26
08	ECO # 2569	R.W.C.	1-26
09	ECO # 2787 A	PAS	1-26



DRAWN R.W.C.JR		DATA GENERAL CORPORATION	
CHECKED R.W.C.JR		SOUTHBORO MASSACHUSETTS 01772	
APPROVED		TITLE	
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOL. .005 ON DECIMAL ANGLES B. R. E. F. B. PARTS TO DR. PAPER FROM BUBBLE SCALE ALL EDGES 0.00		DATA GENERAL CORPORATION	
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOL. .005 ON DECIMAL ANGLES B. R. E. F. B. PARTS TO DR. PAPER FROM BUBBLE SCALE ALL EDGES 0.00		SOUTHBORO MASSACHUSETTS 01772	
MADE BY		DATA GENERAL CORPORATION	
FIRST USED ON 5-31		SOUTH BORO MASSACHUSETTS 01772	
MATERIAL		DATA GENERAL CORPORATION	
FINISH		SOUTH BORO MASSACHUSETTS 01772	
SIZE D	SCRE	DRAWING NUMBER 000-342	REV 09
SCALE	10:1		

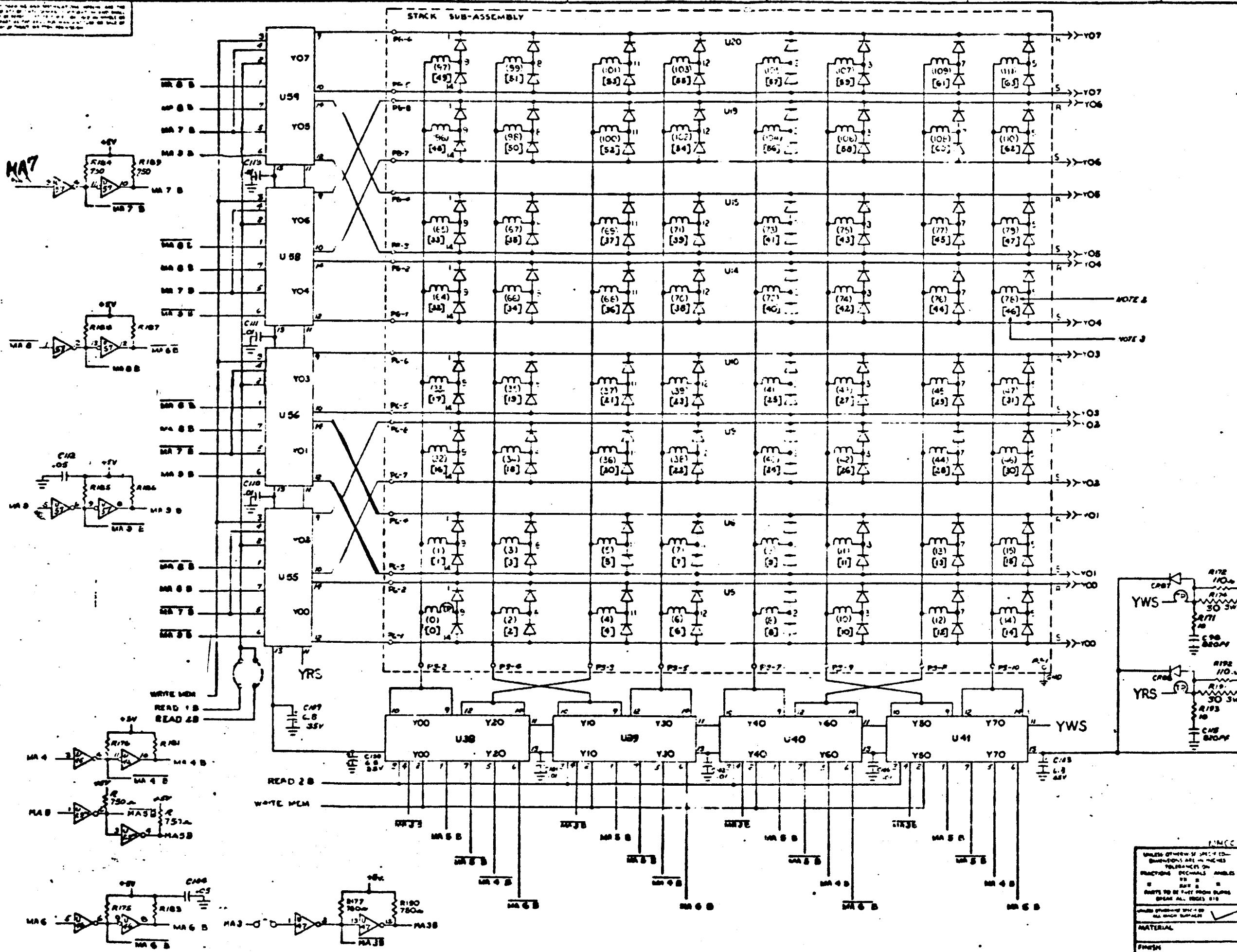




NOTES:  
LC128, R.36 & 203 ARE NOMINAL  
VALUES AND MAY BE  
CHANGED AT TEST LEVEL  
E FOR EACH UNIT USE ONE  
OR THE STACK WIRING.

**NOTE:** ALL CAPACITORS ARE IN  
MICROPARAS. AND ALL  
INDUCTORS ARE IN OUNS.  
1/1 WATT. WITH THE TOL-  
BRANCE UNLESS OTHER-  
WISE SPECIFIED.

UNITS OF MEASURE		QTY	DESCRIPTION	REV. A
DIMENSIONS ARE IN INCHES.				
FRACTIONS - DENOMINATORS ARE 16THS.				
DECIMALS - DENOMINATORS ARE 1000ths.				
RADS TO DEGREES - DENOMINATORS ARE 1000ths.				
DEGREES ALL DECIMALS 6 DIGITS				
UNITS OF MEASURE ARE IN INCHES. ALL LENGTHS IN INCHES		<input checked="" type="checkbox"/>		
MATERIAL				
PHOTOCOPY				
DRAWING NUMBER		SIZE	CODE	REV.
PRINTED ON		D 001	000 342	09
CHECKED				
APPROVED				
FIRST USED ON		8/7-000.223		
REVISIONS				
REMARKS				
DATA GENERAL CORPORATION		SOUTHBURY, CONNECTICUT, 06488		
		TITLE		
		8K MEMORY		
		X DRIVERS		

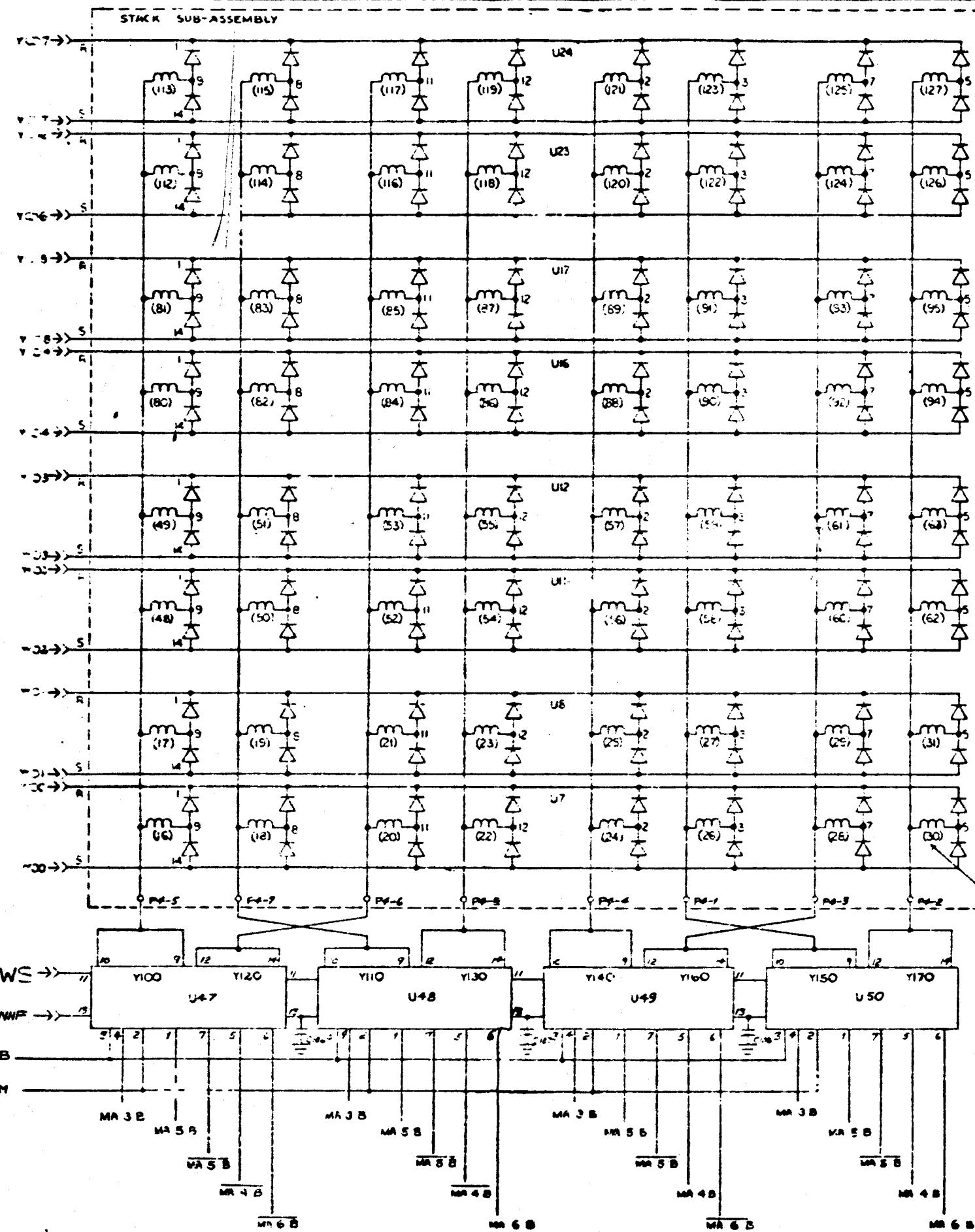


ALL CAPACITORS ARE IN  
MICROFARADS AND ALL  
RESISTORS ARE IN OHMS.  
1/2 WATT, WITH 5% TOL-  
ERANCE UNLESS OTHERWISE  
SPECIFIED.

DATA GENERAL CORPORATION  
SOUTHBEND MASSACH. 01722  
TITLE  
5K MEMORY  
Y DRIVERS  
DRAWN BY JOHNSON  
CHECKED  
ENGINEER  
APPROVED  
MATERIAL  
FINISH  
SCALE  
D 001  
DRAWING NUMBER  
000342  
001

8 7 6 5 4 3 2 1

BEST 4 OF 5



ITEM		DESCRIPTION	PART NO.
MAIN GEARBOX WITH 10- DIMINISHING STEP IN PINIONS TOLERANCES ON GEARBOX DIMENSIONS AS PER MANUFACTURER'S DRAWINGS		JOHNSON	<b>DATA GENERAL CORPORATION</b>
		CHECKED	SD-7000-1 MA-SAL-1000-1002
PARTS TO BE MADE FROM BURNS SHRINK ALL HOLES 0.05		ENGINEER	TITLE
		APPROVED	BY MEMORY
MANUFACTURER OF PARTS NOT IDENTIFIED		FIRST USED ON	Y. DRIVERS
MATERIAL		10-7-000223	
FINISH		SCALE	LAST COPIED DRAWING NUMBER D 10/10 000-342
			REV. DS

*8. NO STICK VENDOR USE ONLY  
ONE STICK VENDING*

**NOTE:** ALL CAPACITORS ARE IN  
MILITARY PARALLEL AND ALL  
TRANSISTORS ARE IN COMMON  
EMITTER WITH THE TOL-  
ERANCE UNLESS OTHERWISE  
SPECIFIED.

A GENERAL CORPORATION  
BY THE STATE OF MASSACHUSETTS 1942  
TITLE  
BK MEMORY  
Y DRIVERS