

GROUP SEL → 1
TERM V/F SEL → 5
TERM W/G SEL → 4
TERM X/G SEL → 3
TERM Y/L SEL → 6
TERM Z/L SEL → 7
TERM A/V SEL → 8
TERM A/W SEL → 9
TERM B/V SEL → 10
TERM B/W SEL → 11
TERM C/W SEL → 12
TERM D/W SEL → 13
TERM E/W SEL → 14
TERM F/W SEL → 15

TERM G/W SEL → 16
TERM H/W SEL → 17
TERM I/W SEL → 18
TERM J/W SEL → 19
TERM K/W SEL → 20

SERIAL IN BUS SEL → 21
SERIAL IN BUS SEL → 22
SERIAL IN BUS SEL → 23
COMMAND DATA OUT → 24

EORERA BUS → 25
EORERA BUS → 26
EORERA BUS → 27
EORERA BUS → 28

INPUT MODE → 29
OUTPUT MODE → 30
EV → 31
-EV → 32

TERM DATA → 33
TERM V/F → 34
TERM W/G → 35
TERM X/G → 36
TERM Y/L → 37
TERM Z/L → 38

TRANSMIT REQ SEL → 39
TRANSMIT SEL → 40
MBR → 41
MBR → 42

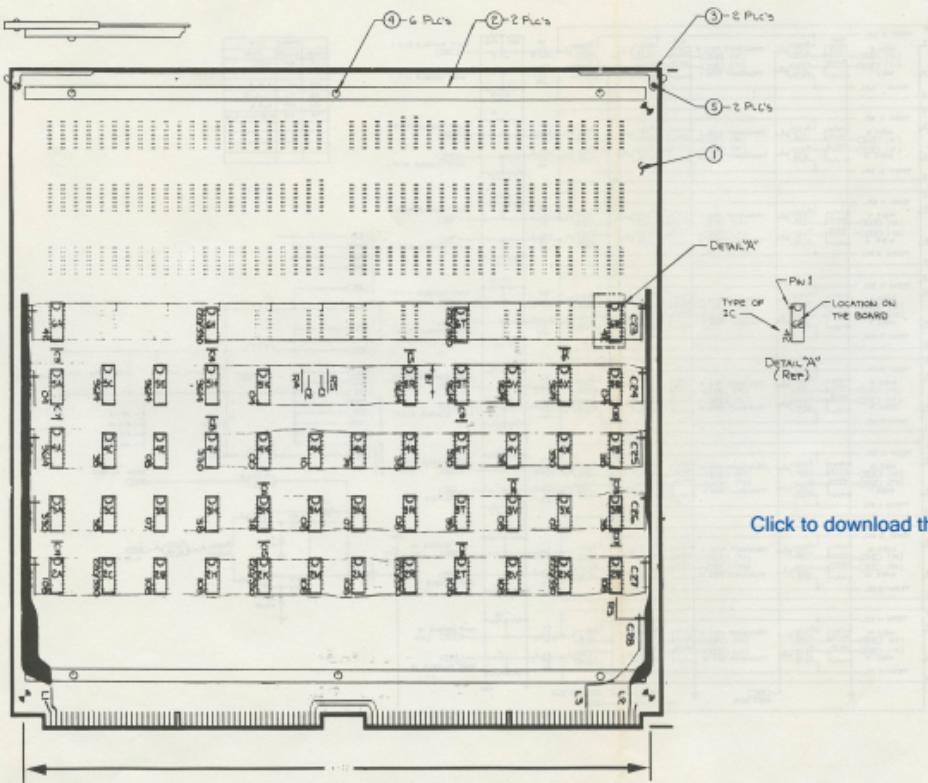
TRANSMIT ENABL. SEL → 43
TRANSMIT THRES. SEL → 44
RECEIVE SEL → 45

TERM SEL → 46
TERM SEL → 47
TERM SEL → 48
TERM SEL → 49
TERM SEL → 50



REF ID	DELETE RESISTORS R11=0.15Ω, R28=5Ω	REF ID	DELETE PART NO.
REF ID 10	DELETE RESISTORS R11=0.15Ω, R28=5Ω	REF ID 15	DELETE PART NO.
REF ID 11	DELETE INTEGRATED CIRCUIT AL.	REF ID 16	DELETE PART NO.
REF ID 12	DELETE RESISTORS R11=5Ω	REF ID 17	DELETE PART NO.

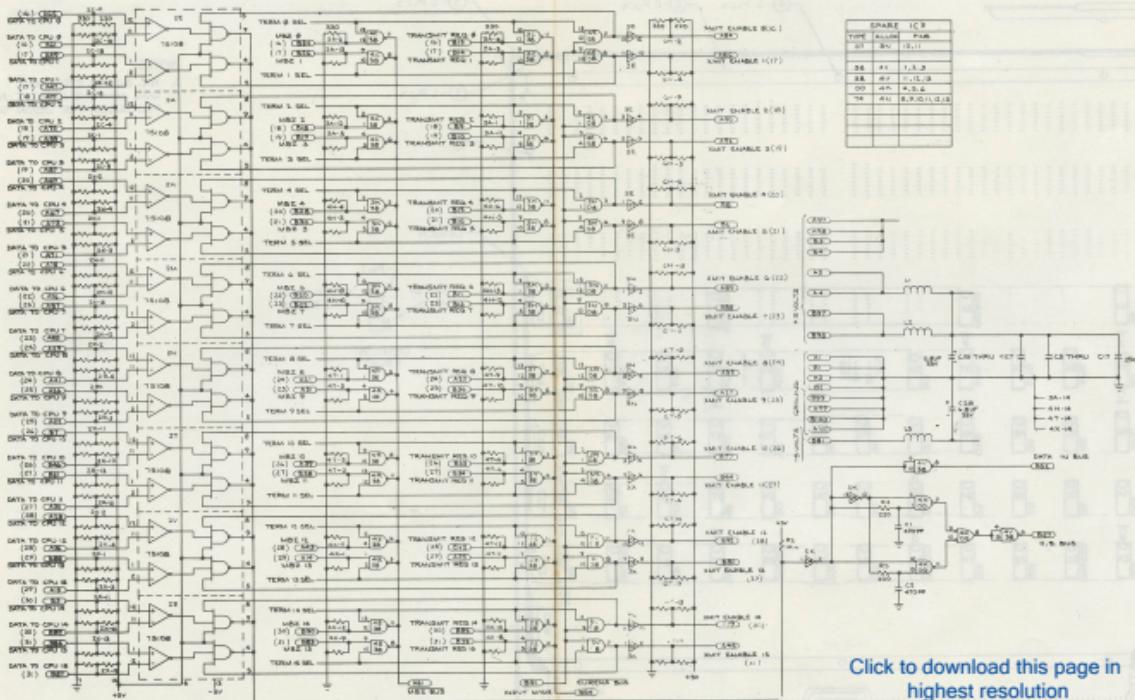
NOTE: 1. ACTUALS OTHERWISE SPECIFIED, RESISTOR VALUES
IN OHMS; INDUCTANCES IN MICRO亨RIES.



NOTES:

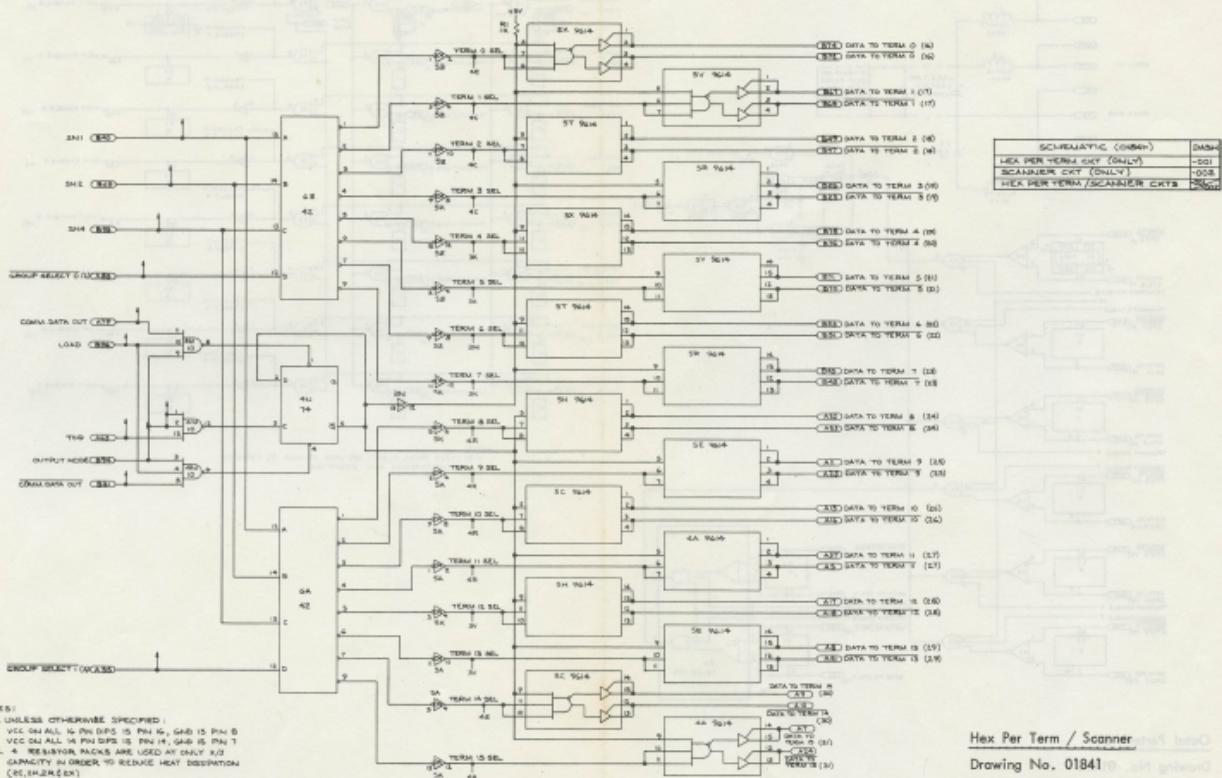
1. PC BOARD ETCH REV 1.
2. MAX COMPONENT HEIGHT AND LEAD PROTRUSION PER DESIGN SPCL-20.

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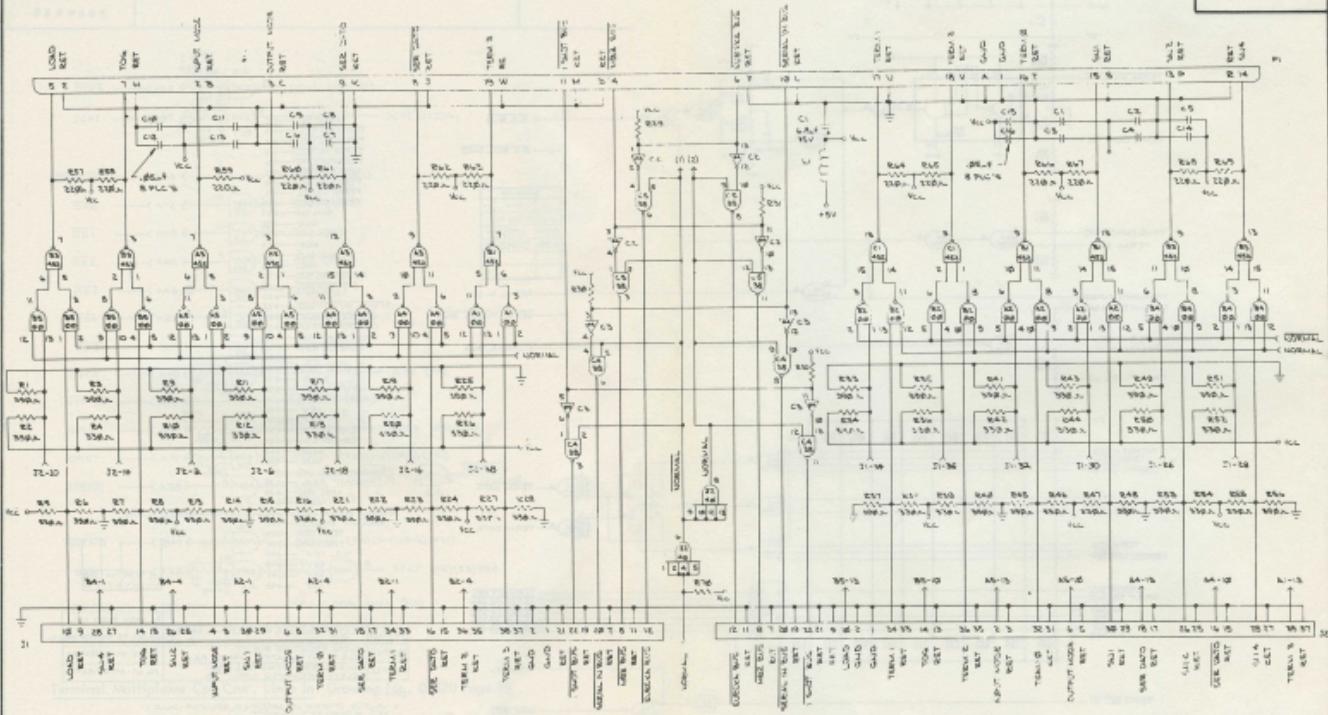


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Hex Per Term / Scanner
Drawing No. 01841
Blatt 1



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Logic Diagram Duplex Board Drawing No. 00134 - Blatt 1 von 1

C E701 750	0-25
B E706	0-25
NEW DESCRIPTION	