

TERMINAL MULTIPLEXER
DRAWING NO. 5D1570

PAGE NO.	DRAWING TITLE	REV LEVEL
P.1A	OVER VIEW	
P.1B	OUTSHIFT TIMING	
P.1C	INPUT TIMING	
P.1D	SCANNER STOP REQUEST	
P.2	SCANNING COUNTER	
P.3	DEVICE SELECT: TERM SEL.	
P.4	BUSY-DONE NET(SCNR)	
P.5	OUTPUT PULSEA	
P.6	DATA REGISTER (HOS'S-OUTPUT)	
P.7	DATA-REGISTER GATING	
P.8		
P.9	INPUT CORE ADDRESS	
P.10	INPUT CORE OVFL INTERRUPT	
P.11	MAIN TIMING GENERATOR	
P.12	DCH. NET	
P.13	INPUT FRAME ERROR	
P.14	INTP AND INTA NETS	
P.15		
P.16	BI-DIRECTIONAL DATA LINES (OUT)	
P.17	CPU CNTL LINES OUT	
P.18A+B	CPU DATA LINES IN	
P.19	CPU CNTL LINES IN	
P.20	IO CONNECTOR	
P.21	RET RIGGERRABLE TIMER	
PARTS LIST	BP-G2-45-01570-001	
ASSEMBLY	PA-G2-45-01570-001	

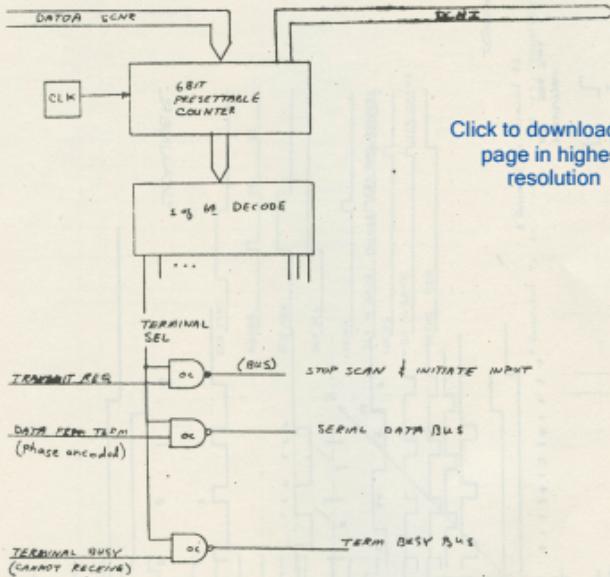
List of Documentation in this Section Drawing No. 01570 - Page

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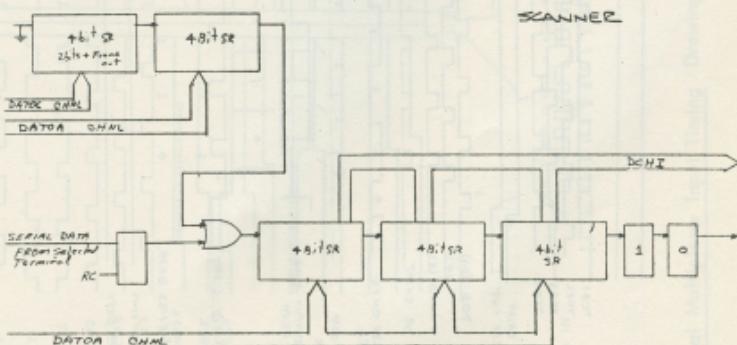
Rev.	Item	Part	Date	Rev.	
A	20	R16XB WAS R13, R17XB WAS R14, R19XB WAS R18, R18XB WAS R16, R21XB WAS R17, R20XB WAS R18, R22XB WAS R19, R23XB WAS R20, R3M8 WAS R21, R3M8 WAS R22, R4MB WAS R23, R5MB WAS R24 NEW RES # 100, (5,21,22,3,4,4 WERE 47A)			
B	276	3 4 18 19 20 21	DELETE LINES TO D6-5,(6,7 # 5 ADD 'SCHL NOT READY (P18) OFF Y4-6 ADD B6 & B4B INPUT LINES DELETE (P21) FROM "START" DELETE TERM 4,6,6#7 * TIE C16,17,18#19 TO D16,17,18,19 RESPECTIVELY MAJOR CHANGE TO TB CKT-DELETE T7-8,9#10 BREAK TB-3#4 -TB-4 -TB-6 -TB-3 - DIODES TO GND & GND, CAP C3 FROM .05 TO 10 WIRE ON S7-1 CHANGE TO S7-4 X1X ON S7-4 CHANGE TO S7-1		P6
C	289	21	ADD BT 7474		P6
D	307	3,4, 310 5,7, 315 8,11 12,14 18	UPDATE 7420-1 TO 7440, (B3,4,W7,V5,V4, R3B4U2 WAS R8, W3 RIGHT CENTER SH 11 WAS V3, ADD R3W3 SH8, ADD RIGHT SH12 REDRAW SEVERAL SHEETS AND RELEASE TO MANUFACTURING		P6
D	107	764	Released to production		P6
E	659	20	ADDED R THRU R AND IC'S		P6

Revision cover Sheet Scanner Logic Drawing No. 01570

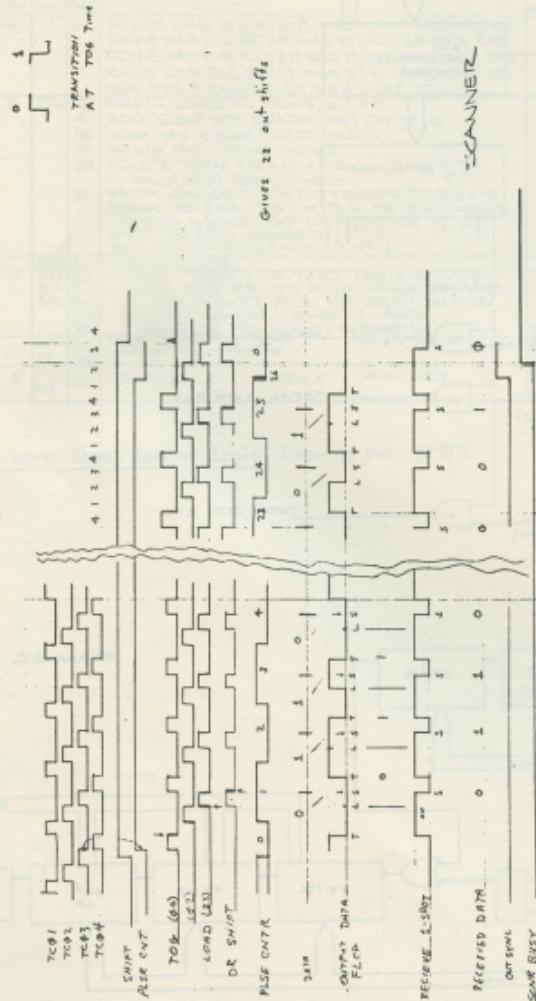
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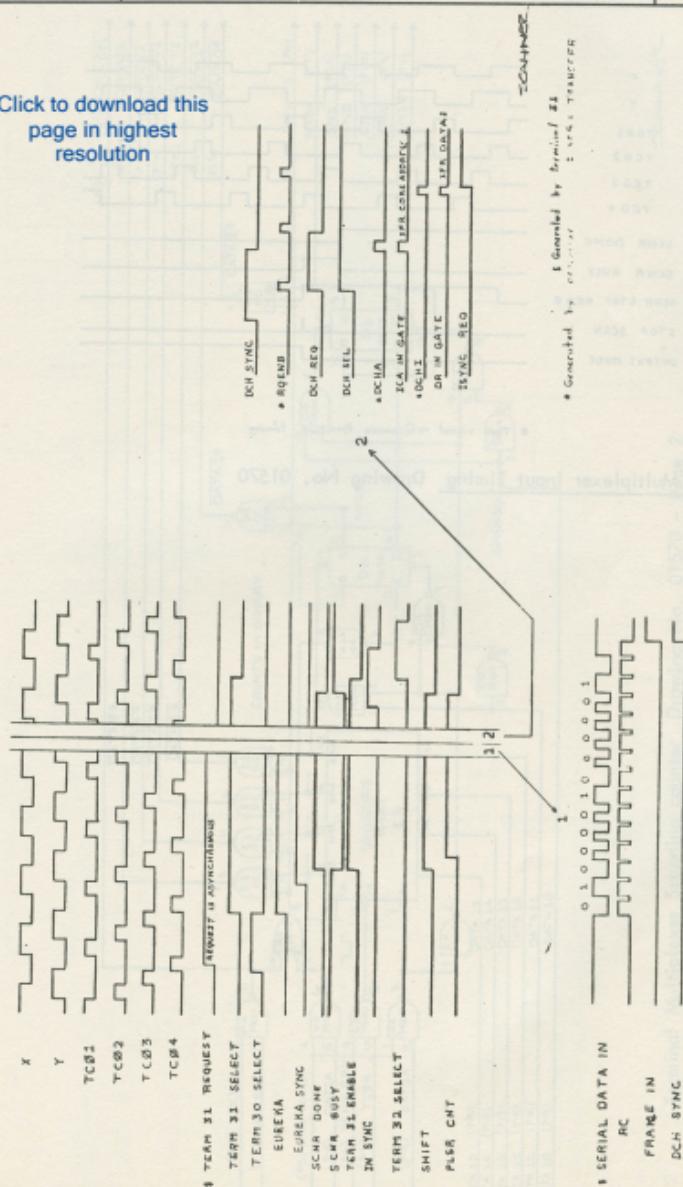


Terminal Multiplexer Over View Drawing No. 01570

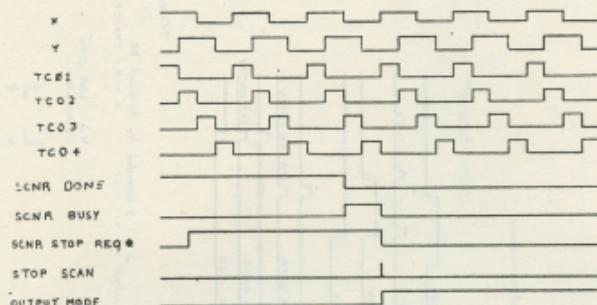


Terminal Multiplexer Outshift Timing Drawing No. 01570

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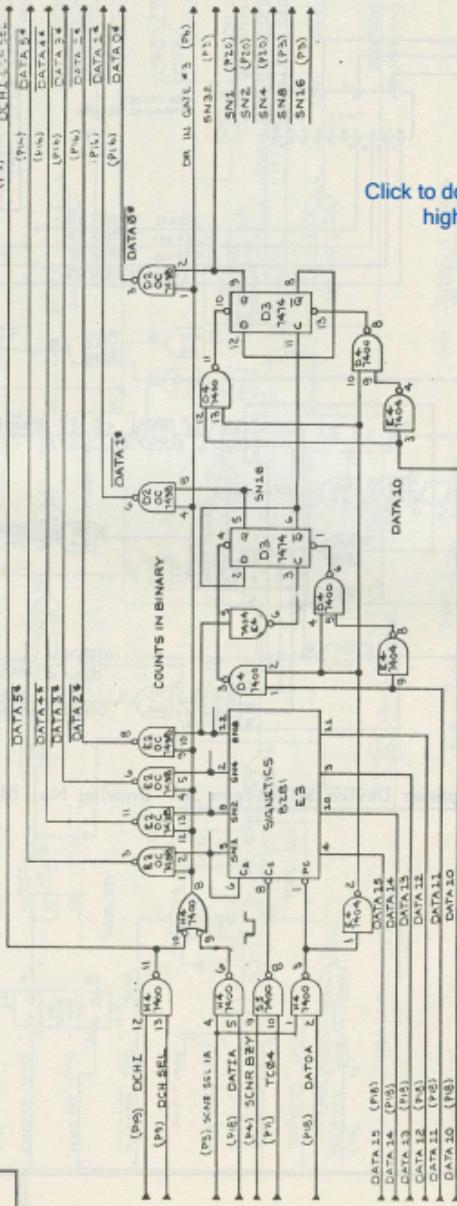
Terminal Multiplexer input Timing Drawing No. 01570



* This signal references Processor timing

Terminal Multiplexer Input Timing Drawing No. 01570

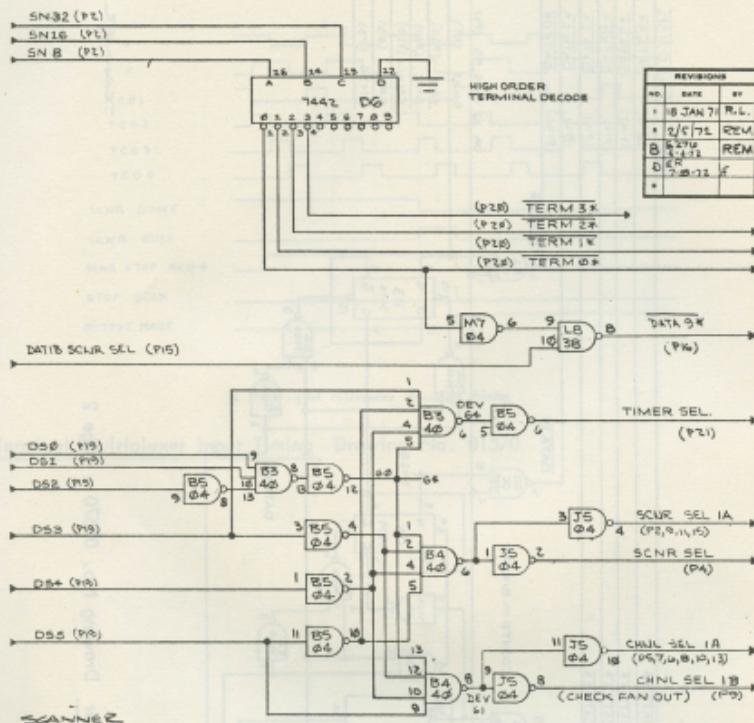
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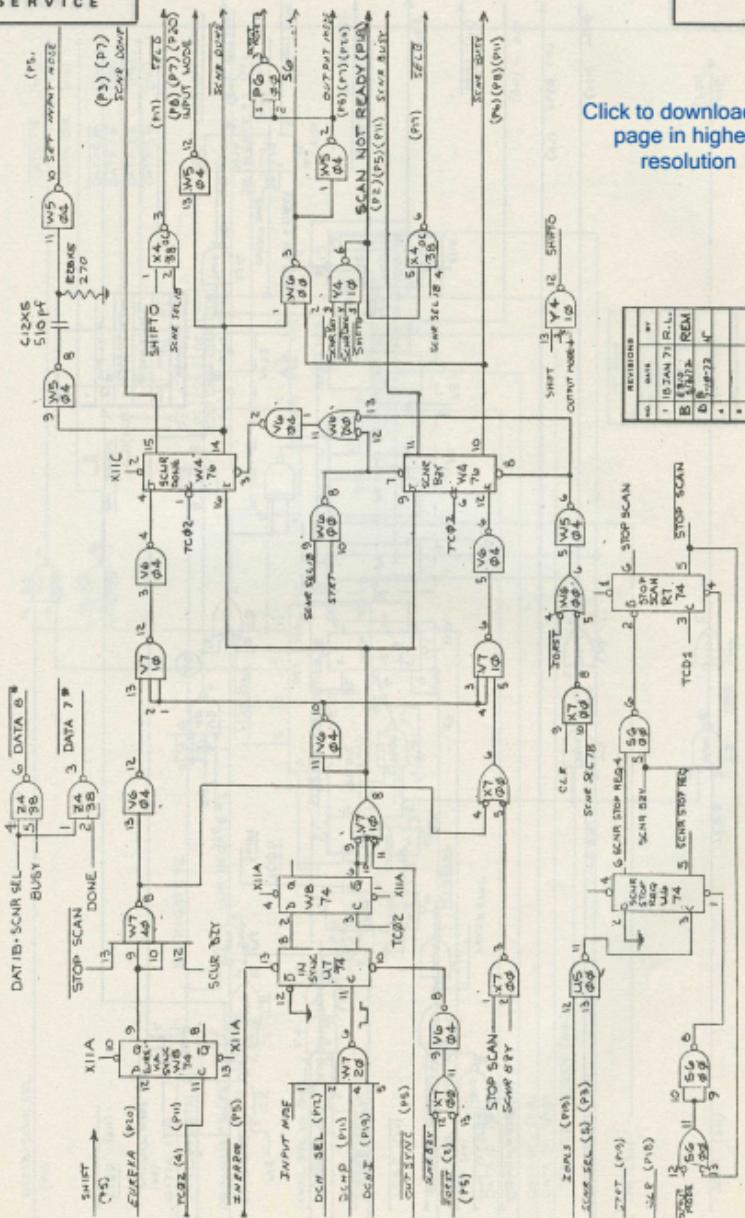
Terminal Multiplexer Scanning counter

Drawing No. 01570 - Page 2



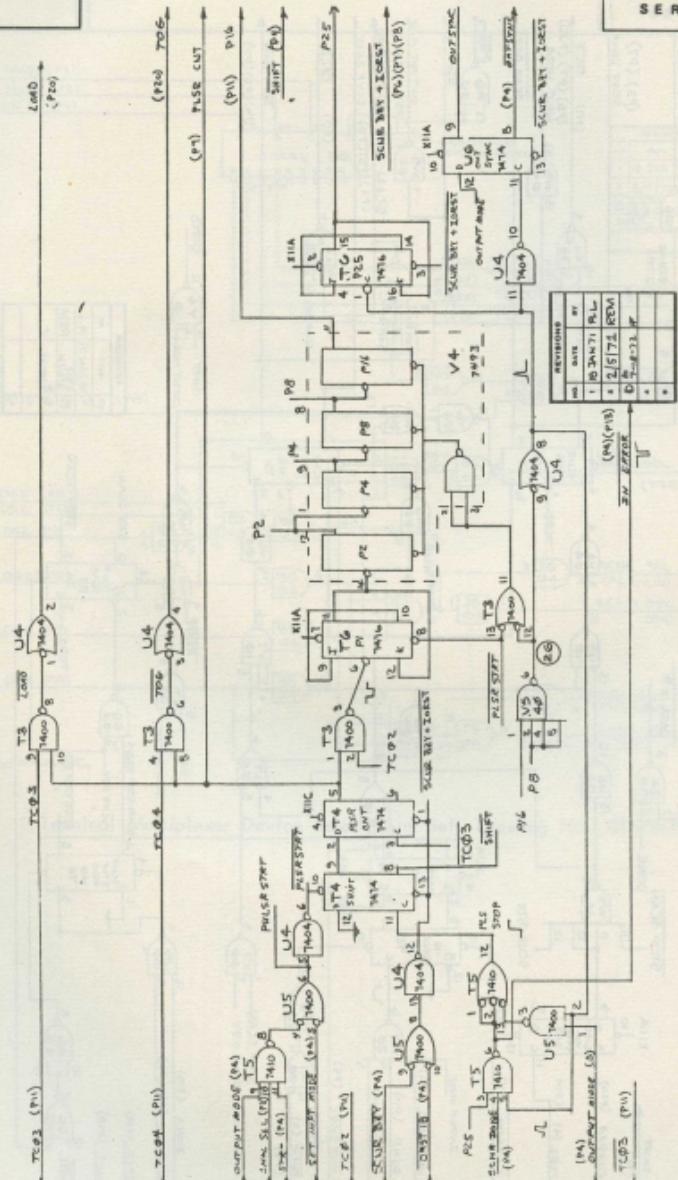
Terminal Multiplexer Device Select:Term Sel Drawing No. 01570- Page 3

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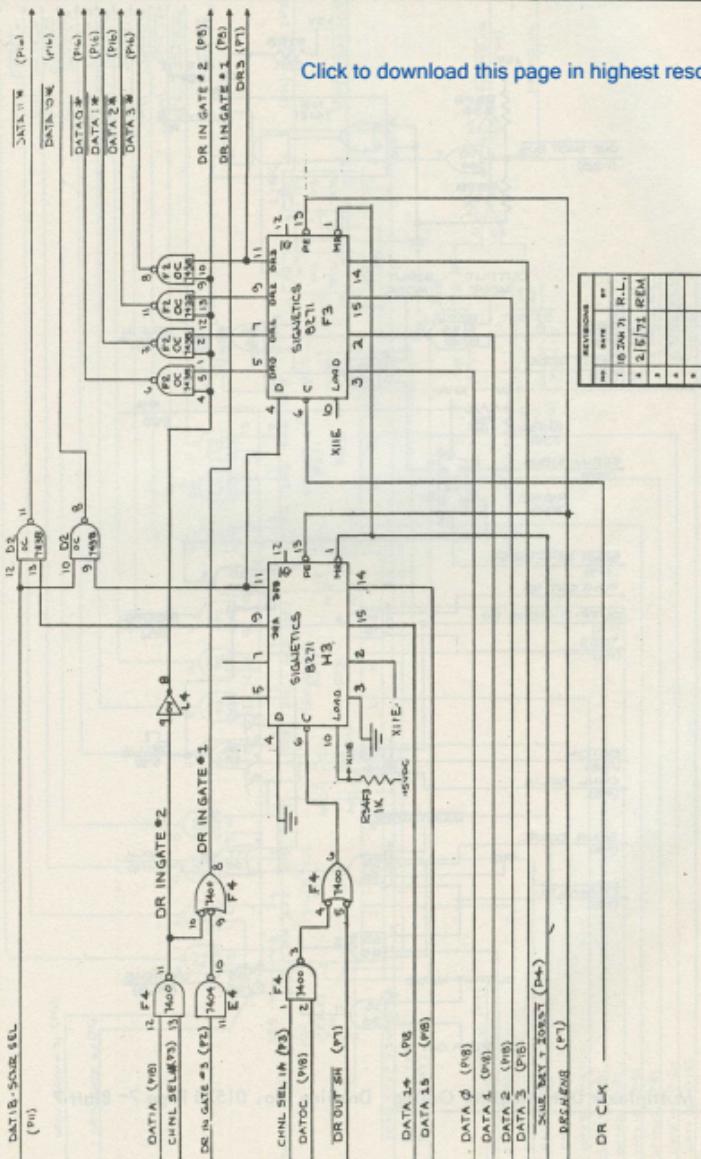


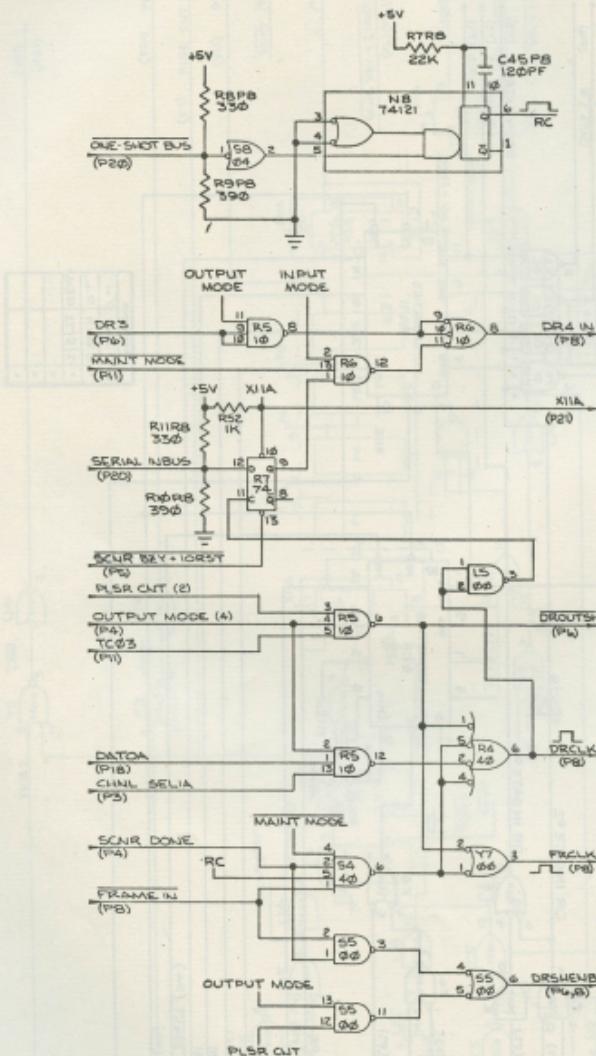
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**NIXDORF
COMPUTER
SERVICE**

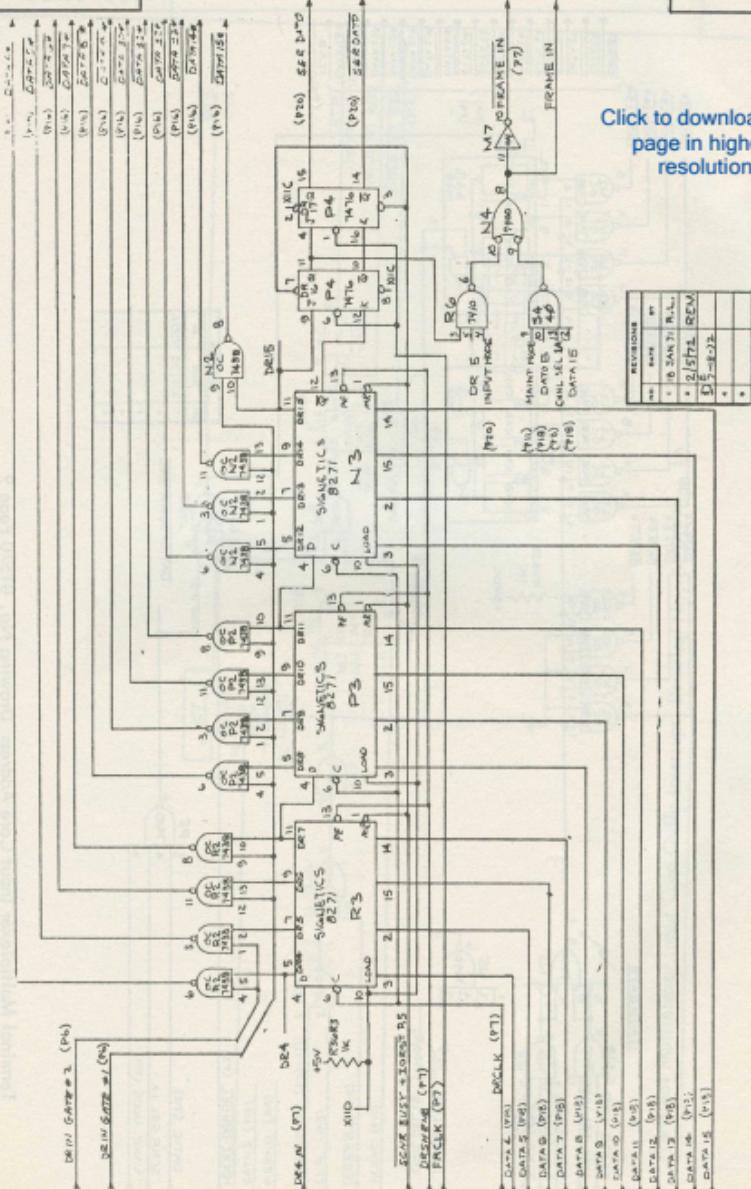


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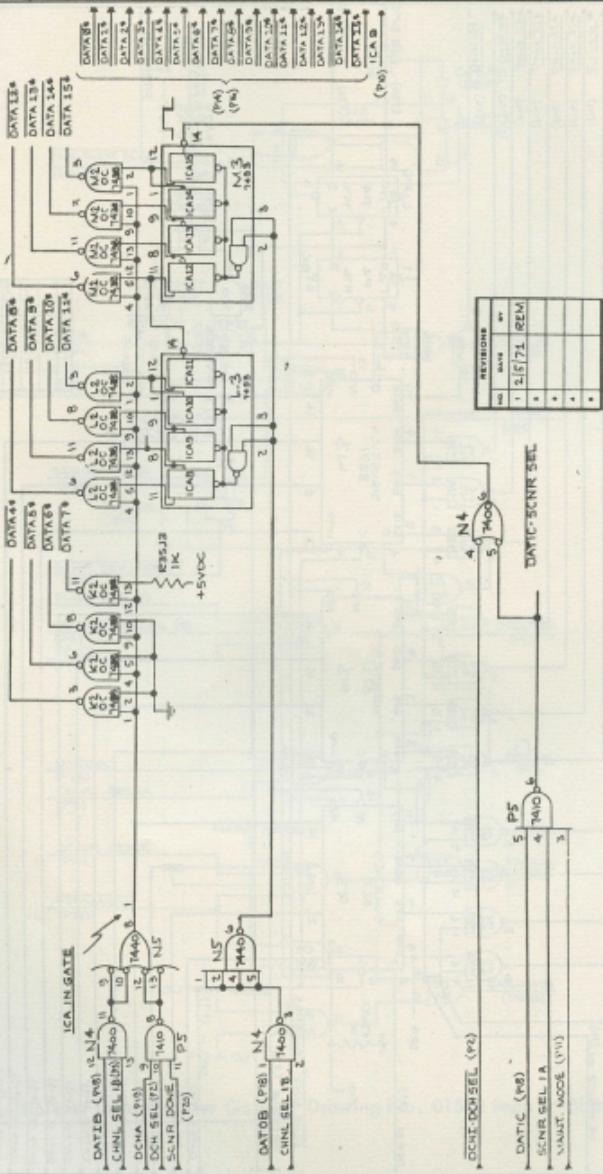


Terminal Multiplexer Data-Register Gating Drawing No. 01570 Page 7- Blatt 7

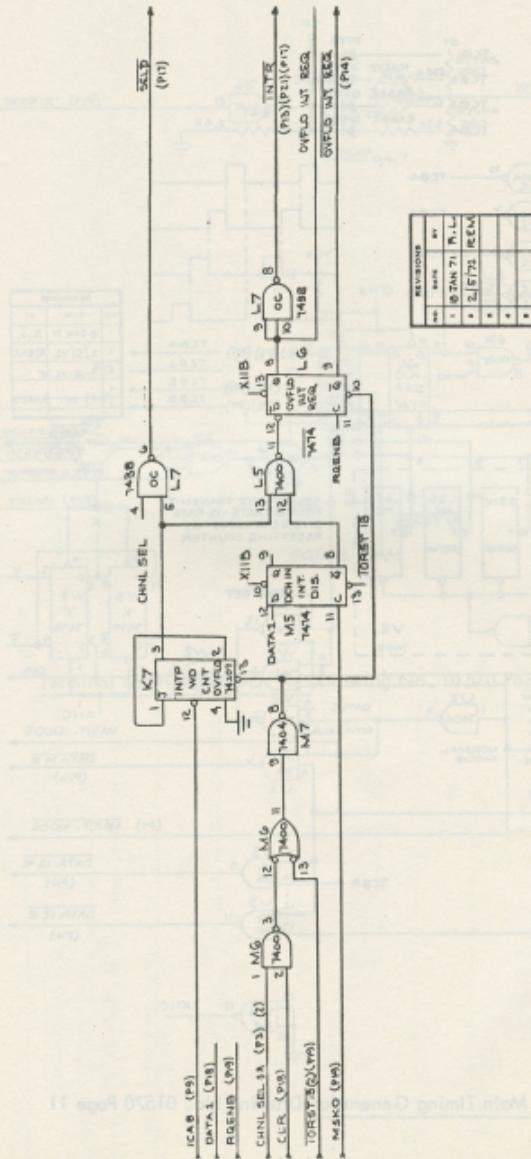


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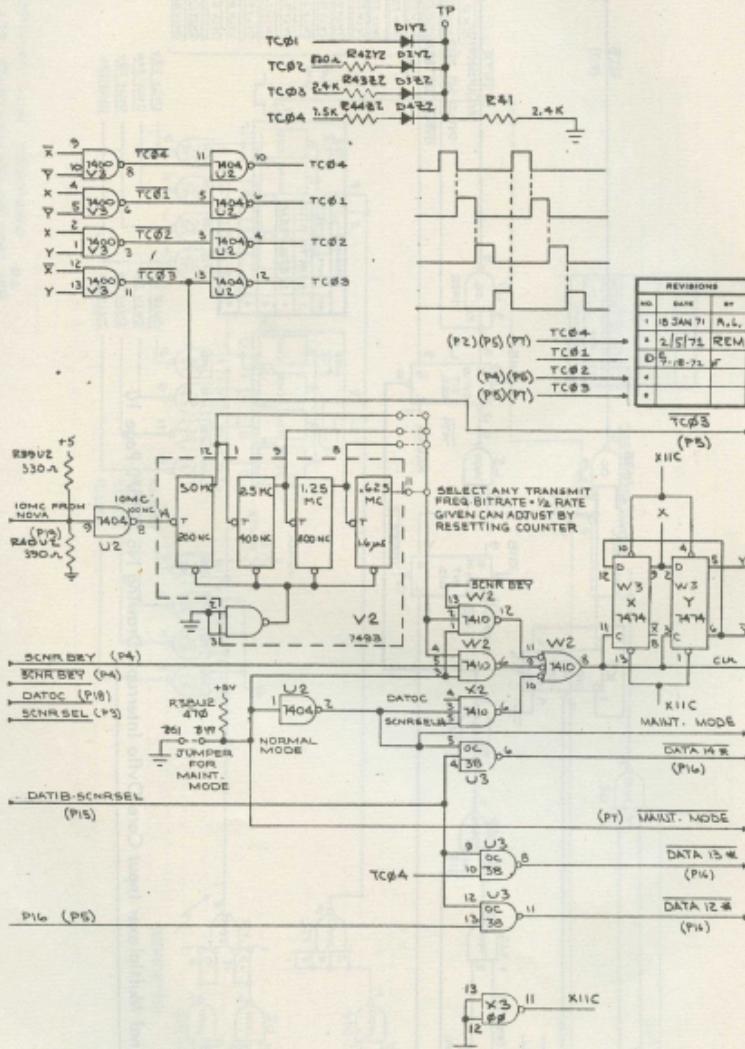
NOTE: LOCATIONS EXIST TO PUT
7415's FOR HIGH CHANNEL BBITS
BUT THEY ARE PERMANENTLY SET
TO 0000001 WITH JUMTERS.



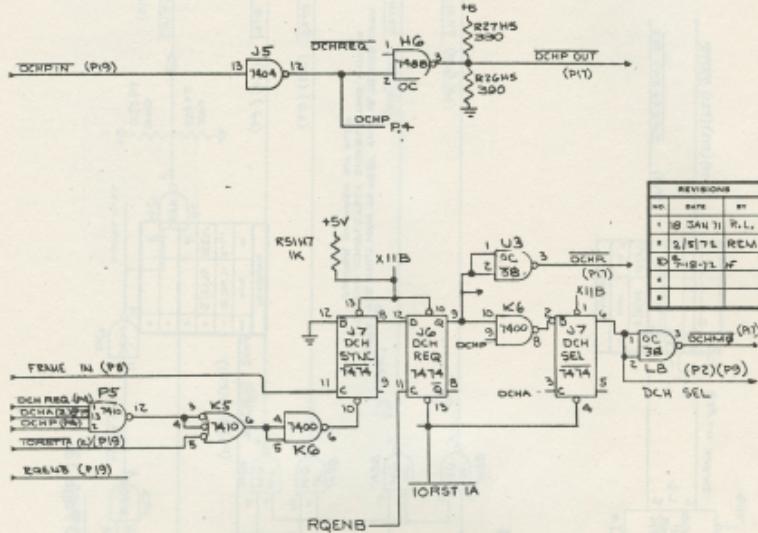
Terminal Multiplexer Input Core Address Drawing No. 01570 Page 9



Terminal Multiplexer Input Core Overflow Interrupt Drawing No. 01570 Page 10

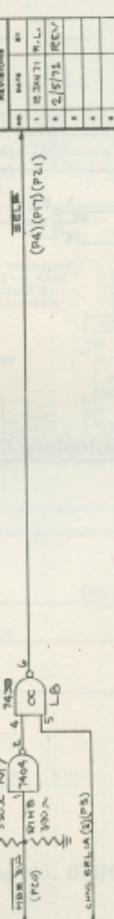
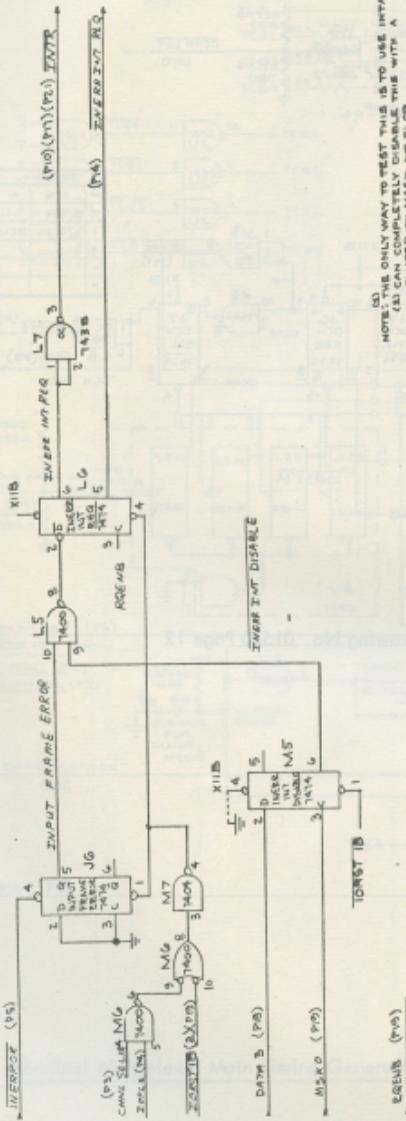


Terminal Multiplexer Main Timing Generation Drawing No. 01570 Page 11

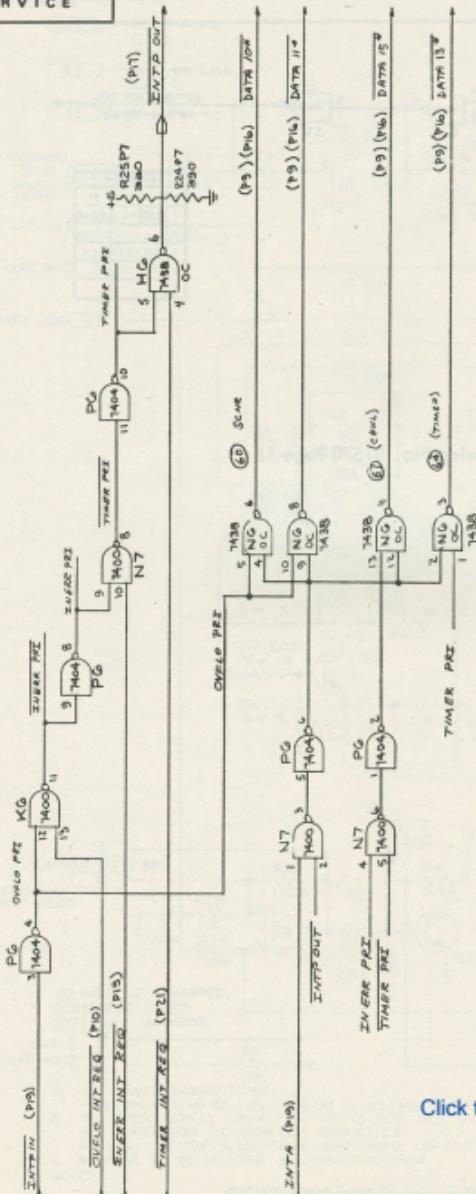


Terminal Multiplexer Dch Net Drawing No. 01570 Page 12

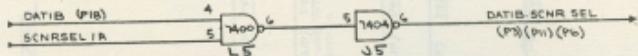
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Terminal Multiplexer Drawing No. 01570 Page 15

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