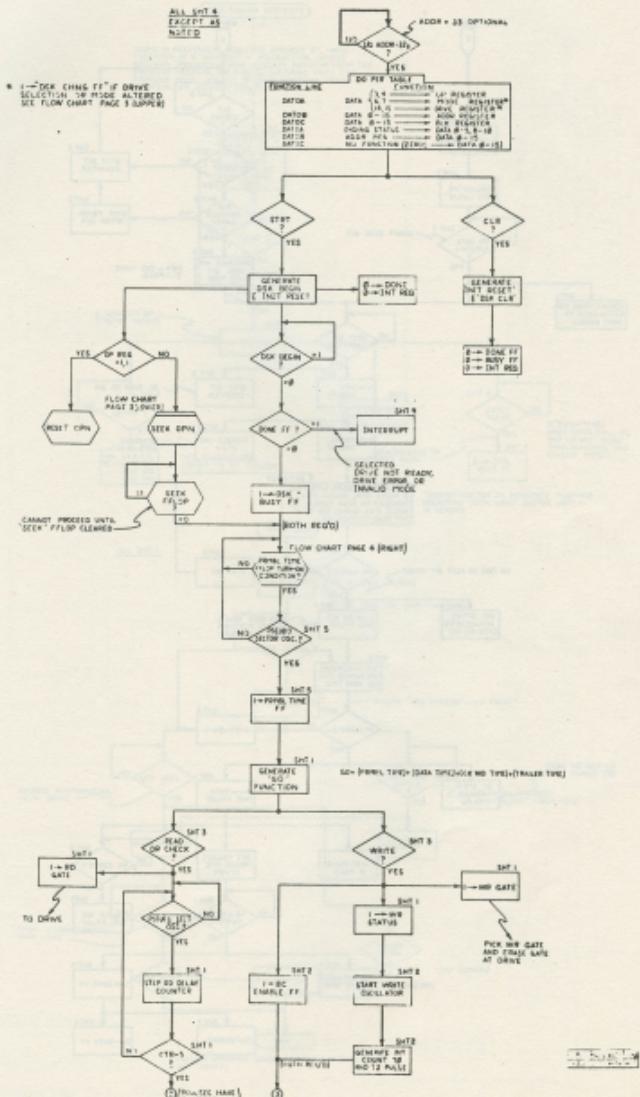
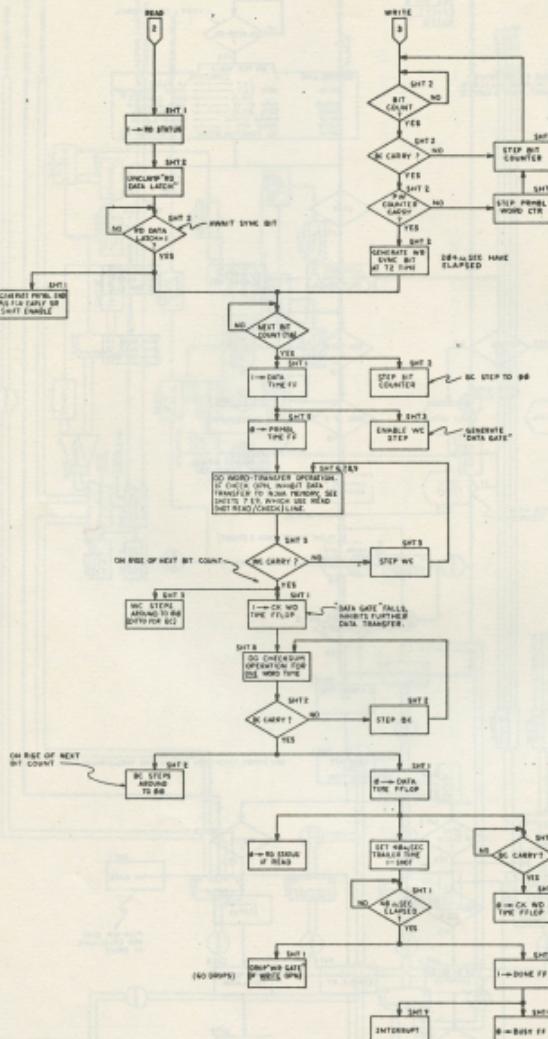


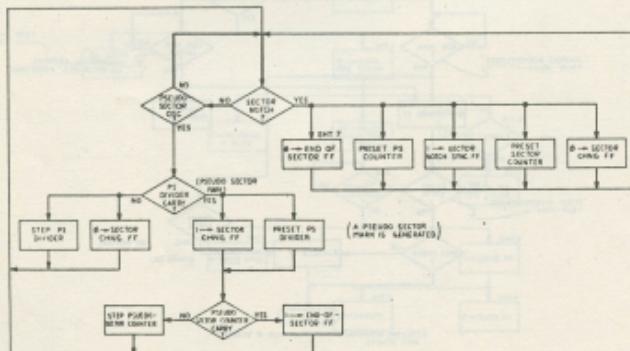
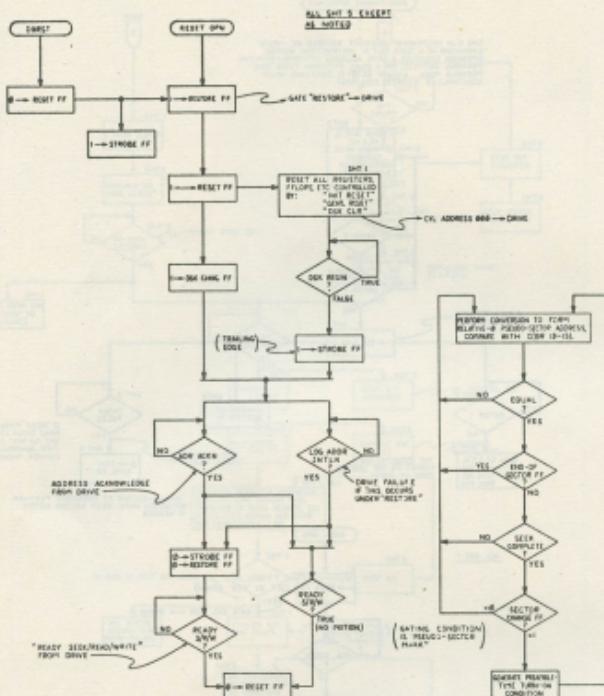
Figure 1 Disk Controller Overview Drawing No. 00107 - Blatt 3

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 Diese Unterlagen sind ausschließlich für
 Service-Zwecke bestimmt. Jede andere
 Verwendung ist strafrechtlich unzulässig.



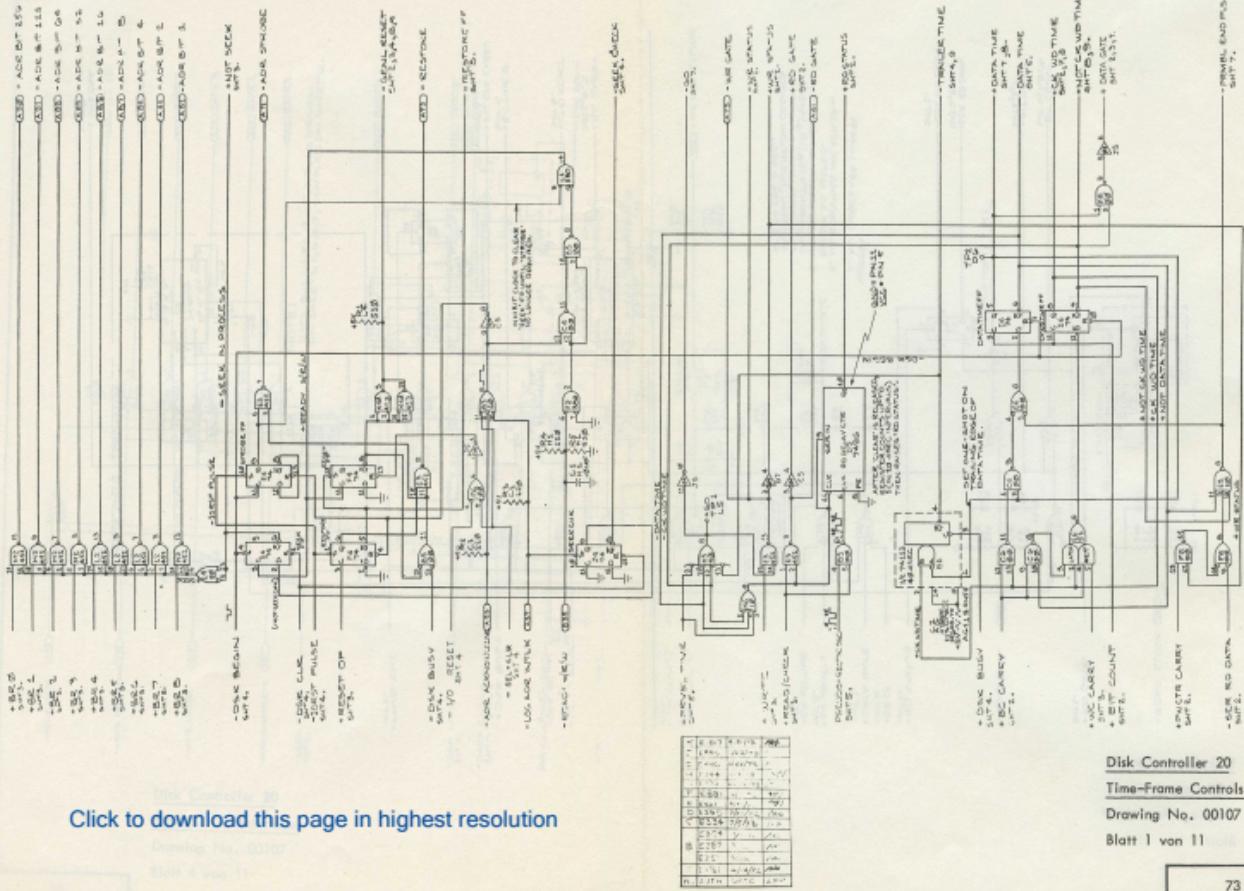


Flow Chart Disk Controller 20 Drawing No. 00135 - Blatt 2 von 4

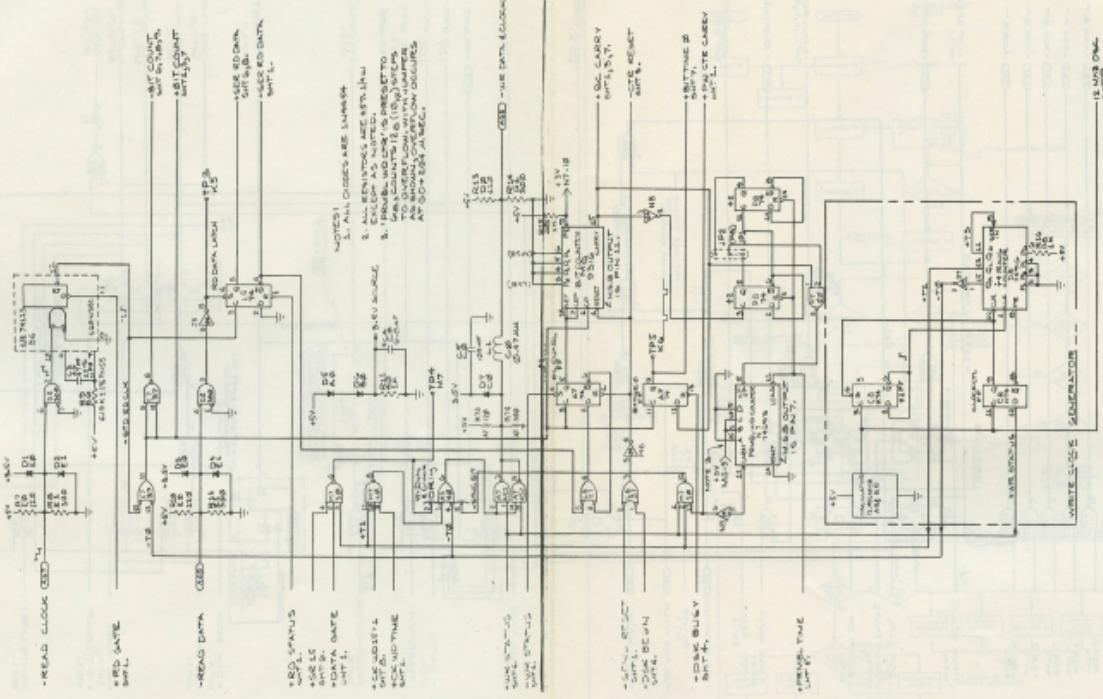


Flow Chart Disk Controller 20 Drawing No. 00135 – Blatt 4 von 4

SEEK, TIME-FRAME CONTROLS



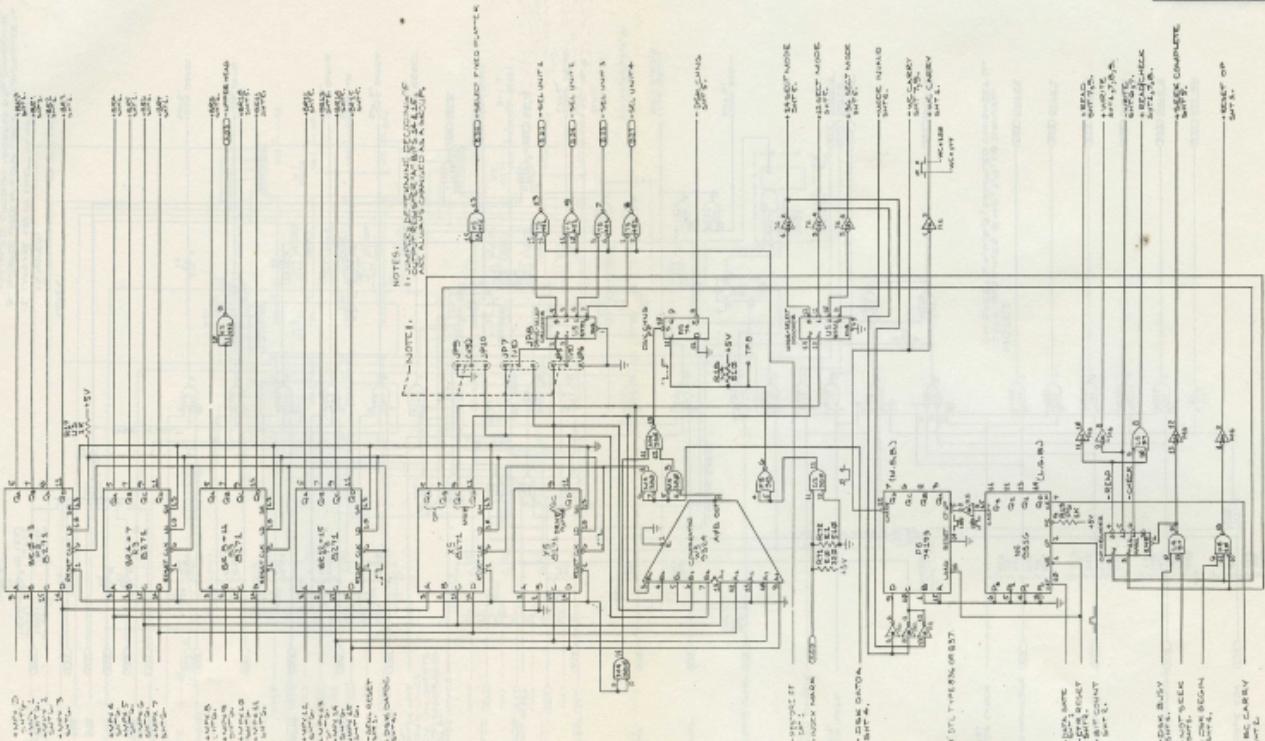
R/W DATA & CLOCK



Disk Controller 20
R/W Data & Clock
Drawing No. 00107
Blatt 2 von 11

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BLOCK/OP/MODE/DISK REGS

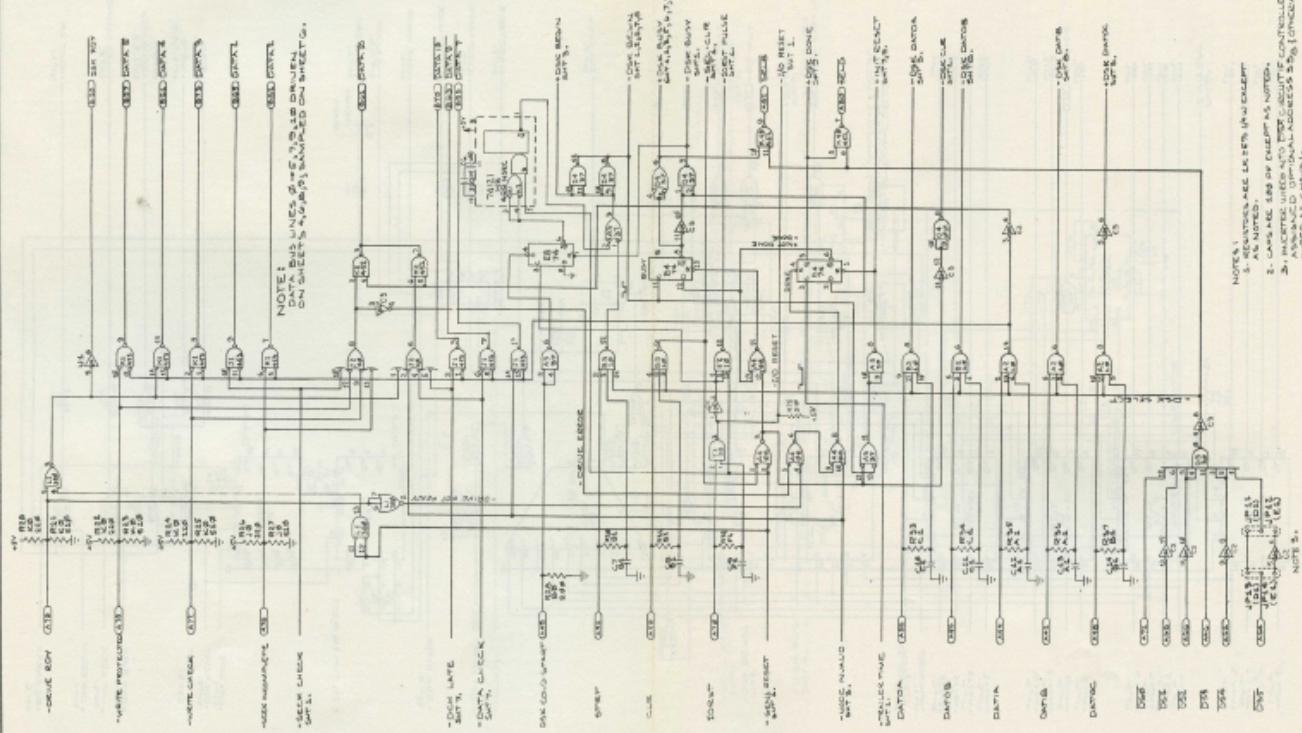


Block Controller
Serial / Parallel Data Path
Drawing No. 00107
Sheet 3 von 11

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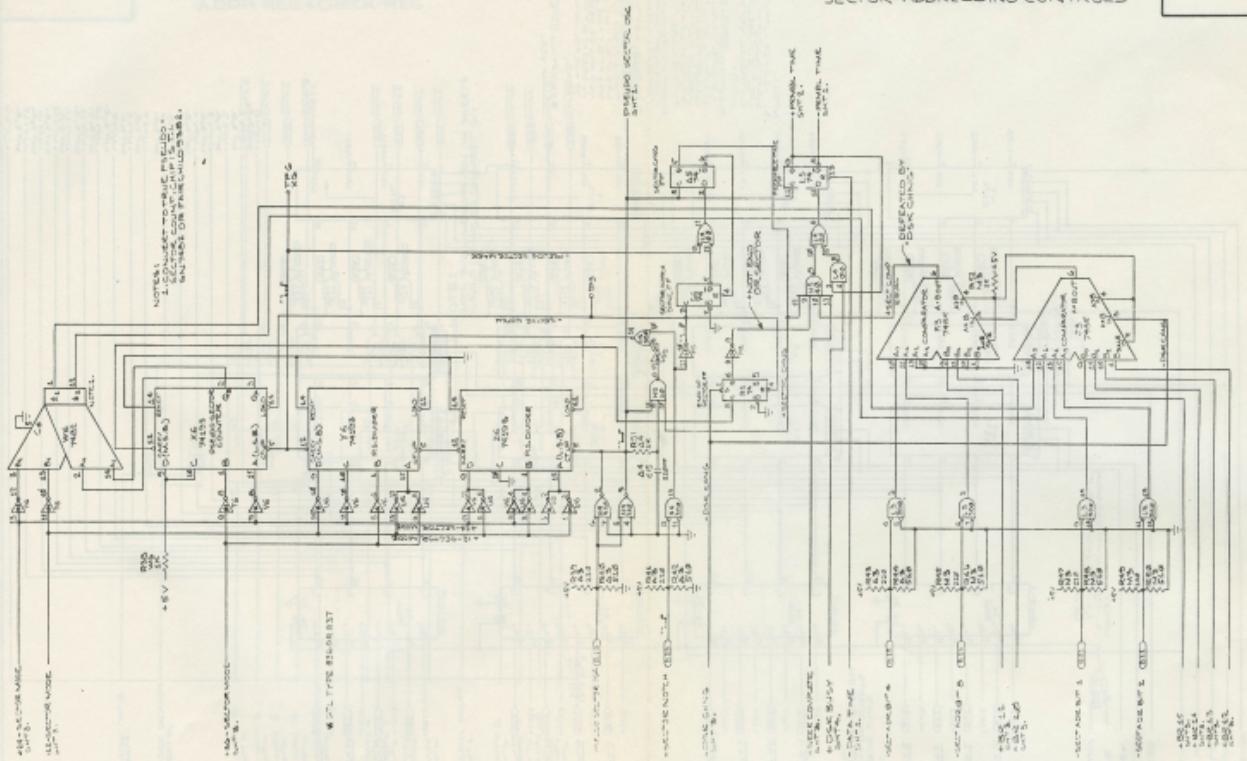
Disk Controller 20
Block / OP / Mode / Disk Regs
Drawing No. 00107
Blatt 3 von 11

PROCR CNTL INTERFACE



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SECTOR ADDRESSING CONTROLS



Disk Controller 20

ADSR Reg & Check

Drawing No. 00107

Blatt 5 von 11

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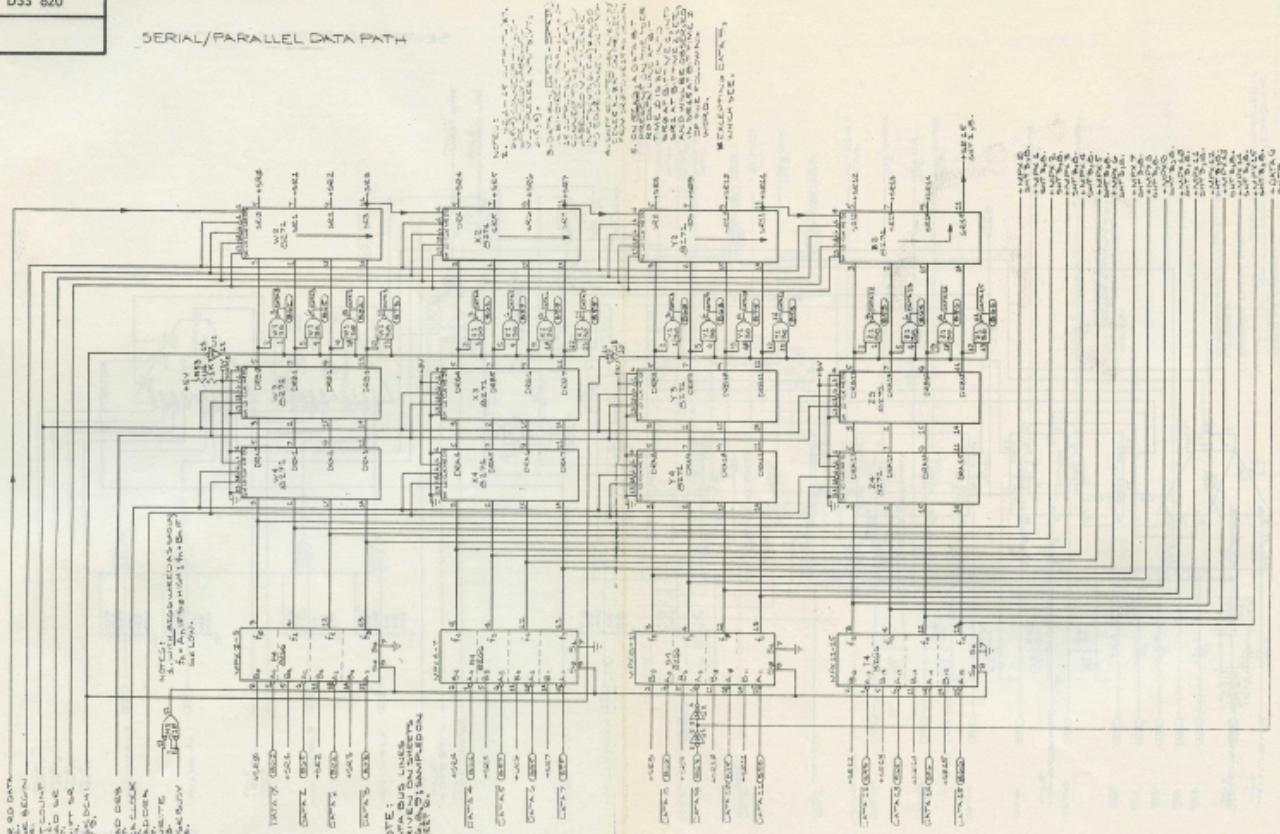
Disk Controller 20

Sector Addressing Controls

Drawing No. 00107

Blatt 5 von 11

SERIAL/PARALLEL DATA PATH



Disk Controller 20

Serial / Parallel Data Path

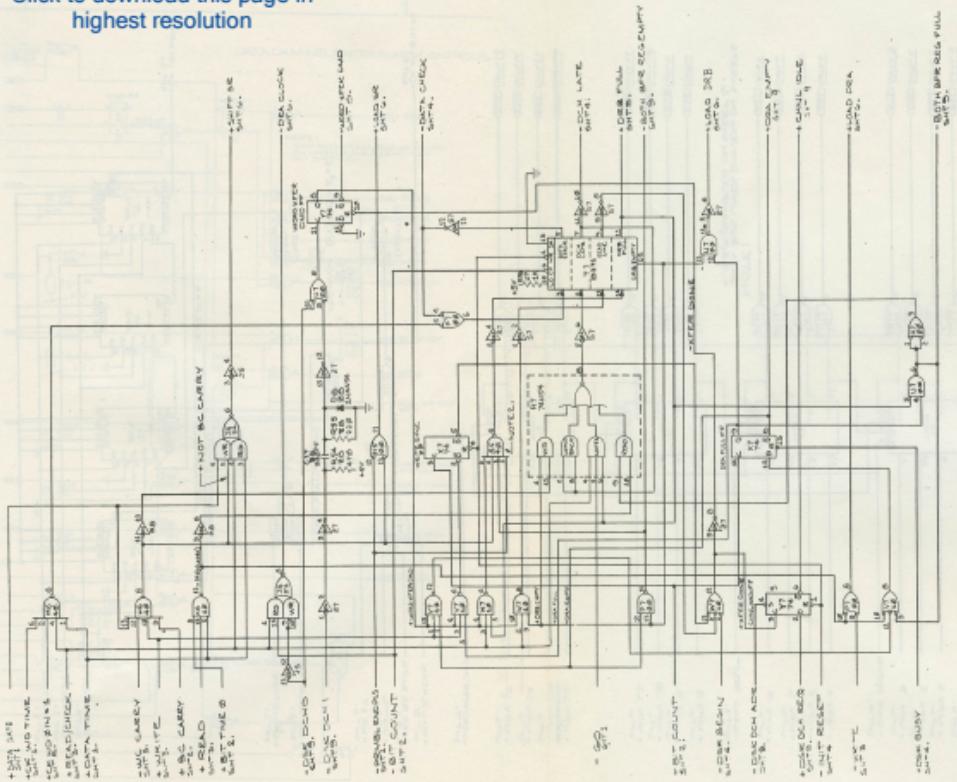
Drawing No. 00107

Blatt 6 von 11

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DRA/DRB/SR CONTROLS

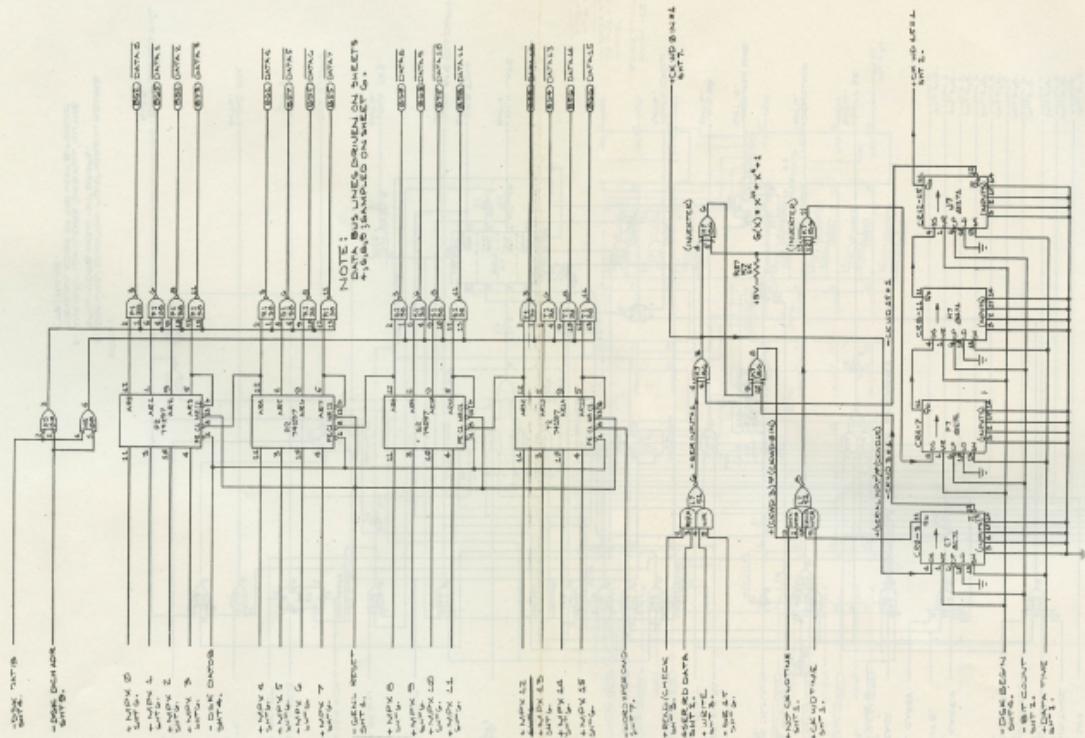
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Disk Controller 20
DRA / DRB / SR Controls
Drawing No. 00107
Blatt 7 von 11

- NOTES :
 1. DRA LATCHES -> ADDRESS IN UNIT WORD IN RECORDED
 2. SR LATCHES -> READ/WRITE DRB/DRB,
 3. A READ CYCLE CLEAR DRB/DRB ON THE
 4. END OF THE NEXT DRB CYCLE.

ADDR REG & CHECK REG



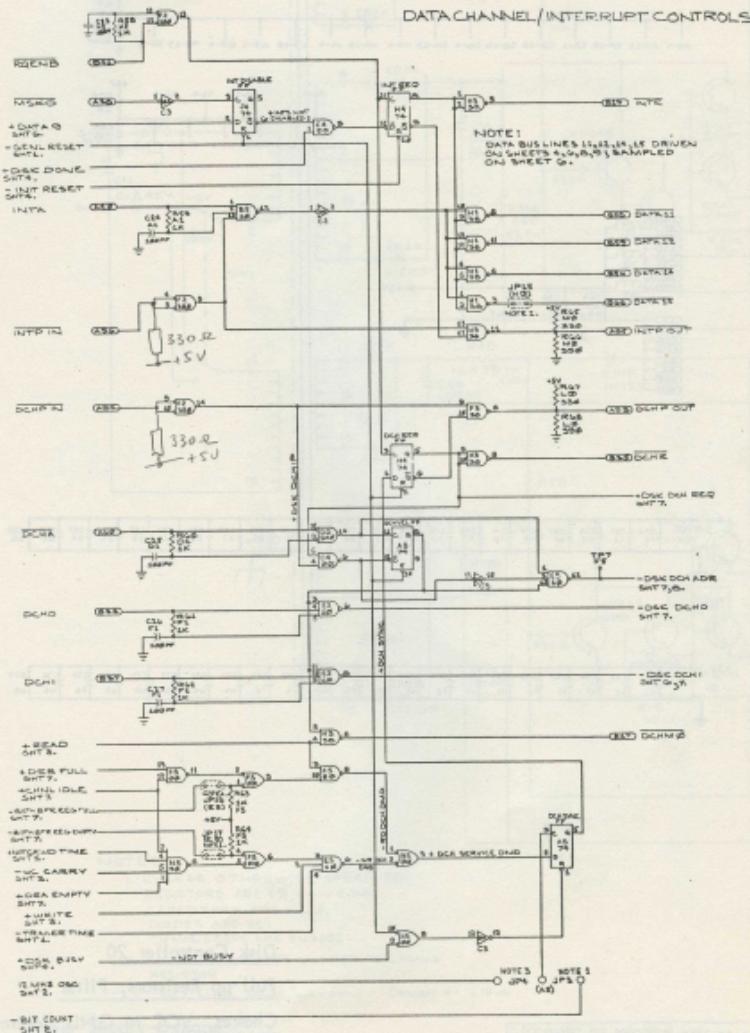
Disk Controller 20

ADDR Reg & Check Reg

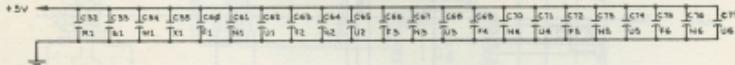
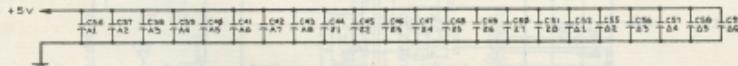
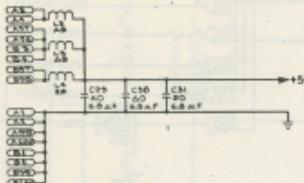
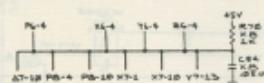
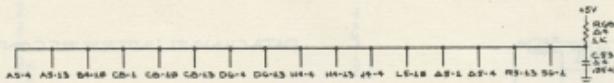
Drawing No. 00107

Blatt 8 von 11

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NOTES:
1. JUMPERS INSTALLED ONLY IF CONTROLLER USED
WITH STANDARD NOVA PROCESSOR.
2. JUMPERS INSTALLED ONLY IF CONTROLLER IS
ACCESSED VIA DISK ADDRESS BUS.
3. JUMPERS INSTALLED IF CONTROLLER USED
WITH OTHER THAN STANDARD NOVA.



Disk Controller 20

Pull up Resistors, Filter

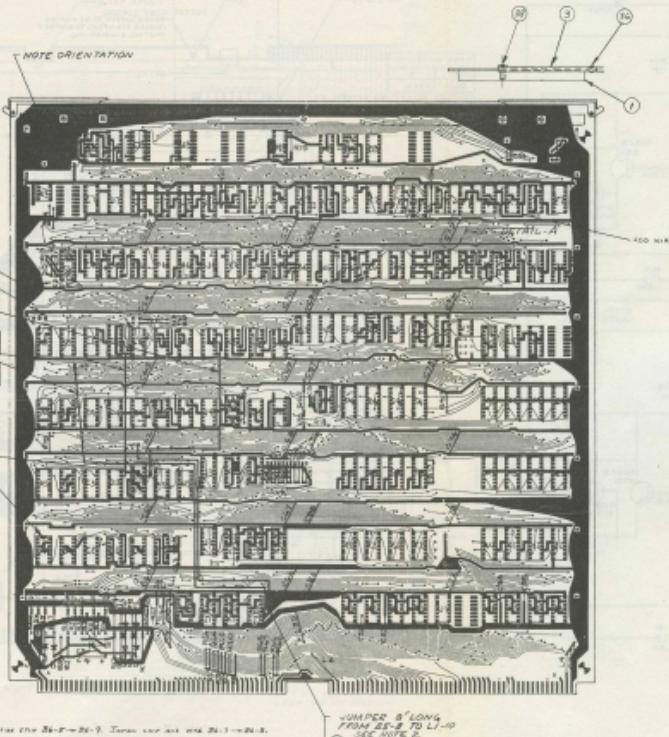
Chokes, VCC to GND Capacitors

Drawing No. 00107

Blatt 10 von 11

4. C101, C102, C103, C104 SMD
 5. LAST REF DESIGNATION FOR:
 CAPACITORS C107
 DIODES D14
 INTEGRATED CIRCUITS
 RESISTORS R18
 6. C105 THRU C107 AND C109 THRU C112 ARE .05MF
 7. C106 THRU C108 ARE .05MF 25V
- NOTES:

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3. ~~See Fig. 20-10~~ Cut connector for the 84-P-26-9. Take care not to damage the 24-1-16-16.
4. ATTACH JUMPER WIRE (ITEM 156) TO BOARD (ITEM 158) USING HORN SCREW (ITEM 155).
5. MAINTAIN CAPACITOR ASSEMBLY ON SIDE (WITH VALUE SHOWN) TO MAINTAIN AS LOW A PROFILE AS POSSIBLE WHEREVER SPACE PERMITS.

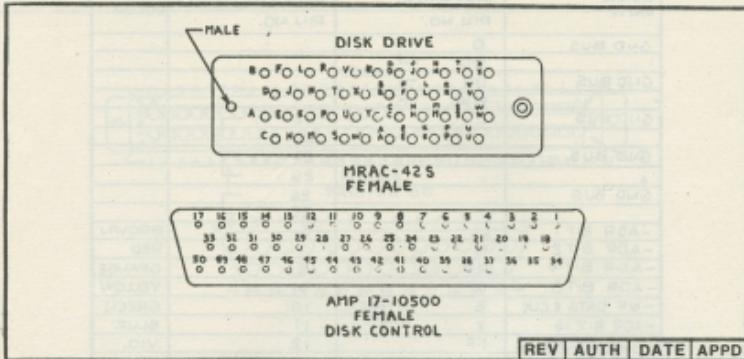
NOTES:

REF ID	DESCR	QTY	UNIT
E2-149		1	PCB
E2-154		1	PCB
E2-155		1	Screw
E2-156	ATTACH TO BOARD	1	Wire
E2-157	LOCATED PER REF. NO. 158	1	Capacitor
	DESCRIPTION		

Component Side Assembly
Drawing Disk Controller 20
Drawing No. 00107
Blatt 1 von 2

SIGNAL NAME	DISK DRIVE PIN NO.	DISK CONTROLLER PIN NO.	WIRE COLOR
GND BUS	D		
	DD	□	
GND BUS	DD	□	
	WW	□	
GND BUS		22	
		29	□
GND BUS		29	□
		26	□
GND BUS		26	□
		25	□
-ADR BIT 1	LL	6	BROWN
-ADR BIT 2	N	7	RED
-ADR BIT 4	RR	8	ORANGE
-ADR BIT 8	J	9	YELLOW
-WR DATA & CLK	B	10	GREEN
-ADR BIT 16	X	11	BLUE
-ADR BIT 32	FF	12	VIO.
-ADR BIT 64	T	13	GRAY
-ADR BIT 128	BB	14	WHITE
-INDEX MARK	Y	18	BROWN
-SEL UNIT 1	L	19	RED
-READ DATA	C	20	ORANGE
-SEL UNIT 2	R	21	YELLOW
-SEEK INCOMPNT	TT	44	GREEN
-LOG ADR INTLK	XX	45	BLUE
-SEL FIXED PLAT	NO CONNECTION	46	VIO.
-UPPER HEAD	AA	47	GRAY

SIGNAL NAME	DISK DRIVE PIN NO.	DISK CONTROLLER PIN NO.	WIRE COLOR
-WR PROTECTED	P	48	WHITE
-ADR STROBE	SS	1	BROWN
-READ GATE	E	2	RED
-WRITE CHECK	HH	3	ORANGE
-SECTOR NOTCH	W	4	YELLOW
-RESTORE	VV	5	GREEN
-SEL UNIT 3	V	30	BLUE
-READ CLOCK	A	31	VIO.
-SEL UNIT 4	Z	32	GRAY
PSEUDO SECT OSC	S	33	WHITE
-SECT ADR BIT 1	CC	34	BROWN
-SECT ADR BIT 2	JJ	35	RED
-SECT ADR BIT 4	KK	36	ORANGE
-SECT ADR BIT 8	MM	37	YELLOW
-ADR BIT 256	NO CONNECTION	38	GREEN
-WR GATE	EE	39	BLUE
-DRIVE RDY	U	40	VIO.
-READY S/R/W	F	41	GRAY
-ADR ACKN	NN	42	WHITE


Cable Configuration Disk Control To Disk Drive

Cable Configuration Disk Control To Disk Drive