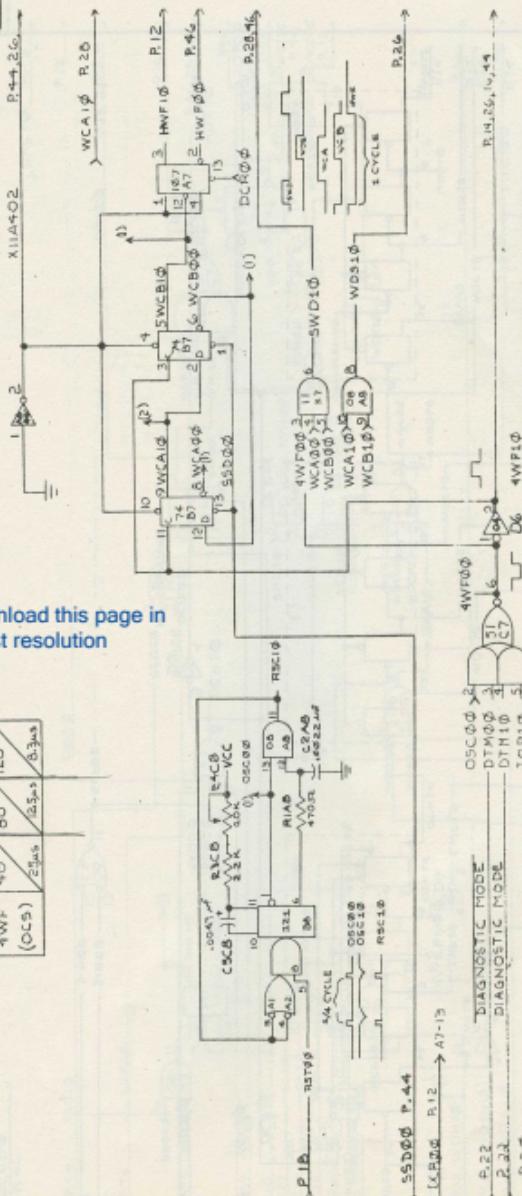
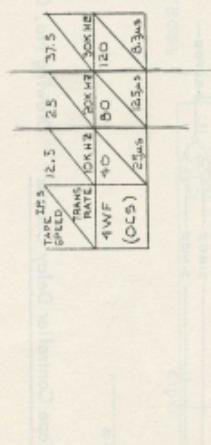


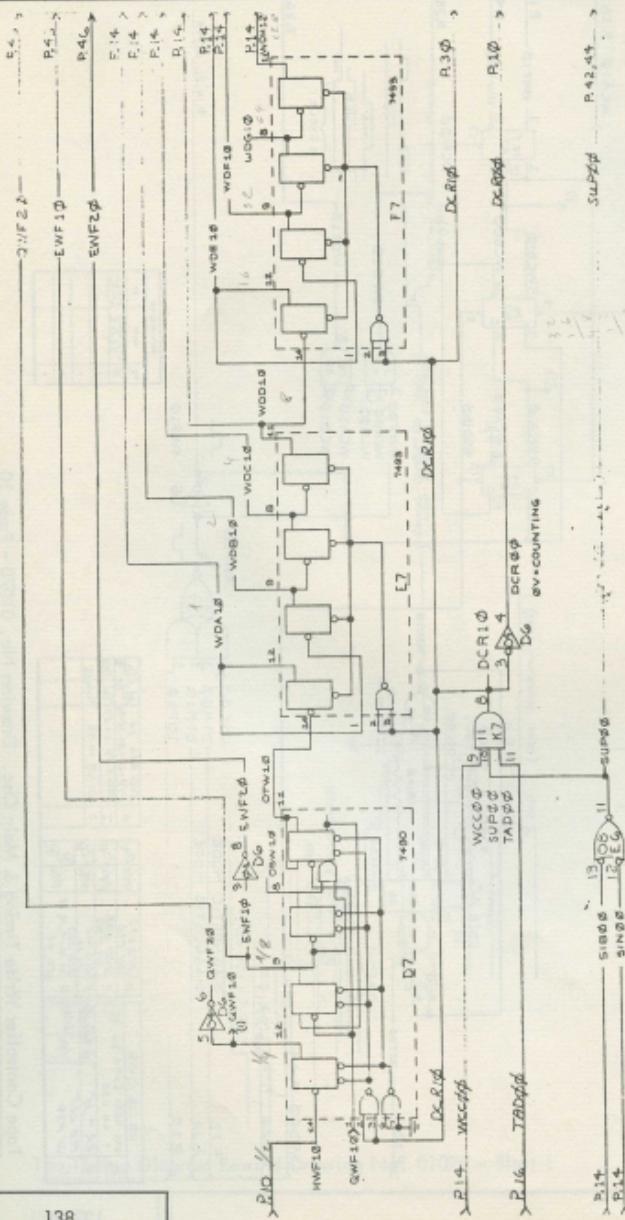
Tcu Timing Diagram Rewind Drawing No. 01070 - Blatt 1

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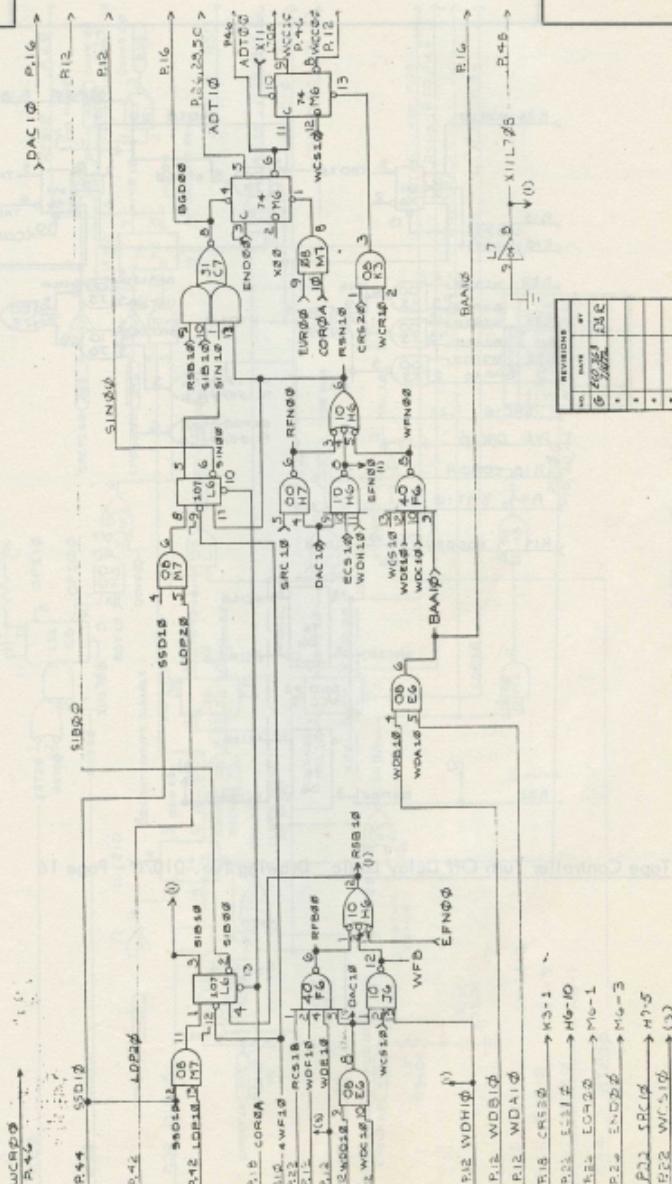
Line	Function
P.44	SWLD
P.26	WCA1φ
P.25	WCA2φ
P.14	SWD1φ
P.5	WCB1φ
P.47	WCB2φ

Line	Function
P.44	SWLD
P.26	WCA1φ
P.25	WCA2φ
P.14	SWD1φ
P.5	WCB1φ
P.47	WCB2φ



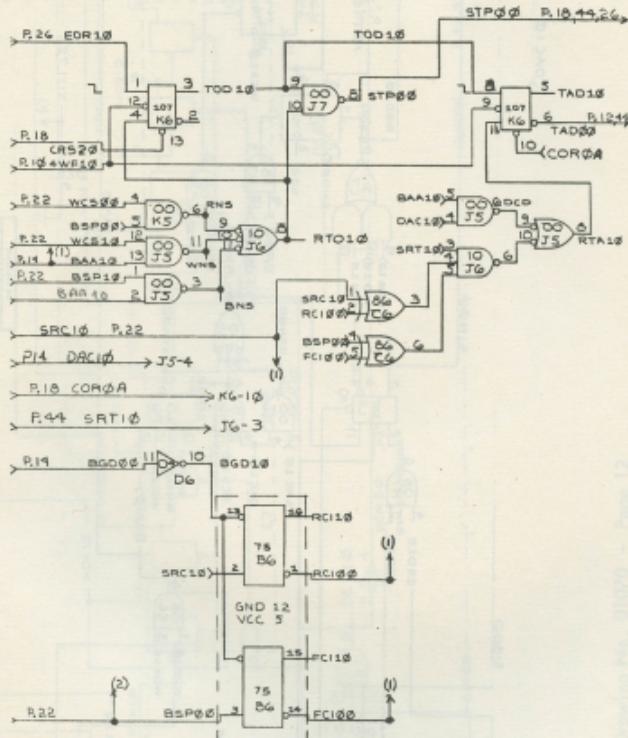
Tape Controller Delay Counter

Drawing No. 01070 - Page 12



Tape Controller Turn on Delay Logic Drawing No. 01070 - Page 14

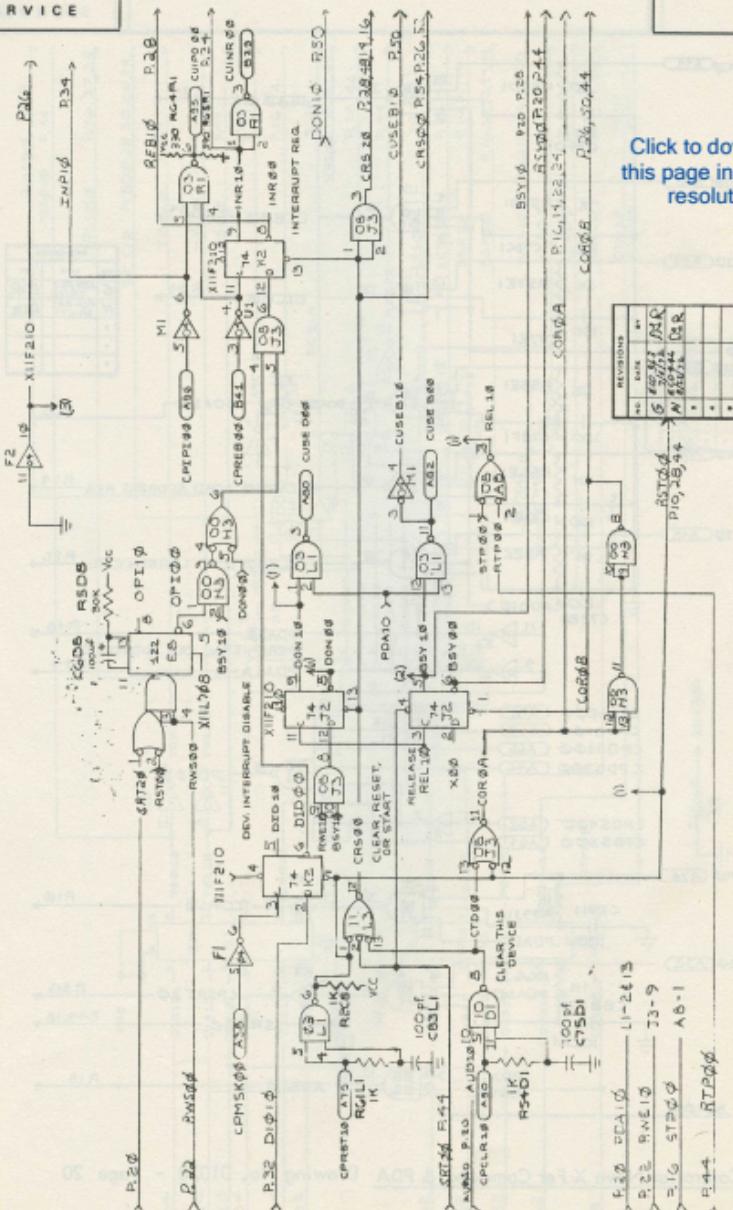
REVISIONS	
P.02	S.03/2
P.03	E.04/2.2
P.2.2	S.04/2.2

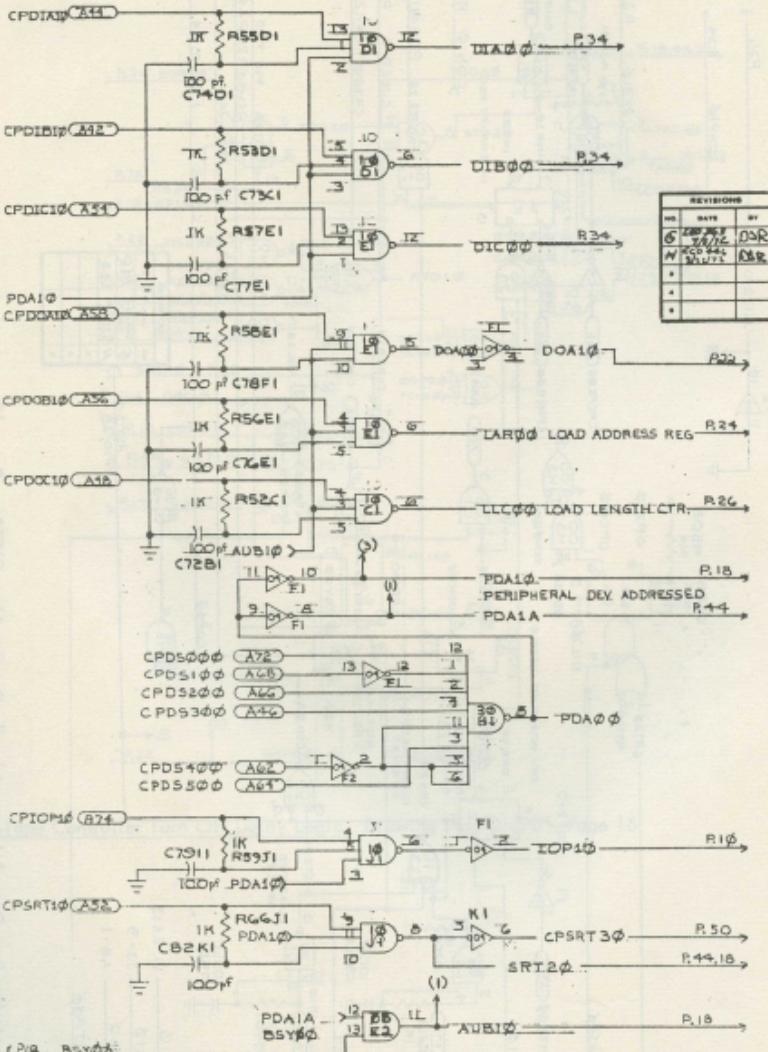


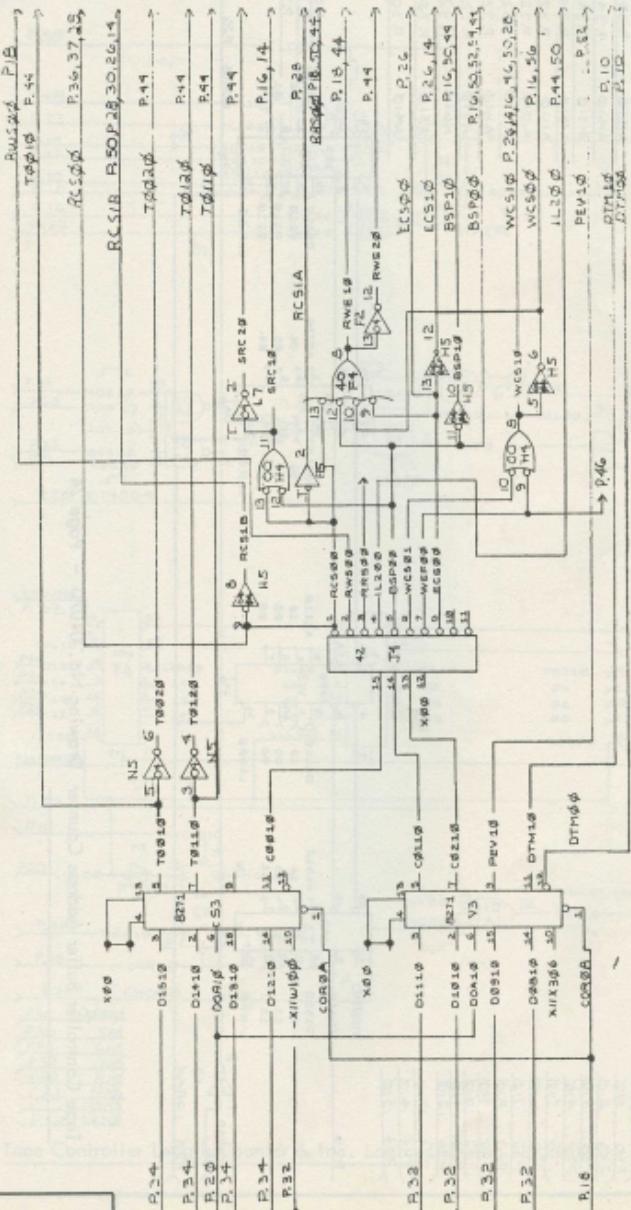
Tape Controller Turn Off Delay Logic Drawing No. 01070 - Page 16

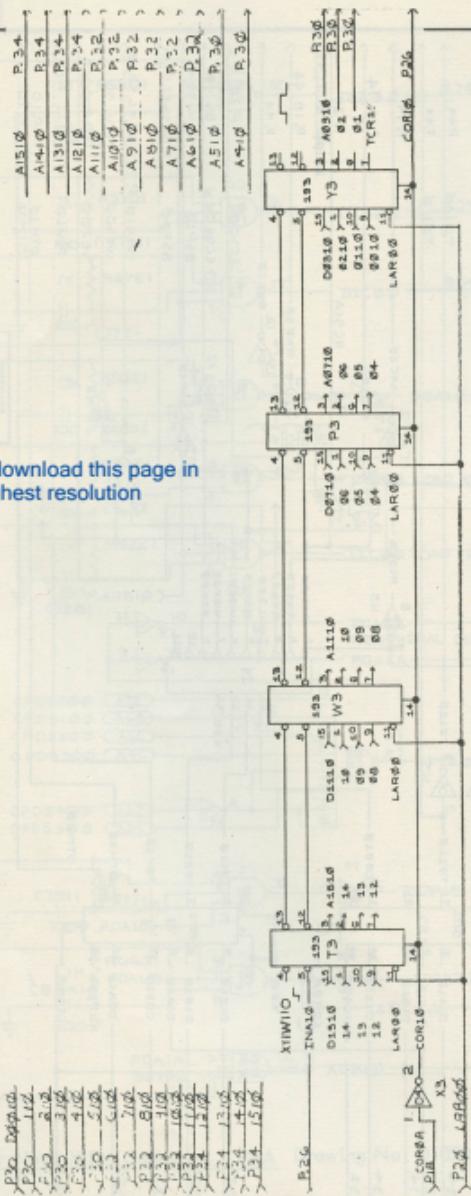
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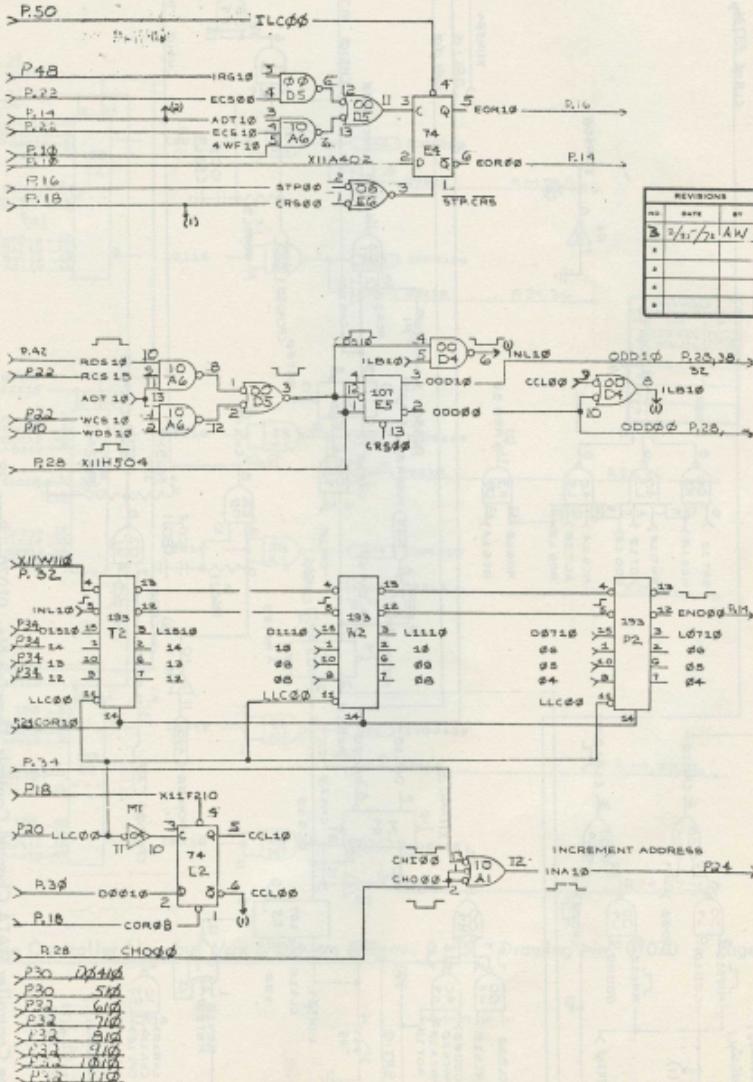




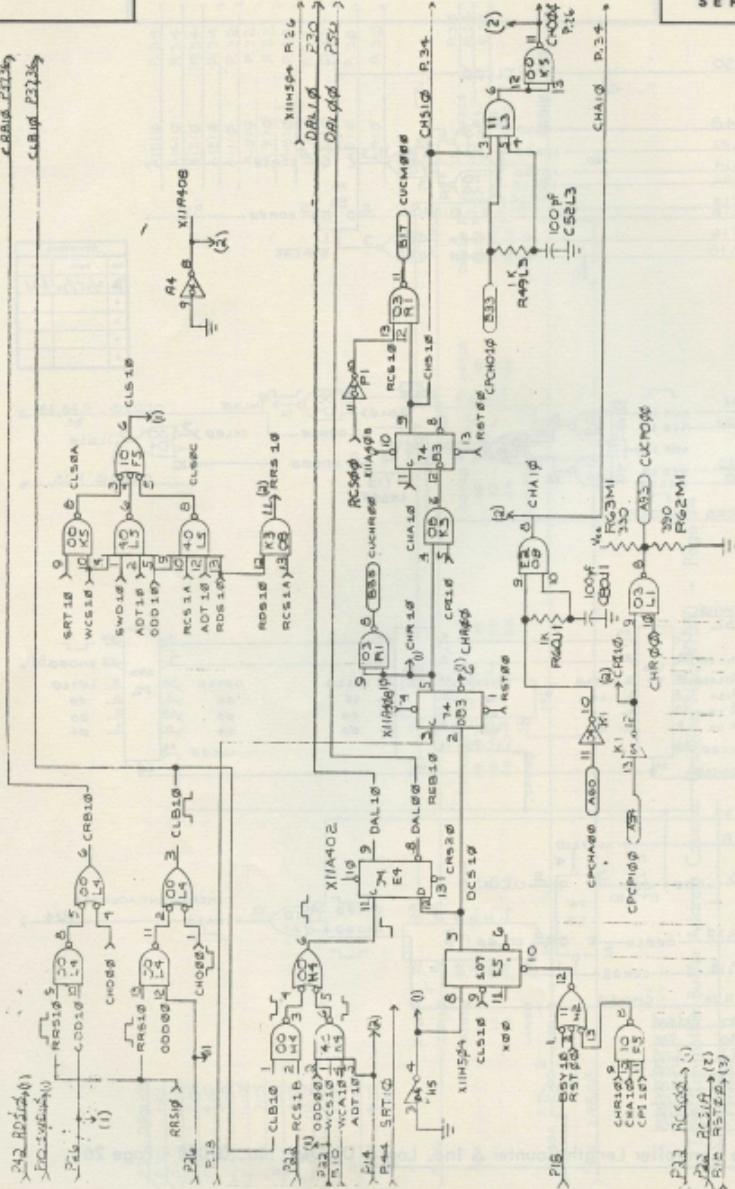


Tape Controller Buffer Address Counter Drawing No. 01070 - Page 24

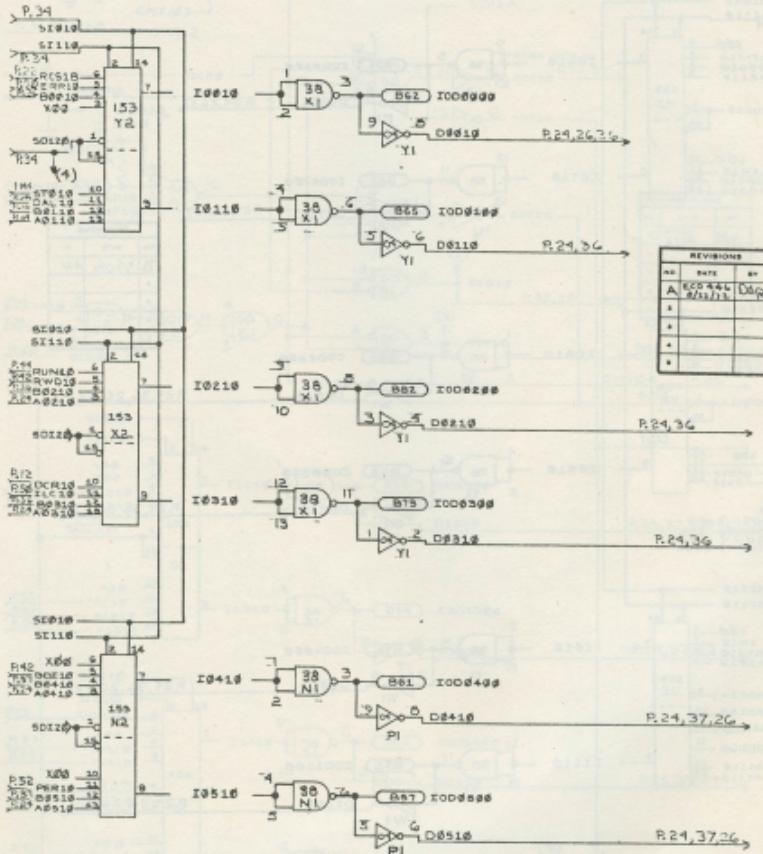
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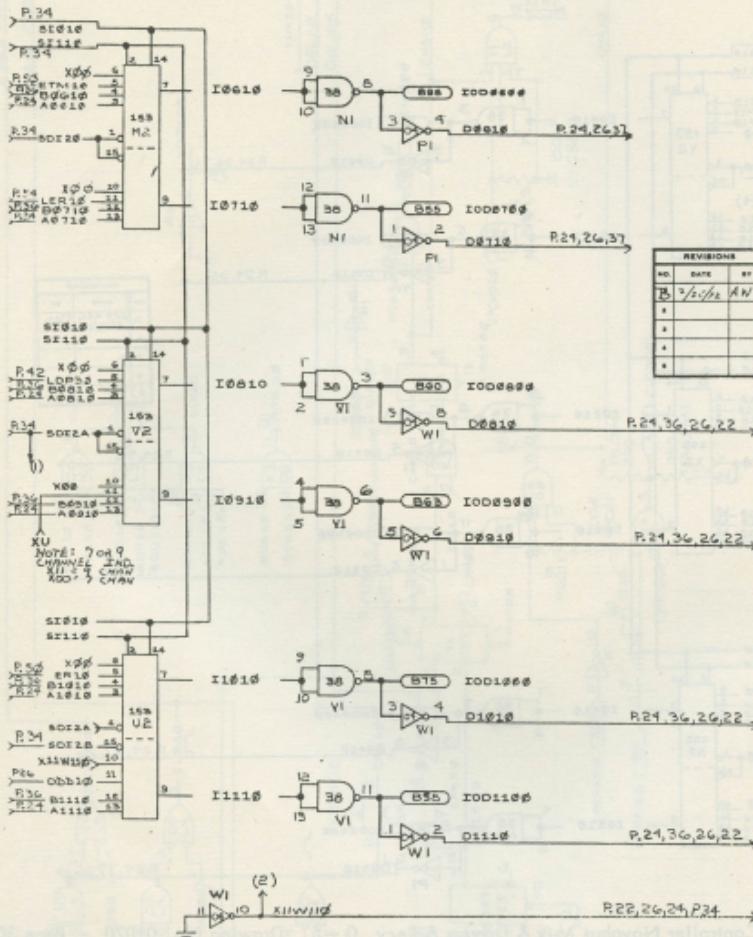
Tape Controller Length Counter & Inc. Logic Drawing No. 01070 - Page 26



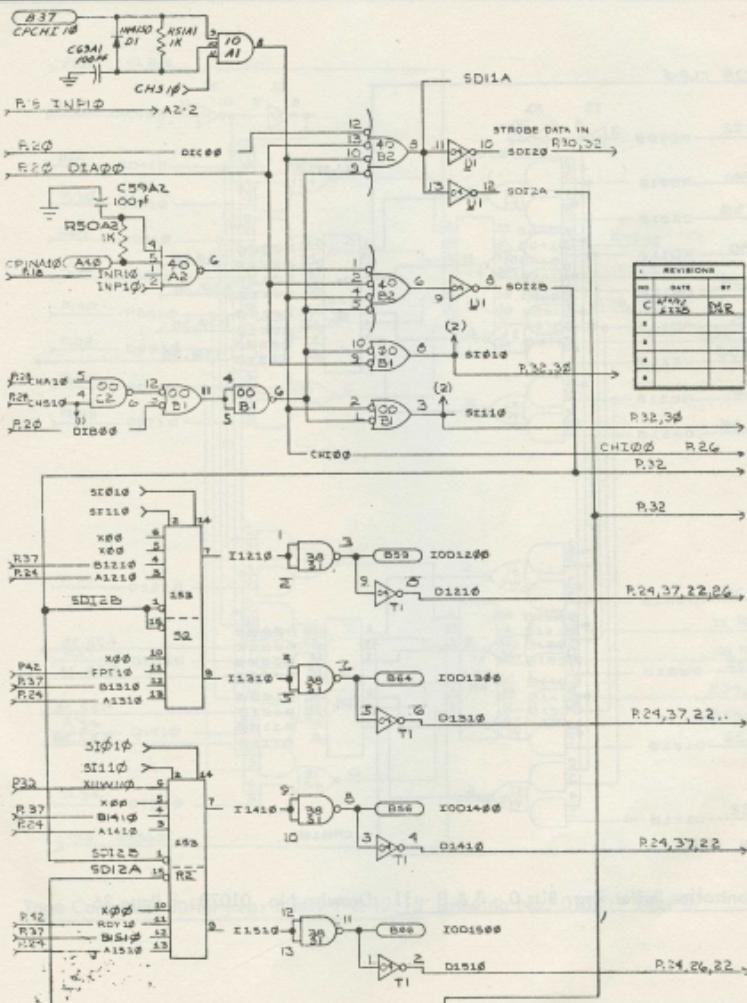
Tape Controller DATA Channel Controls Drawing No. 01070 - Page 28

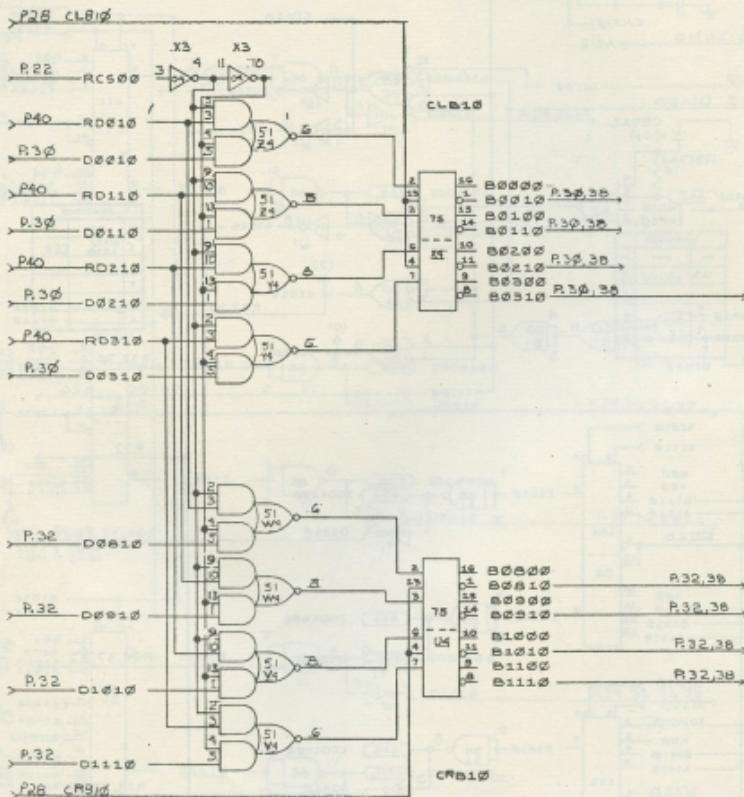


Tape Controller Novabus Mux & Drivers & Recv, 0 - 5 Drawing No. 01070 - Page 30

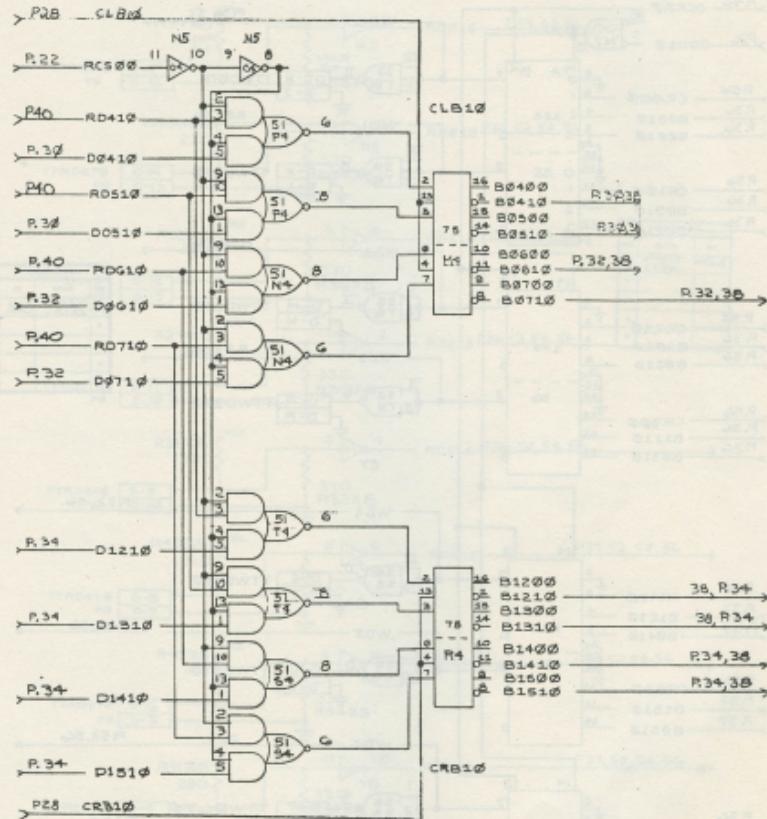


REVISIONS		
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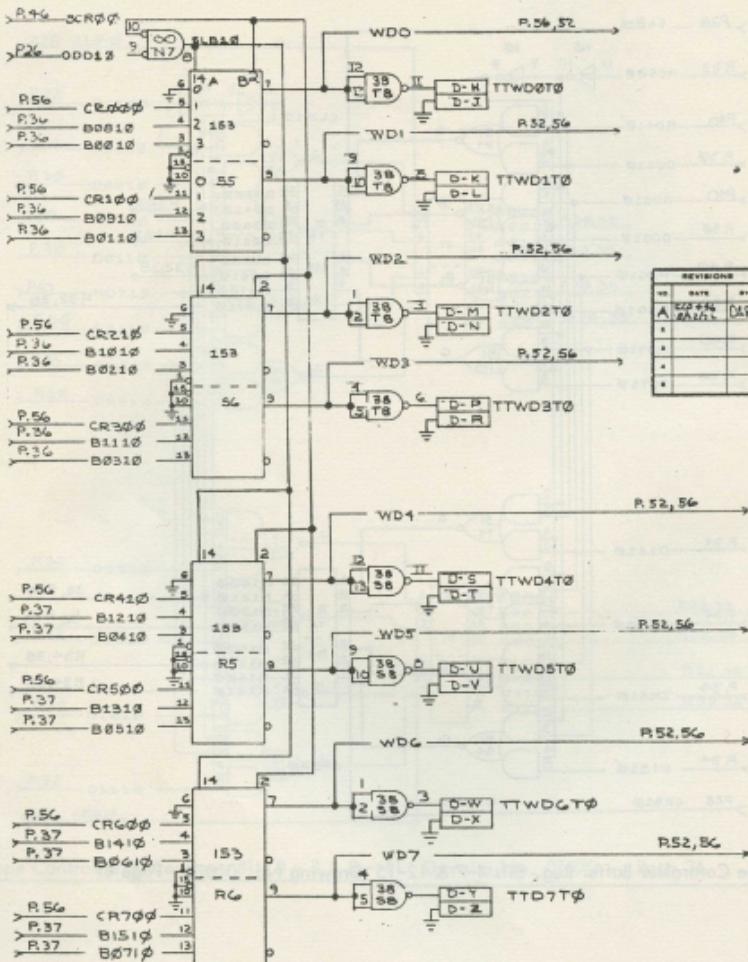
Tape Controller Buffer Reg. Bits 0 - 3 &amp; 8 - 11 Drawing No. 01070 - Page 36



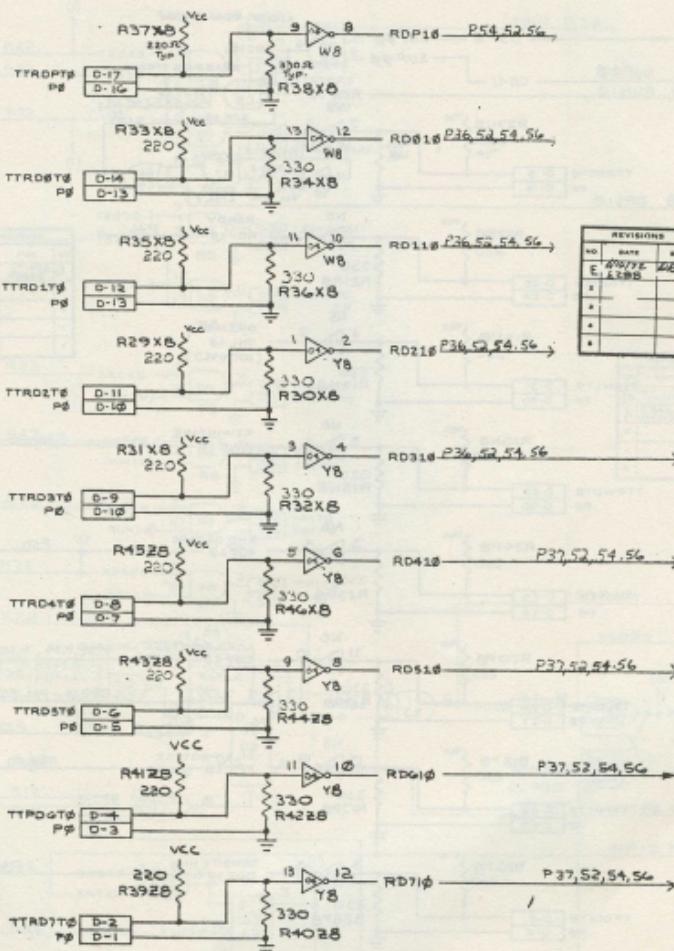
Tape Controller Buffer Reg. Bits 4-7 & 12-15 Drawing No. 01070 - Page 37

Tape Controller Transport Register Drawing No. 01070 - Page 40

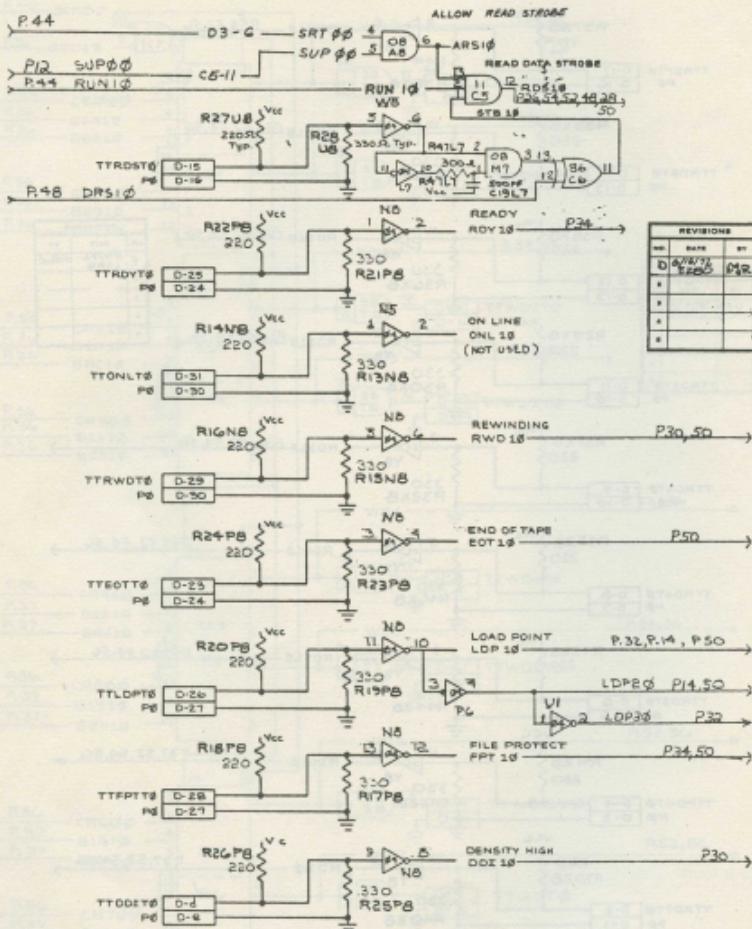
Tape Controller Transport Register Drawing No. 01070 - Page 38



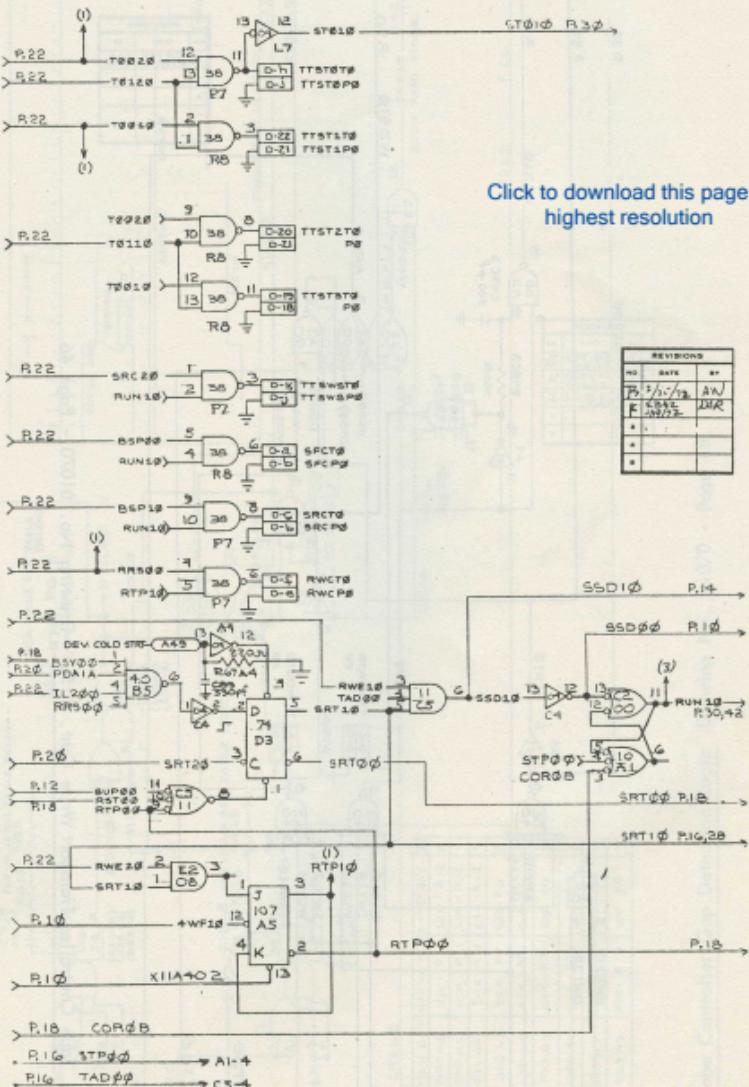
Tape Controller Transport Write Mux &amp; Drivers Drawing No. 01070 - Page 38



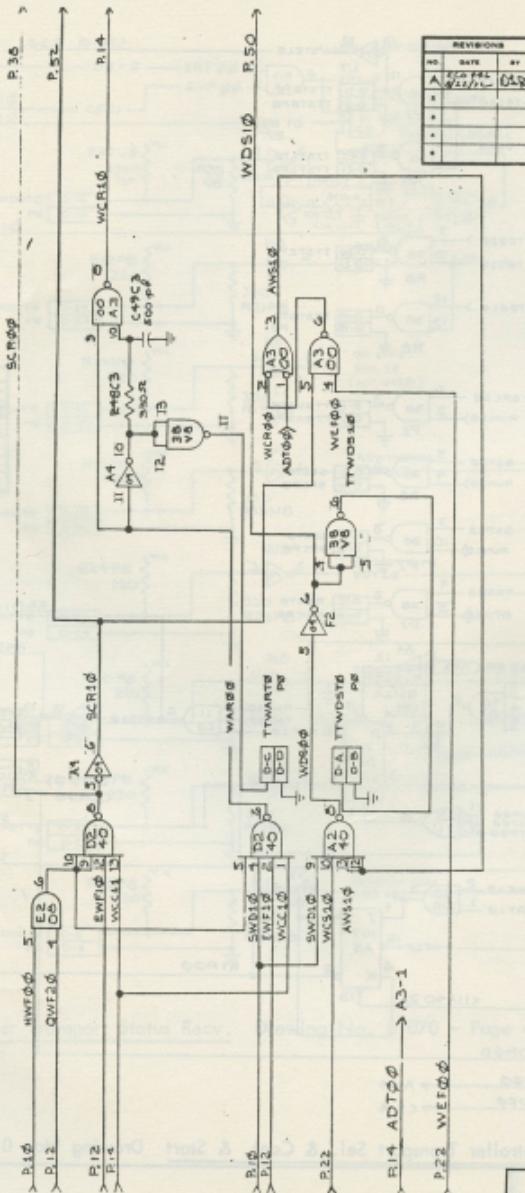
Tape Controller Transport Read Data & Recv. Drawing No. 01070 - Page 40



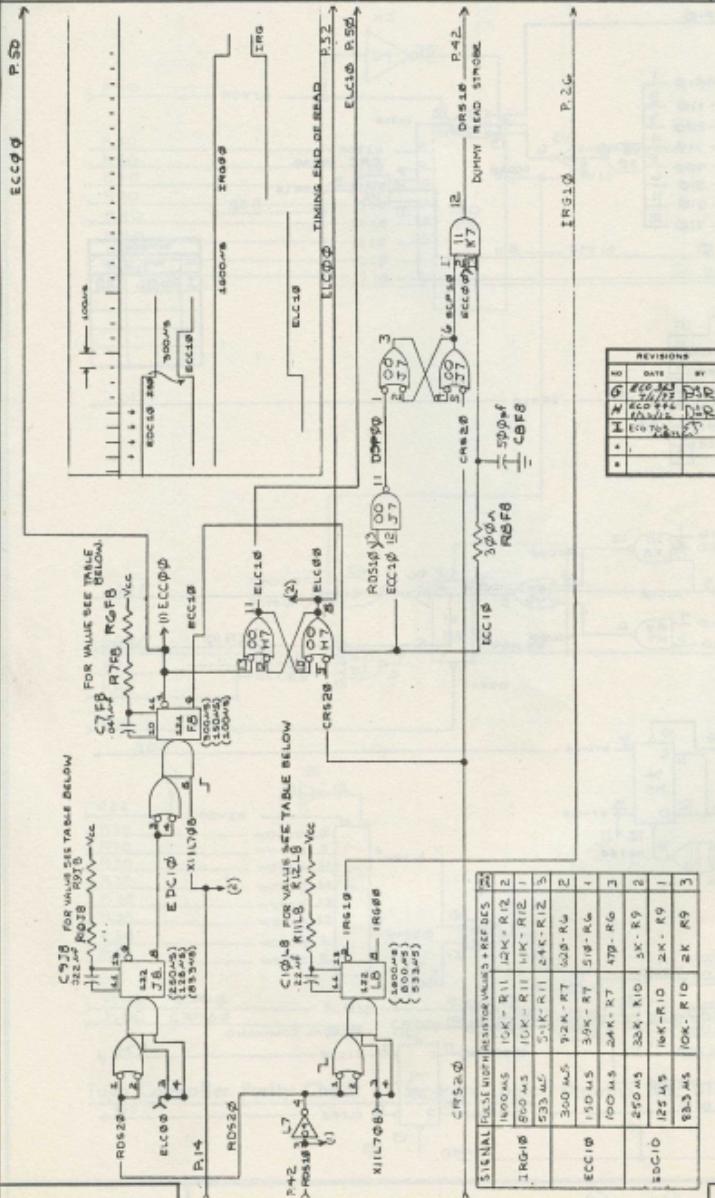
Tape Controller Transport Status Recv. Drawing No. 01070 - Page 42



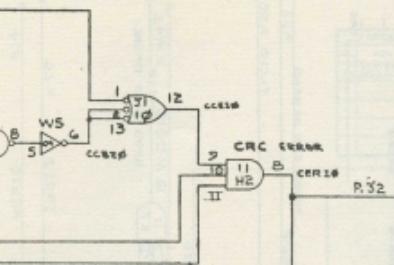
Tape Controller Transport Sel. & Cont. & Start Drawing No. 01070 - Page 44



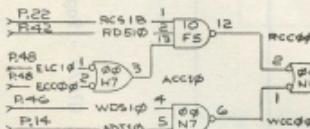
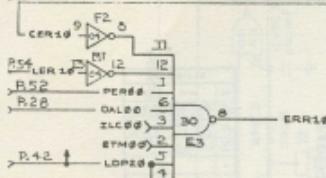
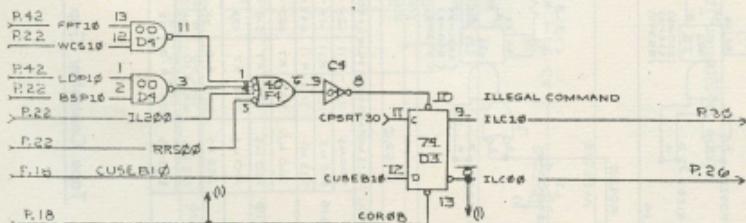
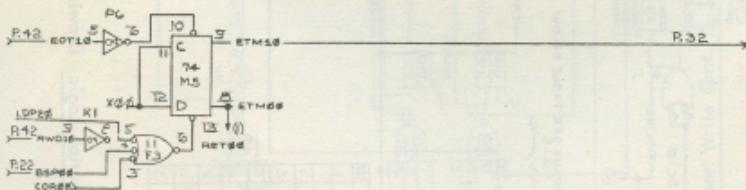
Tape Controller Transport Write Control &amp; Sel Drawing No. 01070 - Page 46

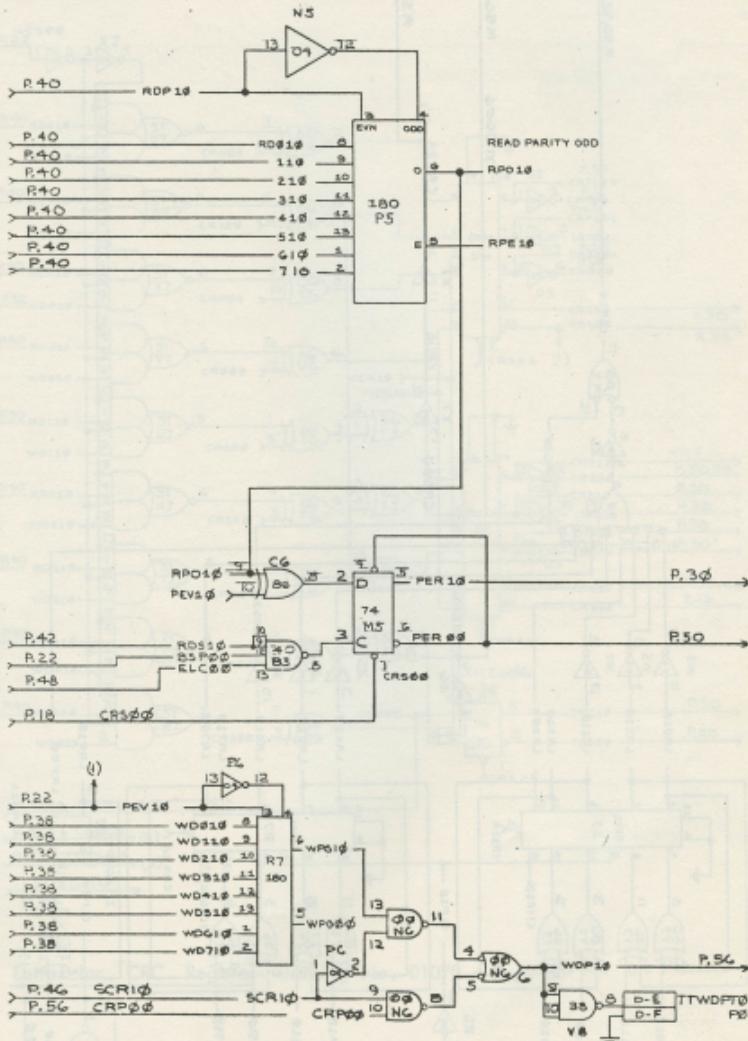


Tape Controller Gap Detection Logic Drawing No. 01070 - Page 48

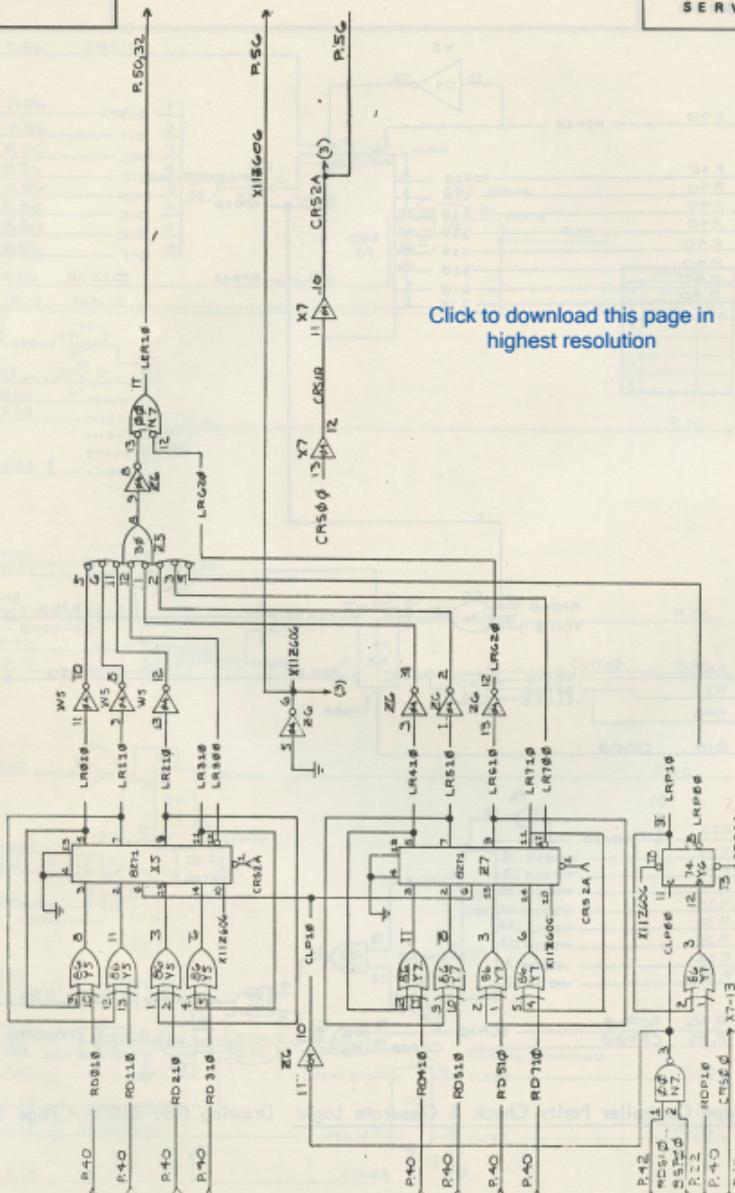
> P.5G CRPI $\phi$ 
 > P.5G CRPI $\phi$  1  
 > P.5G 11 $\phi$  2  
 > P.5G 20 $\phi$  3  
 > P.5G 31 $\phi$  4  
 > P.5G 40 $\phi$  5 39  
 > P.5G 41 $\phi$  6  
 > P.5G 51 $\phi$  7 11  
 > P.5G 61 $\phi$  8  
 > P.5G 71 $\phi$  9


REVISIONS		
NO	DATE	BY
I	80056	PDR
II		
III		
IV		
V		

> P.1G RCI $\phi$ P.1B DON1 $\phi$ CCR1 $\phi$  P.5G



Tape Controller Parity Check & Generate Logic Drawing No. 01070 - Page 52



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