

# 1. Description

## 1.1. Project

Project Name	1404-Reveil_Matrice_STM
Board Name	NUCLEO-F042K6
Generated with:	STM32CubeMX 6.8.1
Date	11/15/2023

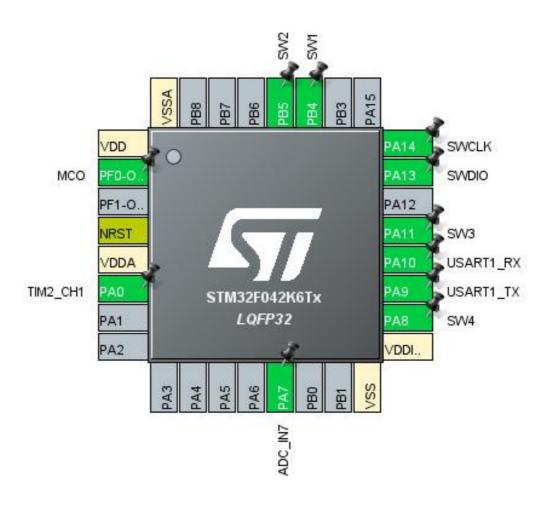
## 1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x2
MCU name	STM32F042K6Tx
MCU Package	LQFP32
MCU Pin number	32

## 1.3. Core(s) information

Core(s)	Arm Cortex-M0

# 2. Pinout Configuration

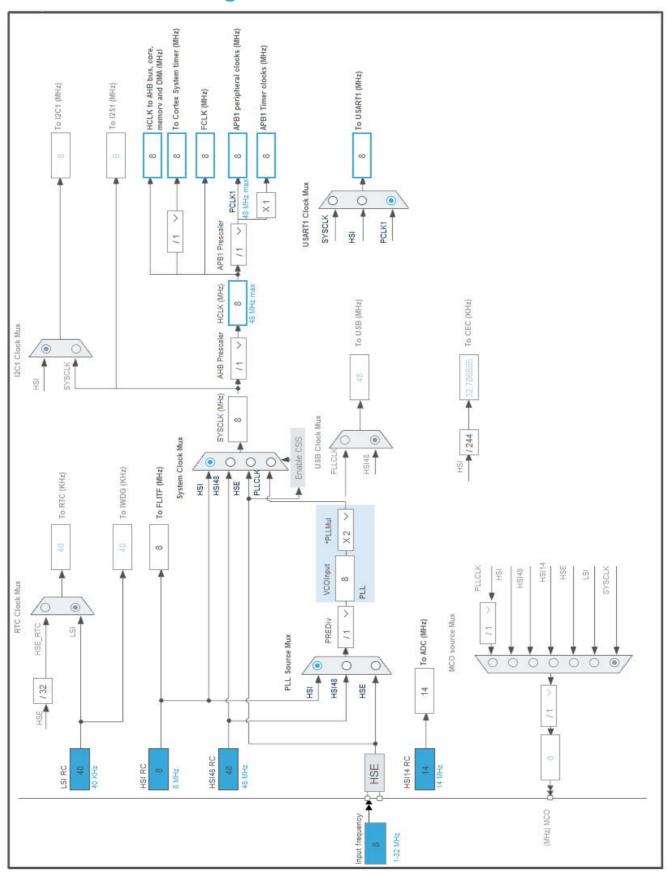


# 3. Pins Configuration

Pin Number LQFP32	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VDD	Power		
2	PF0-OSC_IN	I/O	RCC_OSC_IN	MCO
4	NRST	Reset		
5	VDDA	Power		
6	PA0	I/O	TIM2_CH1	
13	PA7	I/O	ADC_IN7	
16	VSS	Power		
17	VDDIO2	Power		
18	PA8 *	I/O	GPIO_Input	SW4
19	PA9	I/O	USART1_TX	
20	PA10	I/O	USART1_RX	
21	PA11 *	I/O	GPIO_Input	SW3
23	PA13	I/O	SYS_SWDIO	SWDIO
24	PA14	I/O	SYS_SWCLK	SWCLK
27	PB4 *	I/O	GPIO_Input	SW1
28	PB5 *	I/O	GPIO_Input	SW2
32	VSSA	Power		

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



# 5. Software Project

## 5.1. Project Settings

Name	Value	
Project Name	1404-Reveil_Matrice_STM	
Project Folder	C:\Users\alyvasseur\Education Vaud\ETML_FLO-22-24_Teams -	
Toolchain / IDE	STM32CubeIDE	
Firmware Package Name and Version	STM32Cube FW_F0 V1.11.4	
Application Structure	Advanced	
Generate Under Root	Yes	
Do not generate the main()	No	
Minimum Heap Size	0x200	
Minimum Stack Size	0x400	

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

## 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_DMA_Init	DMA
4	MX_TIM2_Init	TIM2
5	MX_USART1_UART_Init	USART1
6	MX_ADC_Init	ADC

# 6. Power Consumption Calculator report

#### 6.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x2
MCU	STM32F042K6Tx
Datasheet	DS10147_Rev5

#### 6.2. Parameter Selection

Temperature	25
Vdd	3.6

## 6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

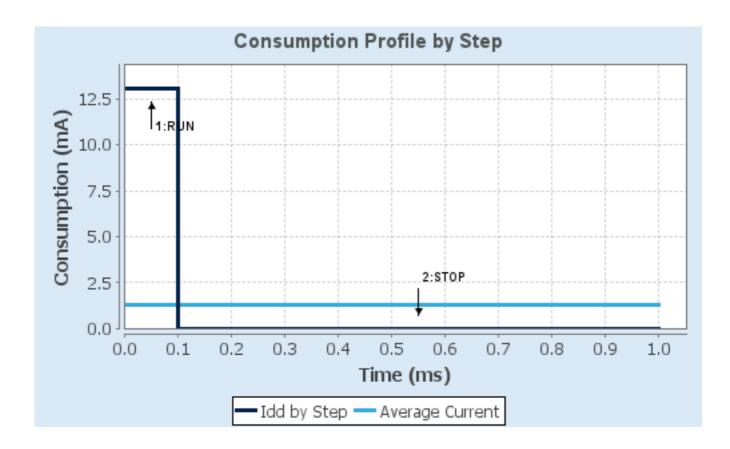
## 6.4. Sequence

	T	
Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.6	3.6
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	48 MHz	0 Hz
Clock Configuration	HSI PLL	Regulator LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	13.04 mA	6.2 µA
Duration	0.1 ms	0.9 ms
DMIPS	0.0	0.0
Ta Max	102.32	105
Category	In DS Table	In DS Table

#### 6.5. Results

Sequence Time	1 ms	Average Current	1.31 mA
Battery Life	3 months, 16	Average DMIPS	0.0 DMIPS
	days, 14 hours		

#### 6.6. Chart



# 7. Peripherals and Middlewares Configuration

# 7.1. ADC mode: IN7

#### 7.1.1. Parameter Settings:

#### ADC\_Settings:

Clock Prescaler Asynchronous clock mode

Resolution ADC 8-bit resolution \*

Data Alignment Right alignment

Scan Conversion Mode Forward
Continuous Conversion Mode Disabled
Discontinuous Conversion Mode Disabled
DMA Continuous Requests Disabled

End Of Conversion Selection End of single conversion

Overrun behaviour Overrun data preserved

Low Power Auto Wait Disabled
Low Power Auto Power Off Disabled

#### ADC\_Regular\_ConversionMode:

Sampling Time 1.5 Cycles

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None

WatchDog:

Enable Analog WatchDog Mode false
Low Threshold 0

#### 7.2. RCC

#### High Speed Clock (HSE): BYPASS Clock Source

#### 7.2.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16
HSI14 Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

#### 7.3. SYS

mode: Debug Serial Wire

7.4. TIM2

Clock Source: Internal Clock
Channel1: PWM Generation CH1

7.4.1. Parameter Settings:

#### **Counter Settings:**

Prescaler (PSC - 16 bits value) 0

Counter Mode Up

Counter Period (AutoReload Register - 32 bits value) 10-1 \*

Internal Clock Division (CKD) No Division auto-reload preload Disable

**Trigger Output (TRGO) Parameters:** 

Master/Slave Mode (MSM bit)

Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

**PWM Generation Channel 1:** 

Mode PWM mode 1

Pulse (32 bits value) 0

Output compare preload Enable

Fast Mode Disable

CH Polarity High

#### 7.5. USART1

**Mode: Asynchronous** 

#### 7.5.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 250000 \*

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples
Single Sample Disable

**Advanced Features:** 

Auto Baudrate Disable TX Pin Active Level Inversion Disable Disable **RX Pin Active Level Inversion** Data Inversion Disable TX and RX Pins Swapping Disable Enable Overrun DMA on RX Error Enable MSB First Disable

<sup>\*</sup> User modified value

# 8. System Configuration

# 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC	PA7	ADC_IN7	Analog mode	No pull-up and no pull-down	n/a	
RCC	PF0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	MCO
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	SWDIO
	PA14	SYS_SWCLK	n/a	n/a	n/a	SWCLK
TIM2	PA0	TIM2_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
GPIO	PA8	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	SW4
	PA11	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	SW3
	PB4	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	SW1
	PB5	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	SW2

#### 8.2. DMA configuration

DMA request	Stream	Direction	Priority
TIM2_CH1	DMA1_Channel5	Memory To Peripheral	High *
USART1_TX	DMA1_Channel2	Memory To Peripheral	Very High *

#### TIM2\_CH1: DMA1\_Channel5 DMA request Settings:

Mode: Circular \*
Peripheral Increment: Disable
Memory Increment: Enable \*
Peripheral Data Width: Word
Memory Data Width: Byte \*

#### USART1\_TX: DMA1\_Channel2 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: Enable \*
Peripheral Data Width: Byte

Peripheral Data Width: Byte Memory Data Width: Byte

## 8.3. NVIC configuration

## 8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
System service call via SWI instruction	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	0	0	
DMA1 channel 2 and 3 interrupts	true	0	0	
DMA1 channel 4 and 5 interrupts	true	0	0	
USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25	true	0	0	
PVD and VDDIO2 supply comparator interrupts through EXTI lines 16 and 31	unused			
Flash global interrupt	unused			
RCC and CRS global interrupts	unused			
ADC interrupt	unused			
TIM2 global interrupt	unused			

## 8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
DMA1 channel 2 and 3 interrupts	false	true	true
DMA1 channel 4 and 5 interrupts	false	true	true
USART1 global interrupt / USART1 wake- up interrupt through EXTI line 25	false	true	true

#### \* User modified value

# 9. System Views

9.1. Category view

9.1.1. Current

#### 10. Docs & Resources

Type Link

IBIS models https://www.st.com/resource/en/ibis\_model/stm32f0\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f0\_svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/gt\_stm32f0-l0.pdf

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