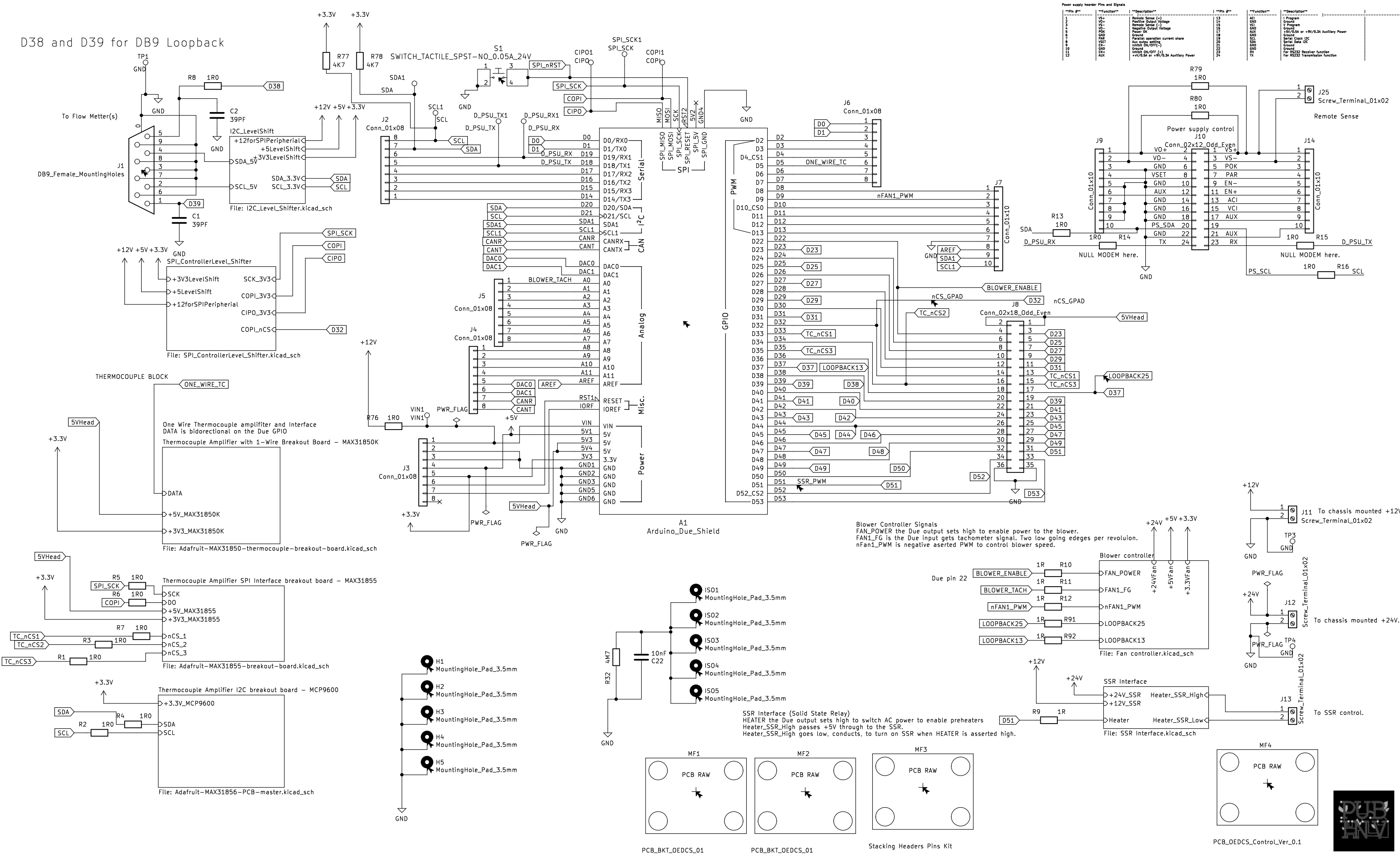


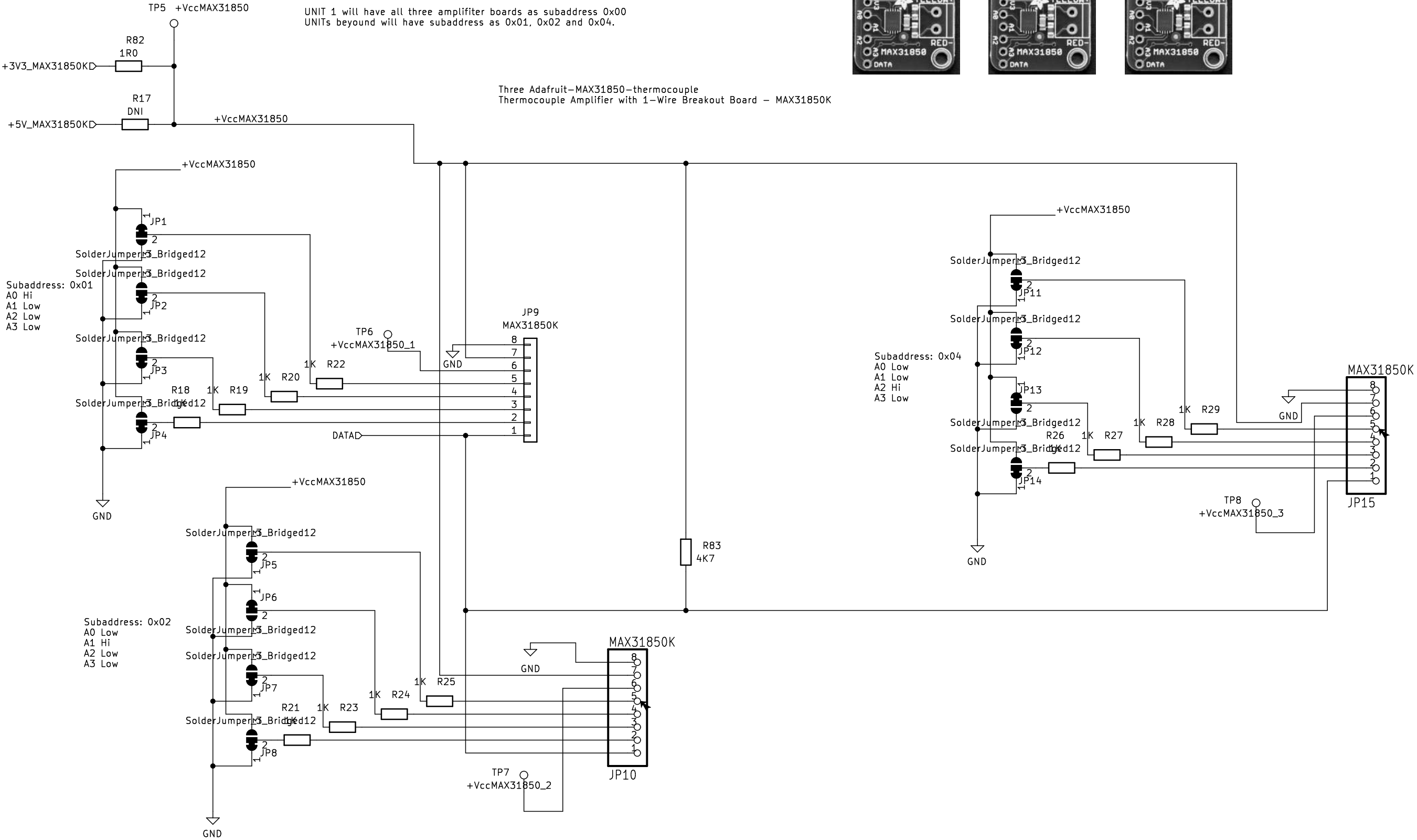
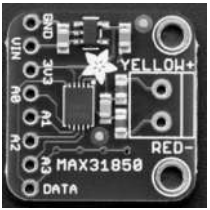
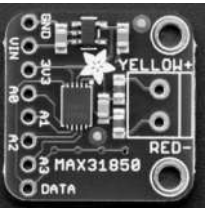
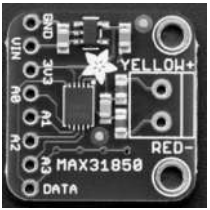
D38 and D39 for DB9 Loopback

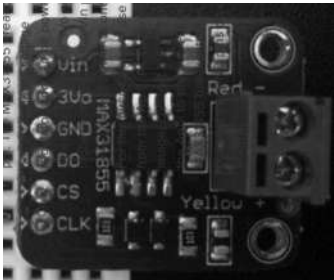


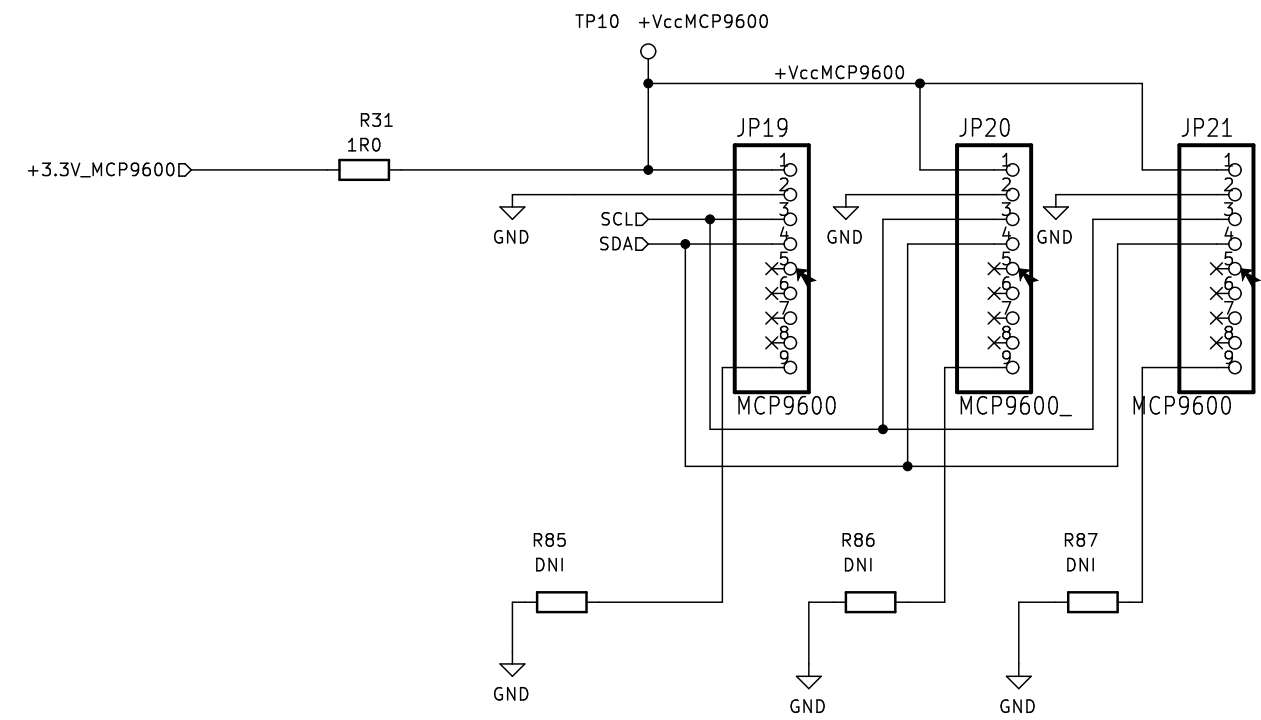
Each board will be addressed by parts on our main board.
The daughter boards will need the addressing traces CUT.

UNIT 1 will have all three amplifier boards as subaddress 0x00
UNITs beyound will have subaddress as 0x01, 0x02 and 0x04.

Three Adafruit-MAX31850-thermocouple
Thermocouple Amplifier with 1-Wire Breakout Board – MAX31850K



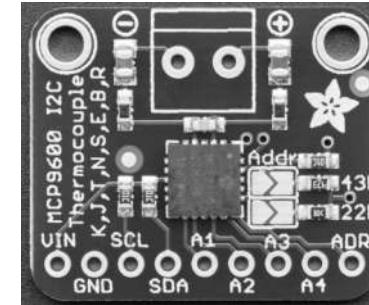




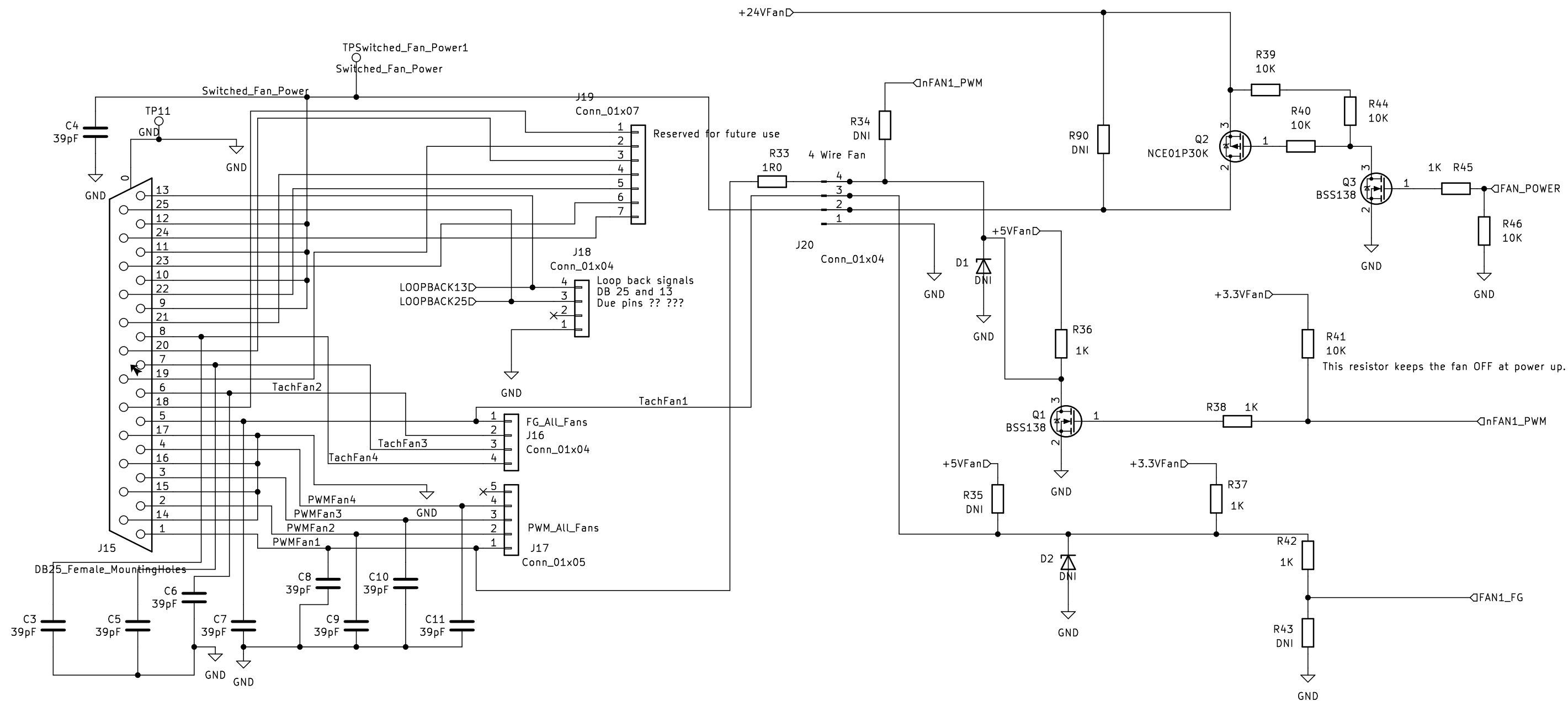
TO DO:
Solder on the modules directly to set subaddress.
Default is 0x67.

MCP9600 Pin Descriptions

- 1 Vin
- 2 GND
- 3 SCL
- 4 SDA
- 5 Alert 1
- 6 Alert 2
- 7 Alert 3
- 8 Alert 4
- 9 ADR

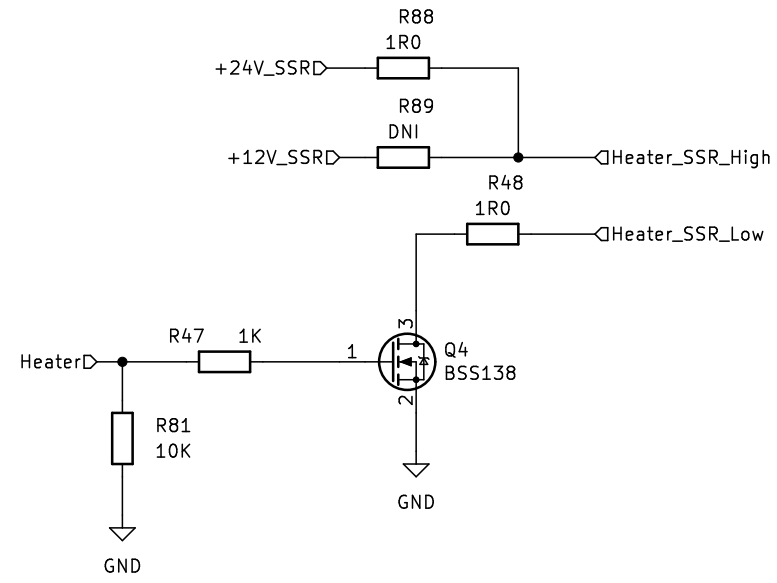


V (EN) and GND



Unit 1 Wring Notes:

Fan Breakout	Signal-Function	DB25 MALE Pin #	DB25 FEMALE Pin #	Other	Due Pin	Due Signal
1	+24V Switched (fans)	2-5	9-12		???	FAN_POWER
2	GND for all fans	22-25	14-17		GND	NA
3	#1 PWM	13	1	J10-1	D9	nFAN1_PWM
4	#1 Tach	9	5	J9-1	A0	FAN1_FG
5	#2 PWM	12	2	J10-2		nFAN2_PWM
6	#2 Tach	8	6	J9-2		FAN2_FG
7	#3 Tach	11	3	J10-3		nFAN3_PWM
8	#3 PWM	7	7	J9-3		FAN3_FG
9	#4 Tach	10	4	J10-4		nFAN4_PWM
10	#4 PWM	6	8	J9-4		FAN4_FG
			25	J11-3		
			13	J11-4		
			18	J12-1		
			19	J12-2		
			20	J12-3		
			21	J12-4		
			22	J12-5		
			23	J12-6		
			24	J12-7		



An interface card for the COG
to bring out SPI bus for the GPAD.
Level shift from 5V to 3V on the CIPO.

An interface card for the COG
to bring out SPI bus for the GPAD.
Level shift from 5V to 3V on the CIP0.

An RJ12 Connector for SPI with 5V logic level.

SPI Connector Signal Names
CIP0: Controller In, Peripheral Out
C_SCK: Controller Serial Clock (output)
COP1: Controler Out, Peripheral In
nCS: not Chip Select, ie /CHIP_SELECT

The schematic illustrates the SPI interface circuit. Key components include:

- Power Supply:** +5VLocal and +3V3Local rails with decoupling capacitors C16 and C17.
- Logic Level Shifting:** Transistors Q5, Q6, Q7, and Q8 (BSS138) used for level shifting between 5V and 3.3V logic levels.
- Resistors:** Various resistors (R49-R65) are used for pull-up/pull-down and current limiting.
- Capacitors:** Capacitors C12, C13, C14, C15, C16, and C17 are used for timing and decoupling.
- Test Points:** TP12 (ControllerVCC), TP13 (CIP0), TP14 (SCK), TP15 (COP1), and TP16 (nCS).



Bring out I2C bus through bidirectional level shifters.

