ELC 2137 Lab 2: Transistors

Forrest Knee

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Summary

This lab's focus was making different physical logic gates using transistors. The gates build were an OR, AND, NOT, and NOR gate.

Q&A

1. What is the Logic/truth table for the final gate?

A	B	OUTPUT
0	0	0
0	1	1
1	0	1
1	1	1

2. What logic operation does it implement?

   AND

Results

Code