ELC 2137 Lab 2: Transistors

Forrest Knee

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Summary

This lab's focus was making different physical logic gates using transistors. The gates built were an OR, AND, NOT, and NOR gate.

Q&A

1. What is the Logic/truth table for the final gate?

A	B	OUTPUT
0	0	0
0	1	0
1	0	0
1	1	1

2. What logic operation does it implement?

   AND

Results

ELC 2137 Lab 2. Transistor Logic Gates

Circuit Demonstration Page

Student names: Forrest Knee _____

_____

Instructor Initials

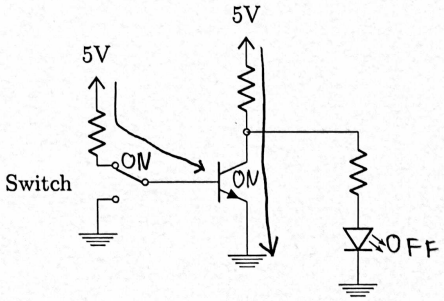
Pushbutton "Or Gate" _____ Transistor Not gate _____

Transistor Nor gate _____ Transistor unknown gate _____

Diagrams

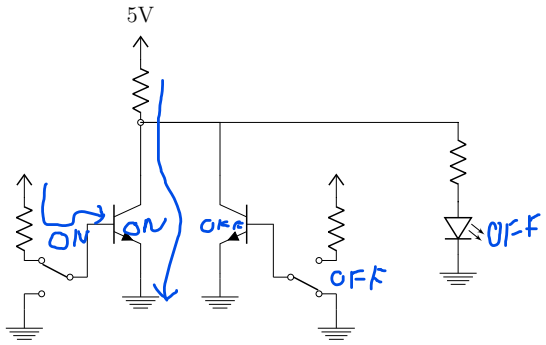
On each of the circuits below, draw the current paths and note whether each switch, transistor, and LED is ON or OFF.

Inverter:

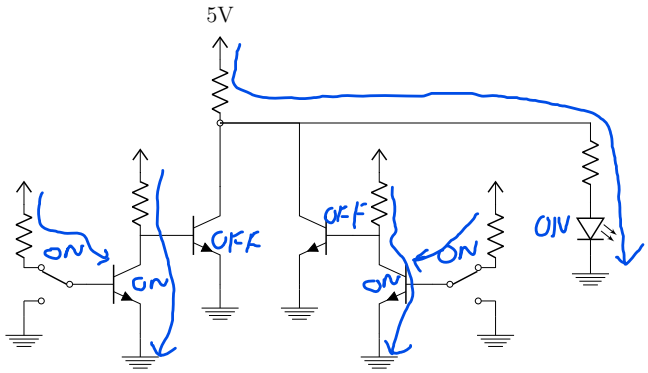


8

NOR:



Final gate:



Code

none