OSA-RTS END-TO-END DEMONSTRATION OSA-RTS DEMONSTRATION



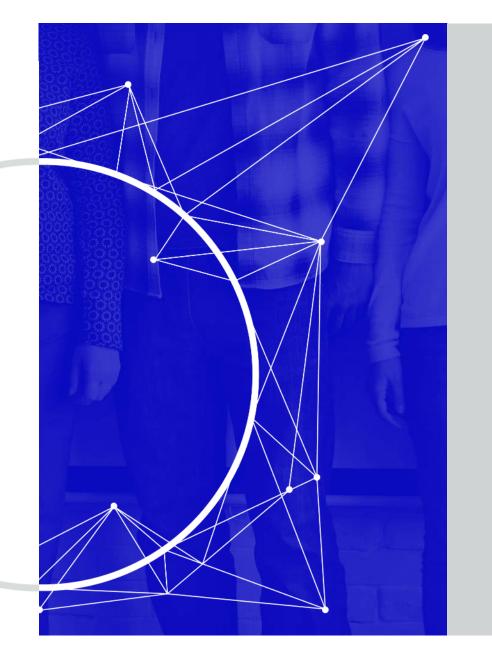
OSA-RTS Introduction

ATML UUT Description

ATML Test Description

Osa-rts Demonstration

Osa-rts Demonstration—Results





OSA-RTS INTRODUCTION



- The Open System Architecture Runtime System (OSA-RTS) provides a common framework of shared ATS components that implement ATML solutions for translating Test Descriptions and Test Equipment Descriptions into "run-able" test programs.
- The current OSA-RTS framework has been designed for the C, C++ and C# textual programming languages and extended to the graphical programming environments:
 - LabWindows / CVI
 - LabVIEW
 - TPL (using project selected carrier language)

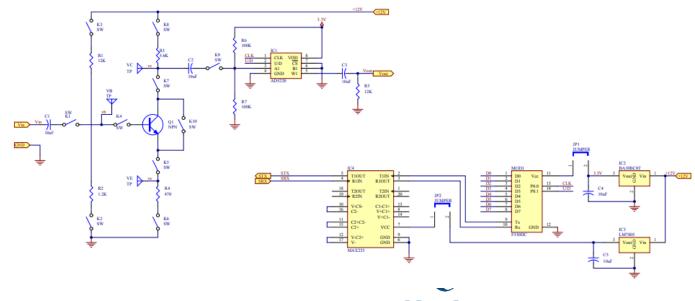


ATML UUT DESCRIPTION



ATML TEST DESCRIPTION

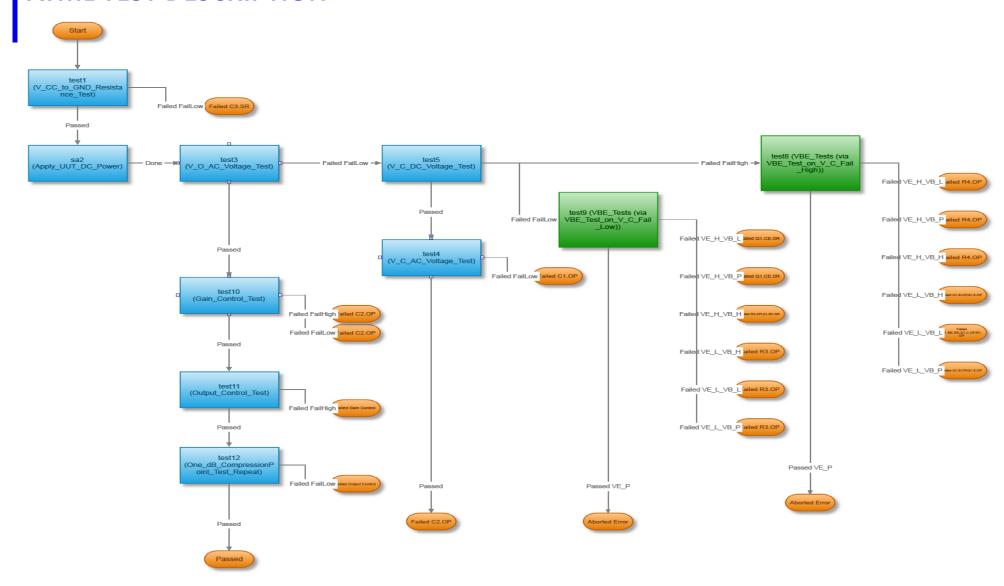




ATML TEST DESCRIPTION



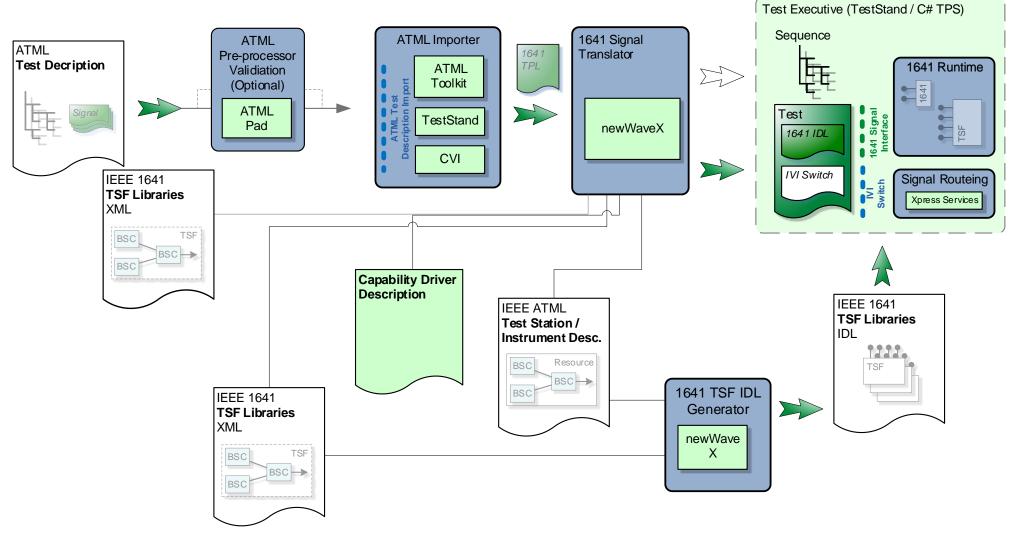
ATML TEST DESCRIPTION







COMPONENTS AND FLOW - LABWINDOWS / CVI ENVIRONMENTS





OSA-RTS DEMONSTRATION COMPONENT – ATML PAD

ATML
Pre-processor
Validiation
(Optional)

ATML
Pad

IEEE 1641
TSF Libraries
XML

BSC
BSC
BSC

BSC

BSC

BSC

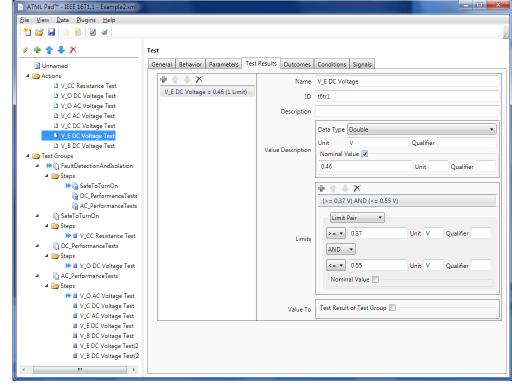
BSC

ATML
Pre-processor
Validiation
(Optional)

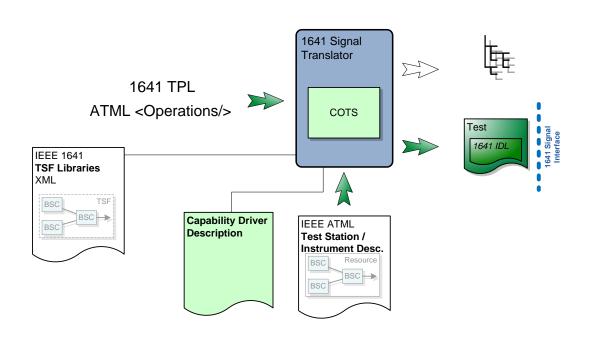
ATML
Pad

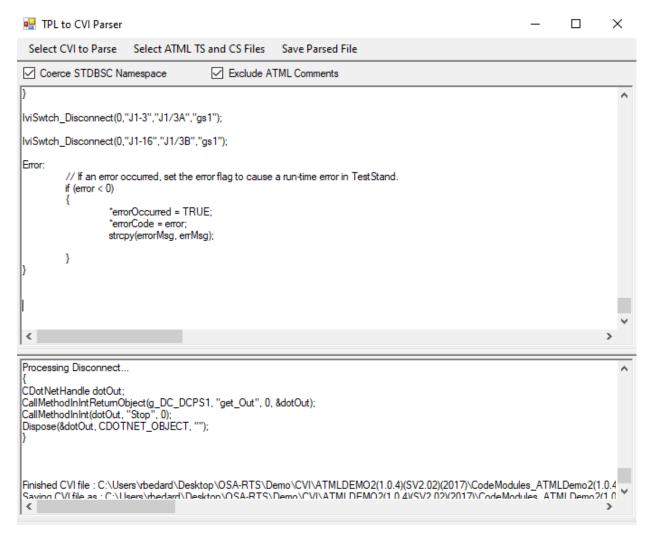
- Visual editor for ATML
- Comprehensive ATML data validation
- Integrated with TestStand ATML Translator
- Plug-in importer architecture
 - Conversion to standard ATML format





COMPONENT – 1641 SIGNAL TRANSLATOR







OSA-RTS TEST STATION RESOURCES COMPONENT – 1641 SIGNAL TRANSLATOR

Test Station Description

- 1.The Test Station Description is an XML file with associated schema (IEEE 1671.6) , that is used to describe the Test Resources and their Capabilities for an ATE.
- 2. The OSA-RTS uses the Test Station Description to assign/allocate test resource capabilities to test signals ATML operations.

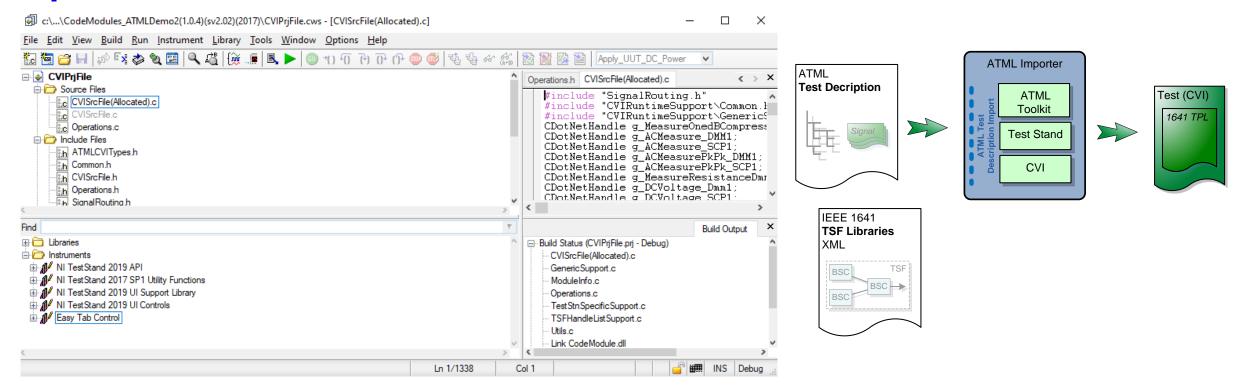
Capability Driver Description

- 1. The Capability Driver Description is an XML file with associated schema, that is used to generate Template Driver Code calls to the runtime system.
- 2.In the OSA-RTS ATML example, the Template Driver Code is targeted specifically to LabWindows/CVI, but is also utilised by the TPL demonstration.
- 3. The template code is inserted into the CVISrcFile.c by the 1641 Signal Translator.
- 4. The Capability Driver Description file is currently generated manually.



¹⁴ I OSA-RTS DEMONSTRATION

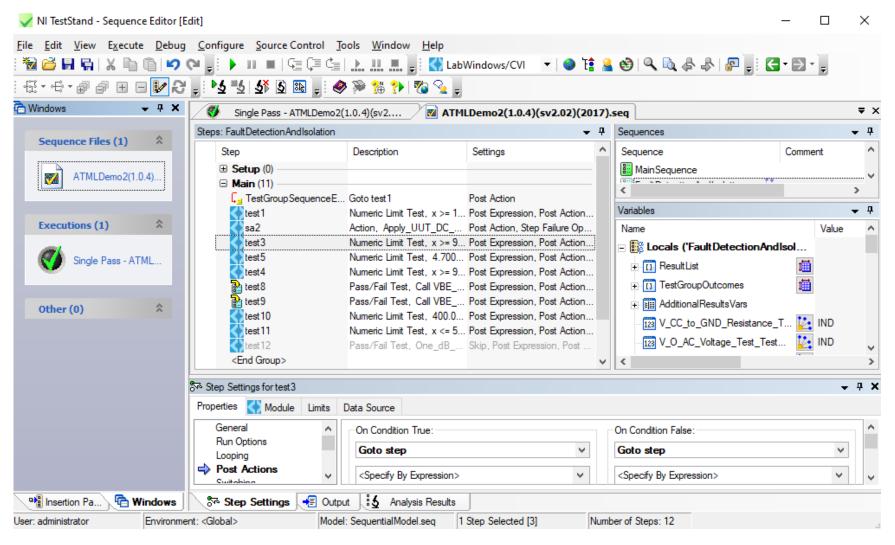
COMPONENT - ATML IMPORTER (LABWINDOWS/CVI)



- Proven ANSI C development environment for test and measurement for more than 20 years.
- Provides C carrier language and access to driver code
- Hardware configuration assistants, built-in measurement libraries, comprehensive debugging tools, interactive execution capabilities used during design along with advanced analysis and scientific user interface tools.
- Used for high performance applications in military, aerospace, telecommunications and automotive industries.

SPHEREA

ATML SEQUENCE EDITOR

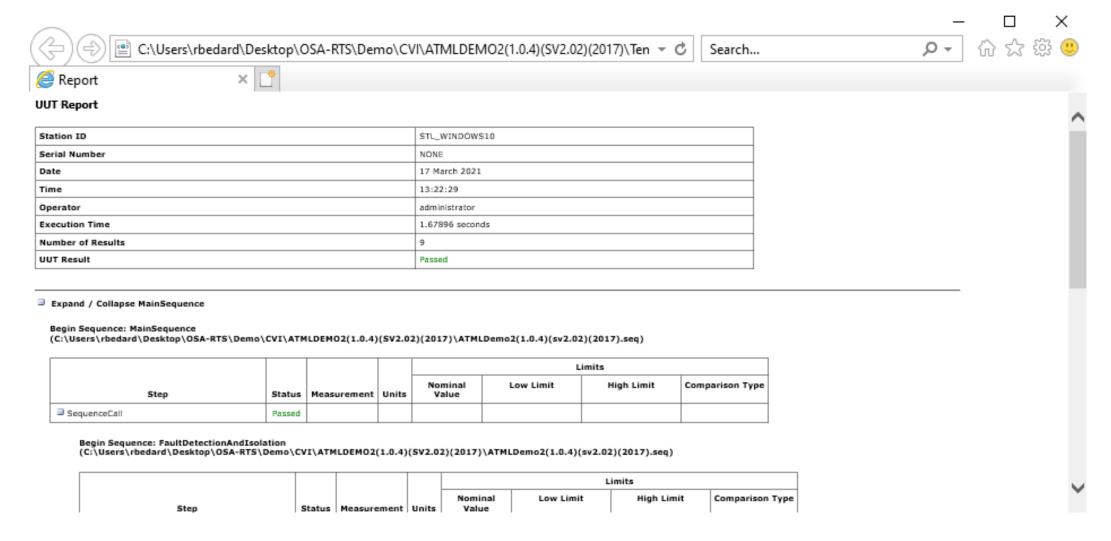




OSA-RTS DEMONSTRATION— RESULTS



OSA-RTS DEMONSTRATION— RESULTS





COTS SOFTWARE

newWaveX[®] IEEE 1641[™] Signal-based T&M software tools:

newWaveX SD (Signal Development)

- Complete graphical signal modelling & simulation environment.
- Compliant with IEEE 1641 and IEEE 1671 standards.
- Supports the creation and editing of IEEE 1641 Signals & TSF libraries;
 XML, XSD, IDL and HTML file formats for storage, interface specification and documentation.
- ActiveX controls enable easy embedding into third-party applications.

newWaveX PD (Platform Development)

- Test platform integration toolset, targeted at getting ATML test signals to real test pins.
- IEEE Std.1671 ATML Instrument and Test Station Description resource (instrument) description and validation environment.
- Compile-time resource manager/translator using IEEE 1641 IDL and TPL signal orientated test program descriptions to generate IEEE 1671 ATML Test Description. driver orientated test code.

