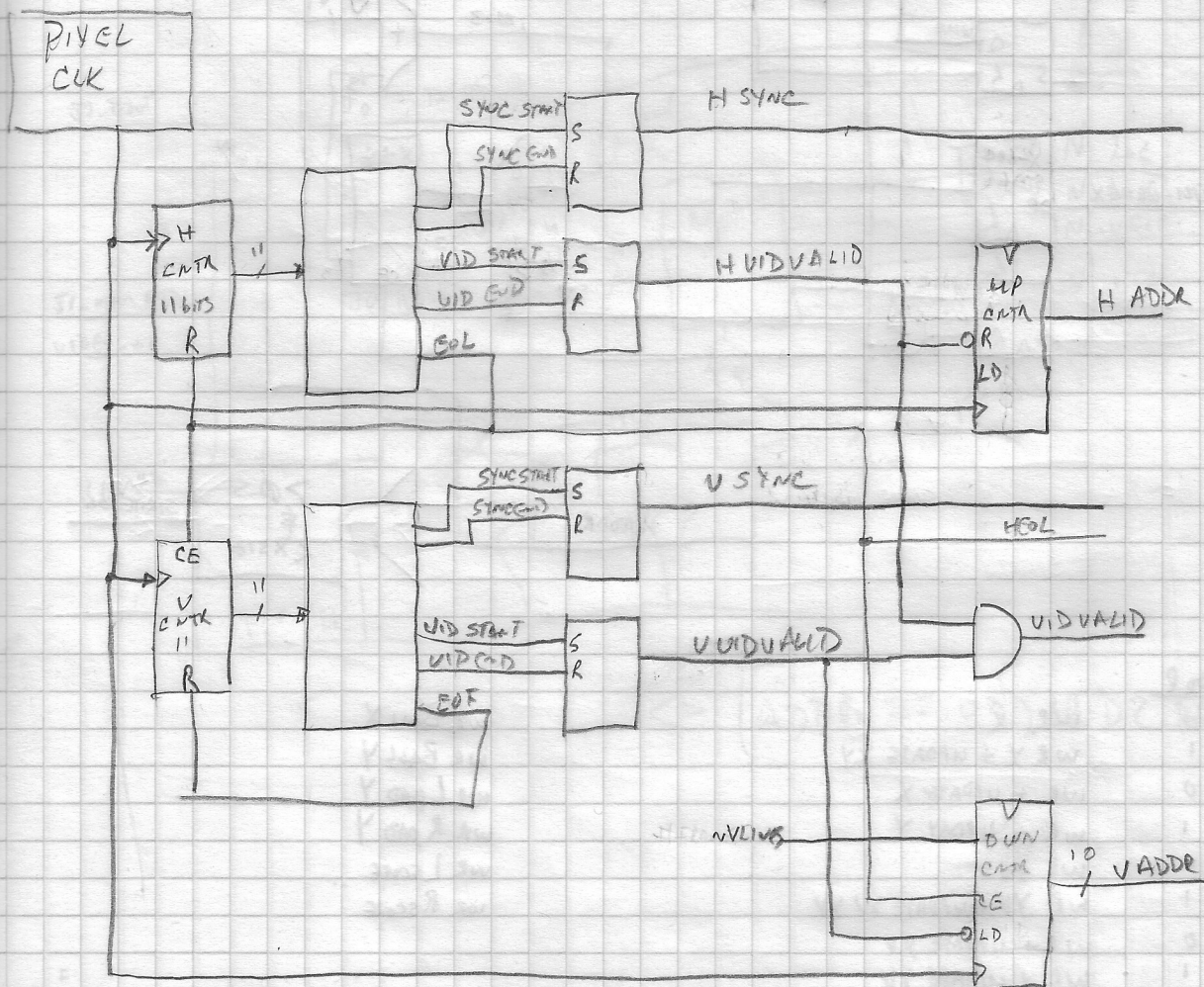


DONG VGA DESIGN

SYNC GEN



H PARAMETERS

HADDR_WIDTH
 HCNTR_WIDTH
 H_SYNCSTART
 H_SYNCEND
 H_VIDSTART
 H_VIDEND = H_VIDSTART + NHLINES
 H_END

$$NHLINES = 1024$$

V PARAMETERS

VADDR_WIDTH
 VCNTR_WIDTH
 V_SYNCSTART
 V_SYNCEND
 V_VIDSTART
 V_VIDEND = V_VIDSTART + NVLINES

$$NVLINES = 768$$