

10420 CS410001 – Computer Architecture 2016

Appendix C-3 - Sample Output for Project 3

Please follow the format guide for generating your output reports, **report.rpt**. For **snapshot.rpt**, please refer to *project_1.doc* and *Appendix C-1*, “*Sample Output for Project 1.*” **You must precisely follow the specified format, or your result will be treated as incorrect!**

You should list the **total hit/miss number** of ICache, DCache, ITLB, DTLB, IPageTable, DPageTable

Example: Test case

```
addi $4 $0 4
addi $8 $0 12
beq $8 $1 4
lw $2 0($1)
sw $2 12($1)
add $1 $1 $4
j 2
add $8 $8 $8
add $1 $0 $0
beq $8 $1 4
lw $3 0($1)
add $1 $1 $4
j 9
halt
halt
halt
halt
halt
```

Sample Output File:

```
ICache :
# hits: 25
# misses: 21

DCache :
# hits: 2
# misses: 10

ITLB :
# hits: 38
# misses: 8

DTLB :
# hits: 10
# misses: 2

IPageTable :
# hits: 0
# misses: 8

DPageTable :
# hits: 0
# misses: 2
```

You may use the following code we recommend to write out the total hit/miss number of each component:

```
fprintf( fptr_report, "ICache :\n");
fprintf( fptr_report, "# hits: %u\n", hits );
fprintf( fptr_report, "# misses: %u\n\n", misses );
```

```
fprintf( fptr_report, "DCache :\n");
fprintf( fptr_report, "# hits: %u\n", hits );
fprintf( fptr_report, "# misses: %u\n\n", misses );
```

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```
fprintf( fptr_report, "ITLB :\n");  
fprintf( fptr_report, "# hits: %u\n", hits );  
fprintf( fptr_report, "# misses: %u\n\n", misses );  
  
fprintf( fptr_report, "DTLB :\n");  
fprintf( fptr_report, "# hits: %u\n", hits );  
fprintf( fptr_report, "# misses: %u\n\n", misses );  
  
fprintf( fptr_report, "IPageTable :\n");  
fprintf( fptr_report, "# hits: %u\n", IPageTable.hitNum );  
fprintf( fptr_report, "# misses: %u\n\n", IPageTable.missNum );  
  
fprintf( fptr_report, "DPageTable :\n");  
fprintf( fptr_report, "# hits: %u\n", DPageTable.hitNum );  
fprintf( fptr_report, "# misses: %u\n\n", DPageTable.missNum );
```