



DWBB Memory – Registers Overview

This section documents the following memory registers found in the DesignWare Building Block IP:

Table 1-1 Memory Registers

IP	Description
DW03_pipe_reg	Pipeline Register
DW03_reg_s_pl	Register with Synchronous Enable Reset
DW03_shftreg	Shift Register
DW04_shad_reg	Shadow and Multibit Register
DW_pl_reg	Pipeline Register

Related Topics

For a listing of Building Block components and associated datasheets, see:

- [DesignWare Building Block IP User Guide](#)

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