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# Shen Fu

### Education

2020 – 2024 **B.E. in Computer Science and Technology**, University of Science and Technology of (expected) China, China

- O School of the Gifted Young
- O Major GPA: 4.05/4.30, Cumulative GPA: 3.89/4.30, Rank: 24/262
- O Relevant coursework:
  - Data Structure
  - Operating System
  - Computer Organization
  - Algorithm
  - Computer Network
  - Compiler

## Awards and Scholarships

- 2020.12 Outstanding Freshman Scholarship (Grade 2)
- 2022.1 Chen Linyi Scholarship
- 2023.1 Jianghuai & NIO Automobile Scholarship

## Projects

May 2022 RISC-V Sub, Self-Designed CPU Core

- A 32-bit RISC-V ISA CPU core with 5-stage pipeline and 2-bit dynamic branch prediction, implemented in Verilog.
- Supports a subset of RV32I instructions, including arithmetic, logical, memory, and control instructions.
- O Successfully demonstrated the design by running on FPGA.
- O Independently developed and integrated the core with a Processor Debug Unit (PDU).

2022 CminusF Compiler, A compiler for C-Minus-F (subset of C)

- $\odot$  Utilized a simplified version of LLVM IR called LightIR.
- O Implemented the front-end parser, the IR generation and some optimizations.
- Implemented a GVN analysis to eliminate redundant code and propagate constant values.
- O Generated machine code via LLVM's backend.

#### Skills

Programming Languages

Proficient C/C++, Python, Verilog

Skilled Markdown, LATEX

Familiar Lua, Shell Script

Tools and Operating Systems

Experienced Git, Vim, Docker

Familiar Linux, Windows

Languages

Native Mandarin Chinese

Fluent English