Adress-Logic Switchless Multi-Speeder 32KB RAM / 8x64KB ROM v2.2c by Frank Eggen

				000	001	010	011	100	101	110	111
Adr	A18	A17	A16	BANK 0	BANK 1	BANK 2	BANK 3	BANK 4	BANK 5	BANK 6	BANK 7
	0	0	0	CBMDOS 2.6	DolphinDos 2.0	SpeedDos+ 40T.	JiffyDos 5.0	S-JiffyDos 1	RapiDos Classic	CBMDOS 2.6	SpeedDos Expert
0000	0	0	0	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ
1000	0	0	0	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ
2000	0	0	1	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ
3000	0	0	1	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ
4000	0	1	0	RAM	RAM	RAM	RAM	RAM	RAM	ROM	ROM
5000	0	1	0	RAM	RAM	RAM	RAM	RAM	RAM	ROM	ROM
6000	0	1	1	RAM	RAM	RAM	RAM	RAM	RAM	ROM	ROM
7000	0	1	1	RAM	RAM	RAM	RAM	RAM	RAM	ROM	ROM
8000	1	0	0	RAM	RAM	RAM	RAM	ROM	ROM	ROM	ROM
9000	1	0	0	RAM	RAM	RAM	RAM	ROM	ROM	ROM	ROM
A000	1	0	1	ROM	ROM	ROM	ROM	ROM	ROM	RAM	RAM
B000	1	0	1	ROM	ROM	ROM	ROM	ROM	ROM	RAM	RAM
C000	1	1	0	ROM	ROM	ROM	ROM	ROM	ROM	ROM	ROM
D000	1	1	0	ROM	ROM	ROM	ROM	ROM	ROM	ROM	ROM
E000	1	1	1	ROM	ROM	ROM	ROM	ROM	ROM	ROM	ROM
F000	1	1	1	ROM	ROM	ROM	ROM	ROM	ROM	ROM	ROM