



J1:

- A14 from CPU (U3) pin 24
- R/W from any: VIA (U6/U8) pin 22 or CPU (U3) pin 34

- ROM_A14 - if closed, then R14 can be omitted and \$A000-\$FFFF is ROM (space for track cache buffer ROM code)
- ROM_A14 - if open, then install R14 and a switch between jumper pin 2 (chip 27) and GND chooses ROM (1 of 4)
- ROM_A15 - close for 27E257 or only upper half 32K of 27C512
- ROM_A15 - if open, then install R15 and a switch between jumper pin 2 (chip 1) and GND chooses ROM
- RAM_A13 and RAM_A14 - close to use only 8K of RAM
- RAM_A13 and RAM_A14 - open and connect jumper pins 2 (chip 1 and 26) to VIA#1 port A (?) to control which 8K bank is visible in \$8000-\$9FFF

1541-II RAM and ROM expansion
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Sheet: /
File: 1541-RAMBoard2-FirstBank.kicad_sch

Title: RAMBoard][: First Bank

Size: A4 Date: 2024-03-21
KiCad E.D.A. kicad (6.0.5)

Rev: 1
Id: 1/1