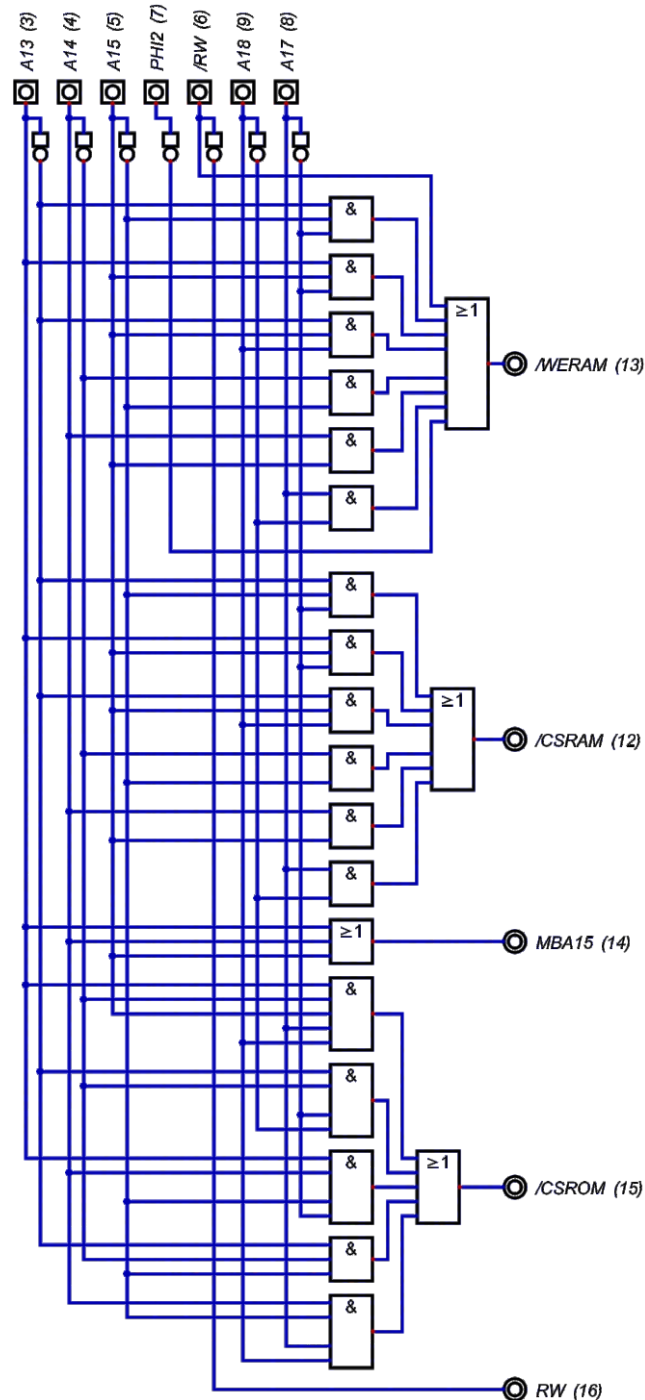


Adressendecoder V2.2 (1541 Multi-Speeder)



Bank-Switching

Adressdecoder GAL/ATF16V8 V2.2 vom 11.08.2024

A16										
A17										
A18	Bank	DOS/KERNAL	EPROM		ROM Bereich *)		RAM			
000	Bank 0	CBMDOS 2.6	\$00000 - \$0FFFF	ROM \$2000 - \$FFFF	RAM \$6000 - \$9FFF					
001	Bank 1	DolphinDos 2.0	\$10000 - \$1FFFF	ROM \$2000 - \$FFFF	RAM \$6000 - \$9FFF					
010	Bank 2	SpeedDos+40T	\$20000 - \$2FFFF	ROM \$2000 - \$FFFF	RAM \$6000 - \$9FFF					
011	Bank 3	JiffyDos 5.0	\$30000 - \$3FFFF	ROM \$2000 - \$FFFF	RAM \$6000 - \$9FFF					
100	Bank 4	S-JiffyDos 1	\$40000 - \$4FFFF	ROM \$2000 - \$FFFF	RAM \$6000 - \$7FFF					
101	Bank 5		\$50000 - \$5FFFF	ROM \$2000 - \$FFFF	RAM \$6000 - \$7FFF					
110	Bank 6		\$60000 - \$6FFFF	ROM \$2000 - \$FFFF	RAM \$A000 - \$BFFF & \$4000 - \$7FFF					
111	Bank 7	SpeedDos2.7Expert	\$70000 - \$7FFFF	ROM \$2000 - \$FFFF	RAM \$A000 - \$BFFF & \$4000 - \$7FFF					

Logic

```
s0 <= NOT A13;  
s3 <= NOT A14;  
s1 <= NOT A15;  
s4 <= NOT A18;  
s2 <= NOT A17;  
MBA15 <= (A13 OR A14 OR A15);  
RW <= NOT notRW;  
  
notWERAM <= (notRW OR (s0 AND s1 AND s2) OR (A13 AND A15 AND s2) OR (s0 AND A15 AND A18)  
OR (s3 AND s1) OR (A14 AND A15) OR (A17 AND s4) OR NOT PHI2);  
  
notCSRAM <= ((s0 AND s1 AND s2) OR (A13 AND A15 AND s2) OR (s0 AND A15 AND A18) OR (s3 AND  
s1) OR (A14 AND A15) OR (A17 AND s4));  
  
notCSROM <= ((A13 AND s3 AND A15 AND A17 AND A18) OR (s0 AND s3 AND s2 AND s4) OR (A13 AND  
A14 AND s1 AND s2) OR (s0 AND s3 AND s1) OR (A14 AND s1 AND A17 AND A18));
```

by Frank Eggen
11.08.2024