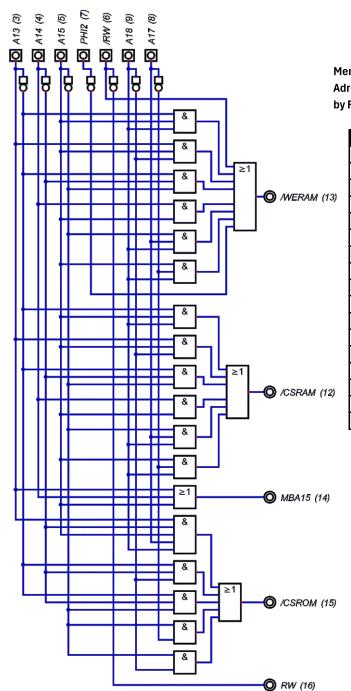
## Adressendecoder V2.2b (1541 8x Multi-Speeder)



Memory Map 8x Multispeeder Speeder 32KB RAM Adressdekoder 2.2b by Frank Eggen

|    |      |     |     |     | 000        | 001            | 010            | 011          | 100          | 101             | 110        | 111             |
|----|------|-----|-----|-----|------------|----------------|----------------|--------------|--------------|-----------------|------------|-----------------|
| Ad | lr / | A18 | A17 | A16 | BANK 0     | BANK 1         | BANK 2         | BANK 3       | BANK 4       | BANK 5          | BANK 6     | BANK 7          |
|    |      | 0   | 0   | 0   | CBMDOS 2.6 | DolphinDos 2.0 | SpeedDos+ 40T. | JiffyDos 5.0 | S-JiffyDos 1 | RapiDos Classic | CBMDOS 2.6 | SpeedDos Expert |
| 00 | 00   | 0   | 0   | 0   | Χ          | Χ              | Χ              | Χ            | Χ            | Χ               | Χ          | Χ               |
| 10 | 00   | 0   | 0   | 0   | Χ          | Χ              | Χ              | Χ            | Χ            | Χ               | Χ          | Χ               |
| 20 | 00   | 0   | 0   | 1   | RAM        | RAM            | RAM            | RAM          | RAM          | RAM             | ROM        | ROM             |
| 30 | 00   | 0   | 0   | 1   | RAM        | RAM            | RAM            | RAM          | RAM          | RAM             | ROM        | ROM             |
| 40 | 00   | 0   | 1   | 0   | RAM        | RAM            | RAM            | RAM          | RAM          | RAM             | ROM        | ROM             |
| 50 | 00   | 0   | 1   | 0   | RAM        | RAM            | RAM            | RAM          | RAM          | RAM             | ROM        | ROM             |
| 60 | 00   | 0   | 1   | 1   | RAM        | RAM            | RAM            | RAM          | RAM          | RAM             | ROM        | ROM             |
| 70 | 00   | 0   | 1   | 1   | RAM        | RAM            | RAM            | RAM          | RAM          | RAM             | ROM        | ROM             |
| 80 | 00   | 1   | 0   | 0   | RAM        | RAM            | RAM            | RAM          | ROM          | ROM             | ROM        | ROM             |
| 90 | 00   | 1   | 0   | 0   | RAM        | RAM            | RAM            | RAM          | ROM          | ROM             | ROM        | ROM             |
| Α0 | 00   | 1   | 0   | 1   | ROM        | ROM            | ROM            | ROM          | ROM          | ROM             | RAM        | RAM             |
| В0 | 00   | 1   | 0   | 1   | ROM        | ROM            | ROM            | ROM          | ROM          | ROM             | RAM        | RAM             |
| C0 | 000  | 1   | 1   | 0   | ROM        | ROM            | ROM            | ROM          | ROM          | ROM             | ROM        | ROM             |
| D0 | 000  | 1   | 1   | 0   | ROM        | ROM            | ROM            | ROM          | ROM          | ROM             | ROM        | ROM             |
| E0 | 00   | 1   | 1   | 1   | ROM        | ROM            | ROM            | ROM          | ROM          | ROM             | ROM        | ROM             |
| F0 | 00   | 1   | 1   | 1   | ROM        | ROM            | ROM            | ROM          | ROM          | ROM             | ROM        | ROM             |

/WERAM = /RW  $\vee$  ( $\overline{A13}$   $\wedge$  A15  $\wedge$  A18)  $\vee$  (A13  $\wedge$  A15  $\wedge$   $\overline{A18}$ )  $\vee$  ( $\overline{A13}$   $\wedge$   $\overline{A14}$   $\wedge$   $\overline{A15}$ )  $\vee$  (A14  $\wedge$  A15)  $\vee$  ( $\overline{A15}$   $\wedge$  A17  $\wedge$  A18)  $\vee$  (A15  $\wedge$   $\overline{A17}$   $\wedge$  A18)  $\vee$  (A18)  $\vee$  (A18)  $\vee$  (A18)  $\vee$  (A18)  $\vee$  (A19  $\wedge$  A19)  $\vee$  (A19

/CSROM = (A13  $\wedge$  A14  $\wedge$  A15  $\wedge$  A17  $\wedge$  A18)  $\vee$  (A13  $\wedge$  A14  $\wedge$  A18)  $\vee$  (A13  $\wedge$  A14  $\wedge$  A15)  $\vee$  (A15  $\wedge$  A17)  $\vee$  (A15  $\wedge$  A18) RW = /RW